

SE-PAC2070 CONTROLLER RING CONFIGURATION DETAIL

(program controller as shown below)

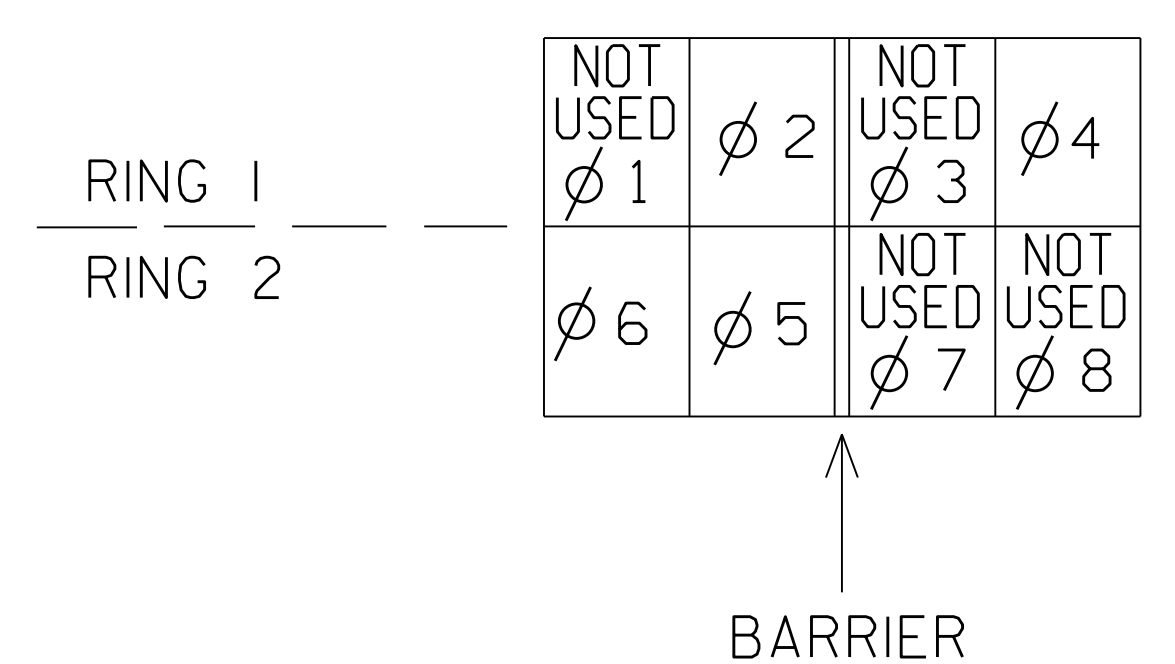
NOTE:
BEFORE PROGRAMMING CONTROLLER, BE SURE TO LOAD DEFAULT PARAMETERS.

SELECT ④ FROM MAIN MENU

<p>SE-PAC UNIT DATA PRESS # DESIRED</p> <p>1- STARTUP & MISC 6- ALT SEQUENCES</p> <p>2- REMOTE FLASH 7- PORT 1 DATA</p> <p>3- OVERLAP STANDARD 8- I/O MISC</p> <p>4- OVERLAP SPECIAL 9- SIG DRV OUT</p> <p>5- RING STRUCTURE</p> <p style="text-align: right;">F- PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 3 RING: 1 NXT PHS: 4 </p> <p>CONCUR PHS: 001000110 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 001000000 0000000000 00000</p> <p>PED CHN(S): 000000000 0000000010 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 6 RING: 2 NXT PHS: 5 </p> <p>CONCUR PHS: 110001000 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 000001000 0000000000 00000</p> <p>PED CHN(S): 000000000 0100000000 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>
<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 1 RING: 1 NXT PHS: 2 </p> <p>CONCUR PHS: 100011000 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 100000000 0000000000 00000</p> <p>PED CHN(S): 000000000 0000000100 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 4 RING: 1 NXT PHS: 1 </p> <p>CONCUR PHS: 000100110 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 000100000 0000000000 00000</p> <p>PED CHN(S): 000000000 1000000000 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 7 RING: 2 NXT PHS: 8 </p> <p>CONCUR PHS: 001100100 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 000000100 0000000000 00000</p> <p>PED CHN(S): 000000000 0000000000 10000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>
<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 2 RING: 1 NXT PHS: 3 </p> <p>CONCUR PHS: 010011000 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 010000000 0000000000 00000</p> <p>PED CHN(S): 000000001 0000000000 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 5 RING: 2 NXT PHS: 7 </p> <p>CONCUR PHS: 110010000 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 000010000 0000000000 00000</p> <p>PED CHN(S): 000000000 0000000001 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>	<p>SE-PAC RING STRUCTURE (0-NO / 1-YES)</p> <p>PHASE: 8 RING: 2 NXT PHS: 6 </p> <p>CONCUR PHS: 001100010 0000000</p> <p>PHS/CHN: 123456789 0123456789 01234</p> <p>VEH CHN(S): 000000010 0000000000 00000</p> <p>PED CHN(S): 000000000 0010000000 00000</p> <p>A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU</p>

end of programming

DUAL-QUAD WITH
PHASES 5 & 6 ROTATED



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1642T1
DESIGNED: December 2015
SEALED: 2/1/2016
REVISED: N/A

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

Electrical Detail - Temp Design 1 (TMP Area II Phase I & III) - Sheet 2 of 3

<p>ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="text-align: center;">Prepared in the Offices of:</p> <p style="text-align: center;">Signal Management Systems, Inc. 750 Greenfield Parkway, Garner, NC 27529</p>	<p style="text-align: center;">W. Peace Street at US 70 WB-401/NC 50 NB (Capital Blvd.) Ramps</p> <p>Division 5 Wake County Raleigh</p> <p>PLAN DATE: January 2016 REVIEWED BY: T. Joyce</p> <p>PREPARED BY: S. Armstrong REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p style="text-align: center;">SEAL</p> <p style="text-align: center;">DocuSigned by: Keith M. Mims 2/2/2016</p> <p style="text-align: center;">SIG. INVENTORY NO. 05-1642T1</p>
REVISIONS	INIT.	DATE						

05-1642T1.dgn 05/16/15 S:\Projects\Signal Management Systems\Signal Management Systems\05-1642T1.dgn