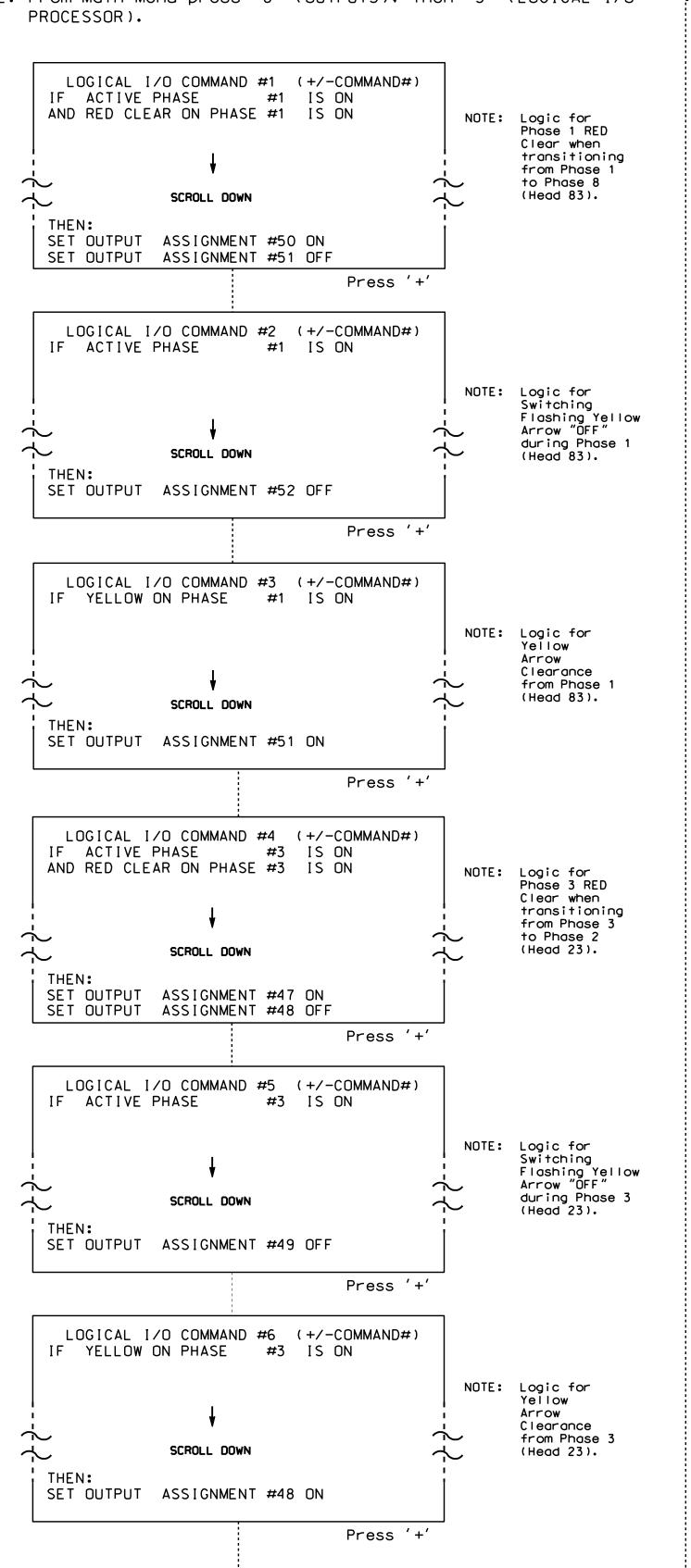
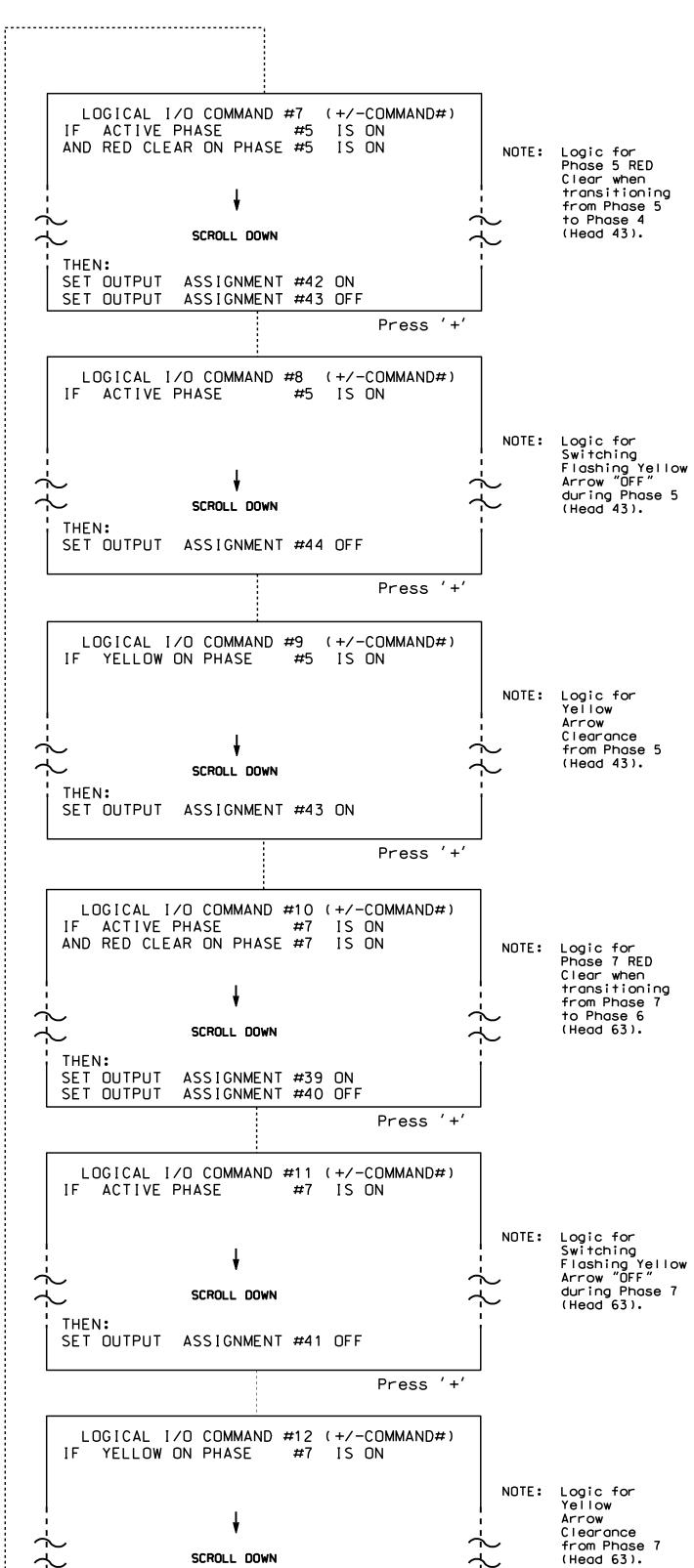
(program controller as shown below)

1. From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 and 12.

2. From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



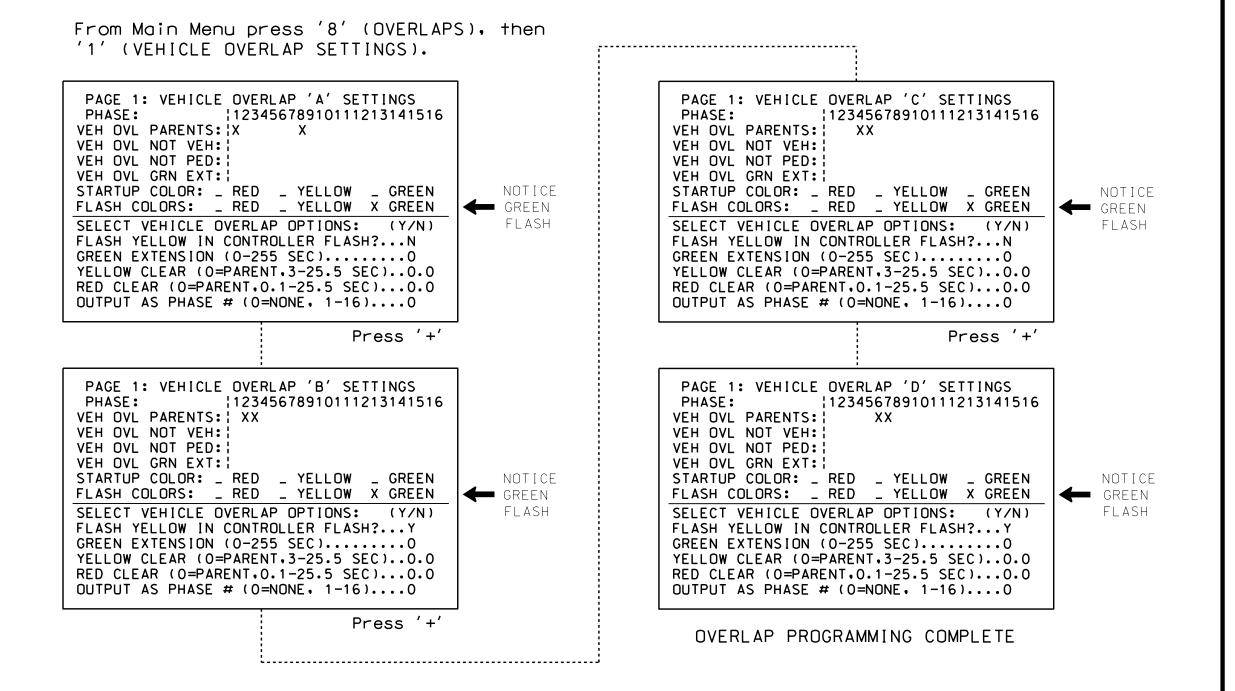


SET OUTPUT ASSIGNMENT #40 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

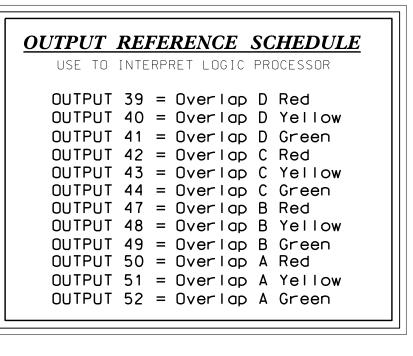
1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-3.

2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-2.

3. REMOVE FLASHER UNIT 2.

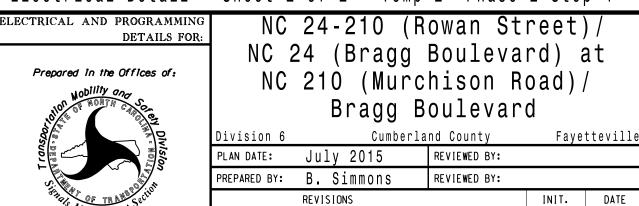
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

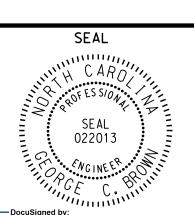
750 N.Greenfield Pkwy, Garner, NC 27529



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1336T2 DESIGNED: June 2015 SEALED: 8/28/15 REVISED: N/A

Electrical Detail - Sheet 2 of 2 - Temp 2 Phase 2 Step 4





SIG. INVENTORY NO. 06-1336T2