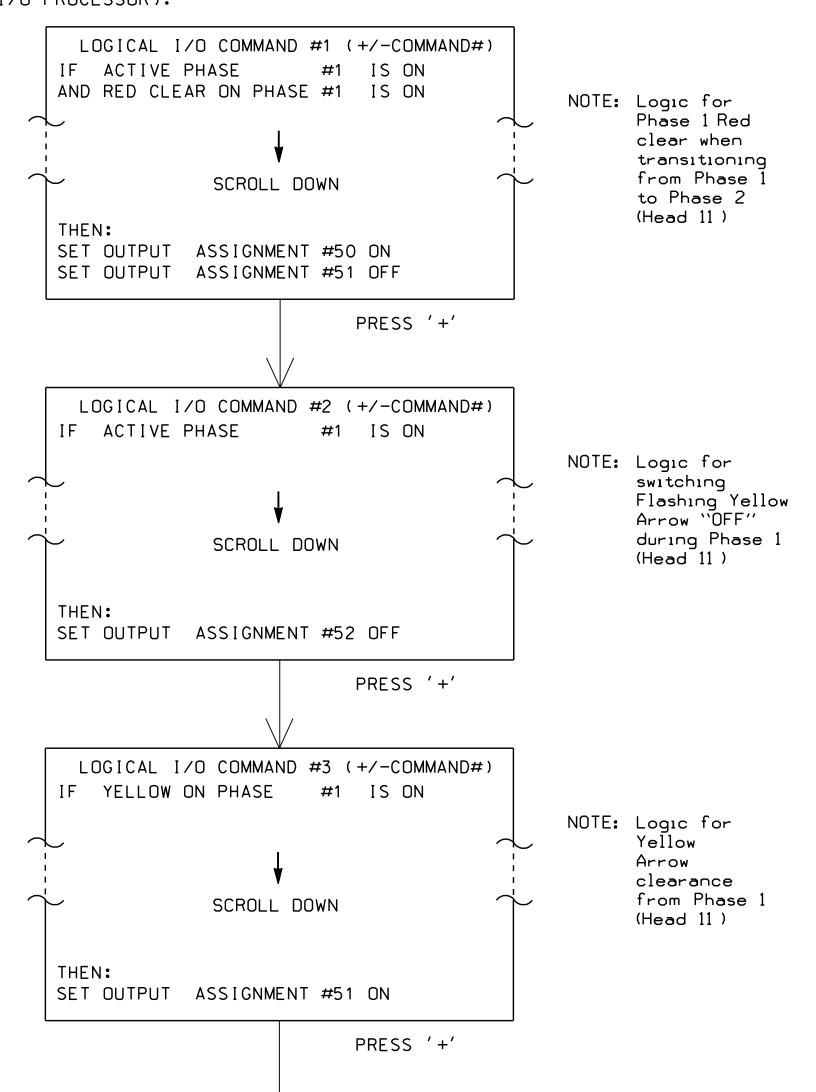
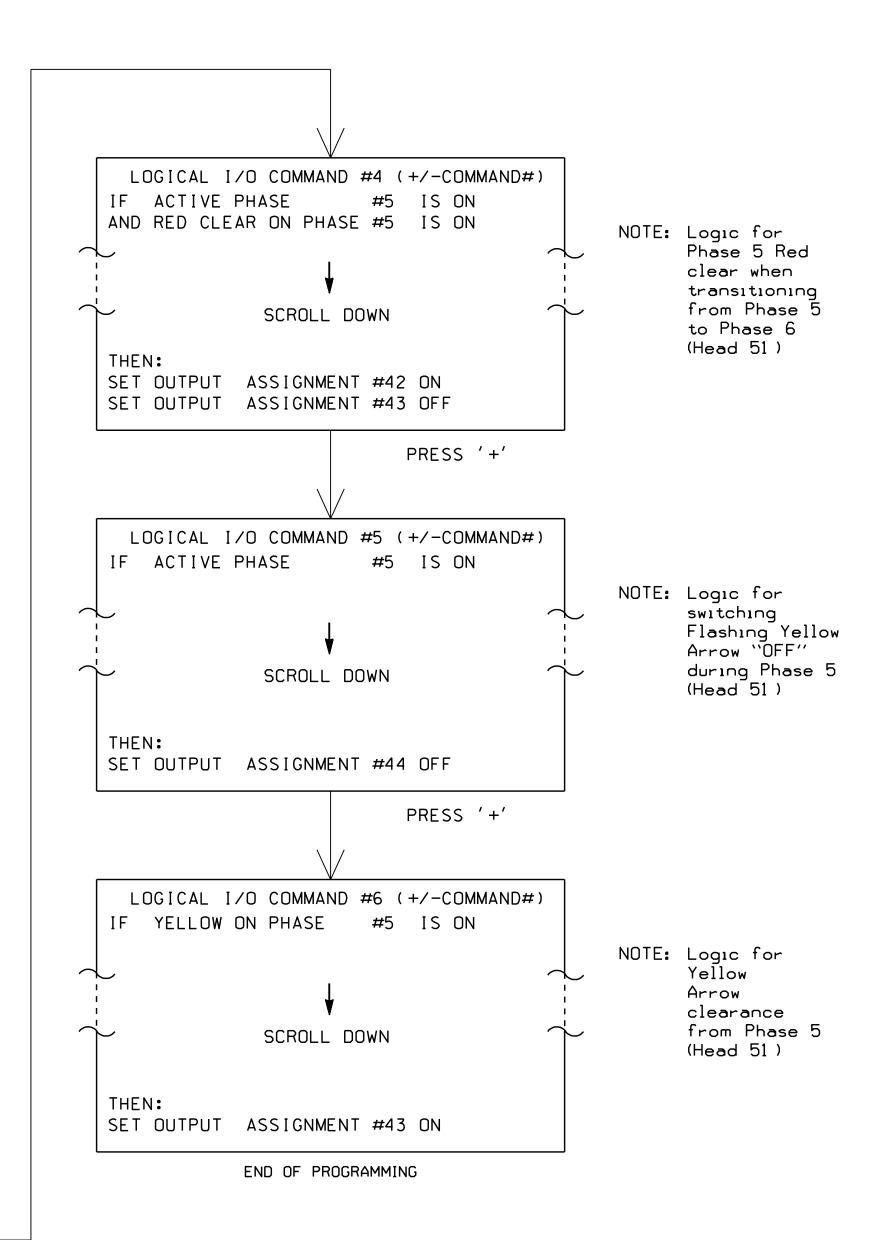
2. From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).





## OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

PROJECT REFERENCE NO. Sig. 12.2 B-4490

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN ◆ NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

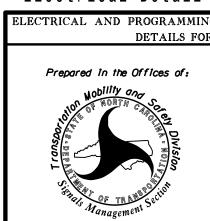
PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516 PHASE: VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED: | VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1210T2 DESIGNED: July 2015 SEALED: 8/27/15 REVISED: N/A

Electrical Detail - Sheet 2 of 2 - Temp 2 Phase 2 Step 4



SR 3828 (Bragg Boulevard) Walter Street

Fayetteville ivision 6 July 2015 REVIEWED BY:

PLAN DATE: PREPARED BY: B. SIMMONS REVIEWED BY: REVISIONS INIT. DATE

George C. Brown 8/31/2015 SIG. INVENTORY NO. 06-1210T2