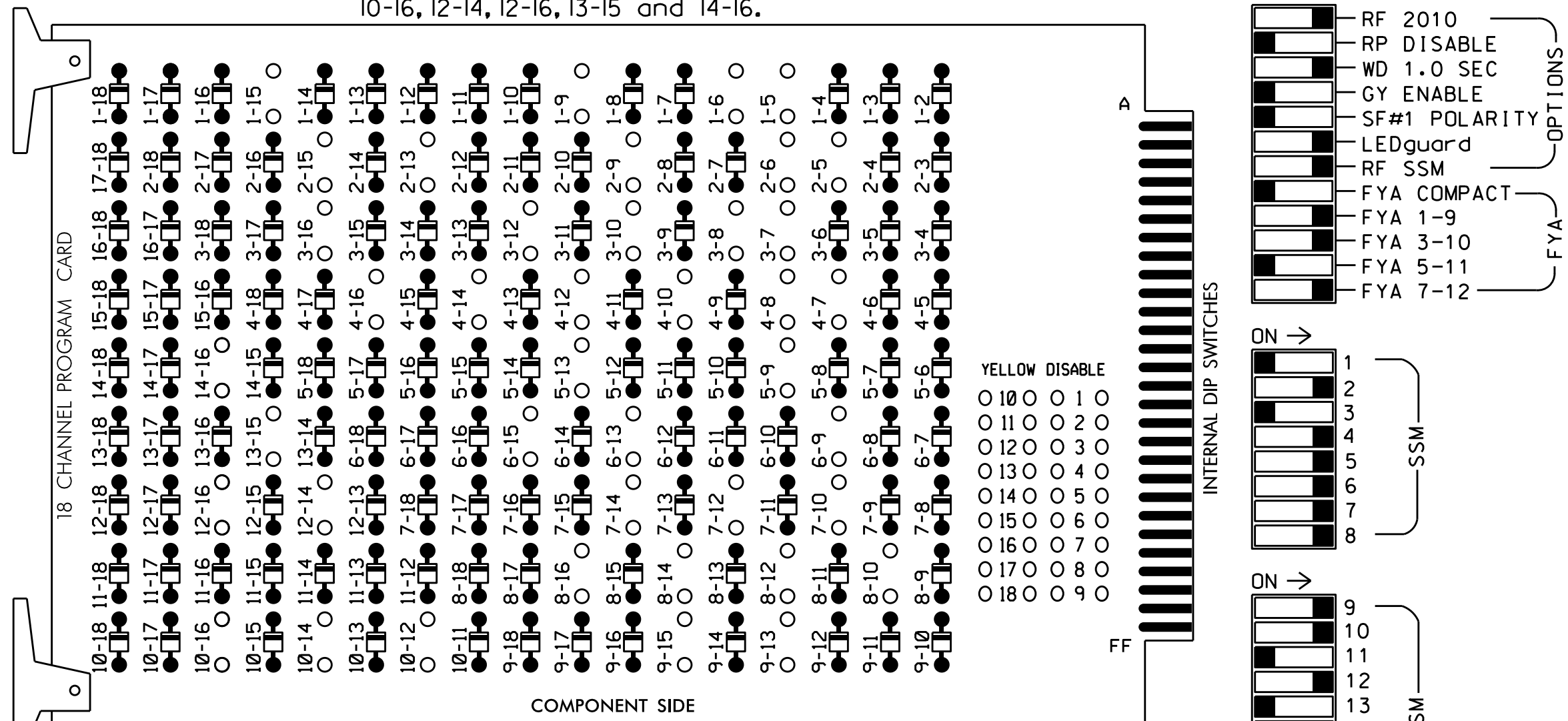


EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

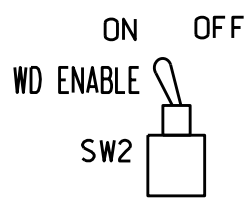
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-15, 2-5, 2-6, 2-9, 2-13, 2-15, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-13, 6-9, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-13, 9-15, 10-12, 10-14, 10-16, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Kernersville CLS #1.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 (12-STD; 6-AUX)
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10
 S11,S12,AUX S1,AUX S2,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2*
 OVERLAP "B".....3+4*
 OVERLAP "C".....NOT USED
 OVERLAP "D".....7+8*
 * Alternate Phasing Overlap Programming Detail shown on sheet 6.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	31	41,42	P41, P42	51,52	61,62	P61, P62	71	81,82	P81, P82	11	31	NU	NU	71	NU
RED		128		101			134		*	107								
YELLOW	*	129		102			135			108								
GREEN		130		103			136			109								
RED ARROW							131						A121	A124			A101	
YELLOW ARROW							132			123			A122	A125			A102	
FLASHING YELLOW ARROW													A123	A126			A103	
GREEN ARROW	127			118			133			124	124							
Hand			113			104			119			110						
Person			115			106			121			112						

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
1A	2A,2B	3A	4A	5A	6A,6B	7A	8A	SYS. DET. S1	SYS. DET. S2	2 PED	6 PED	FS		
NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
5A	6A,6B	7A	8A	9A	10A	11A	12A	13A	14A	15A	16A	17A	18A	19A
NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

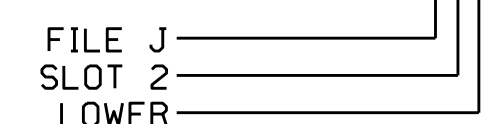
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	10	26	6	Y	Y			
	-	I1U	56	18	51**	1	Y	Y			
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
	TB4-5,6	I5U	58	20	3	3	Y	Y			15
3A ²	-	J8U	50	12	28	8	Y	Y			3
	-	I5U	58	20	53**	3	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			
6A,6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A ³	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- * System detector only. Remove the vehicle phase assigned to this detector in the default programming.
- ** See input Page Assignment programming detail on sheets 3, 4 and 5. detector in the default programming.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

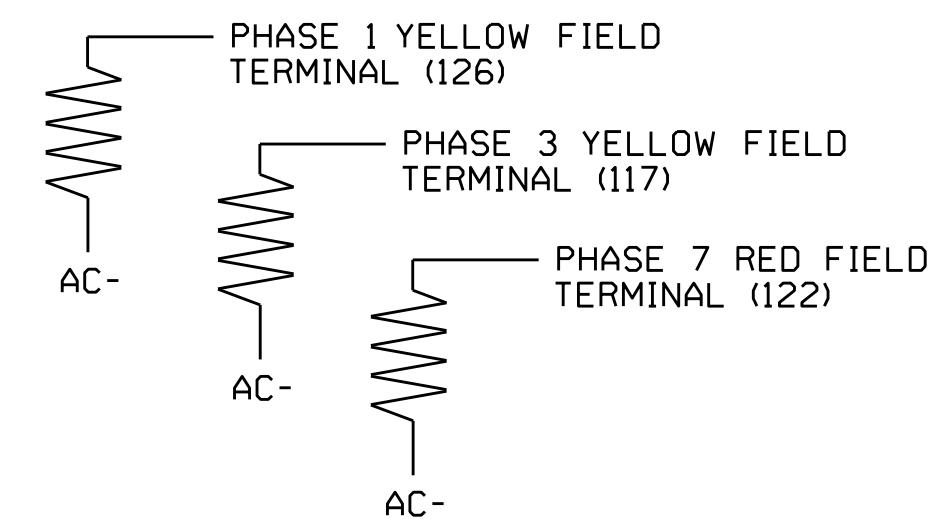
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

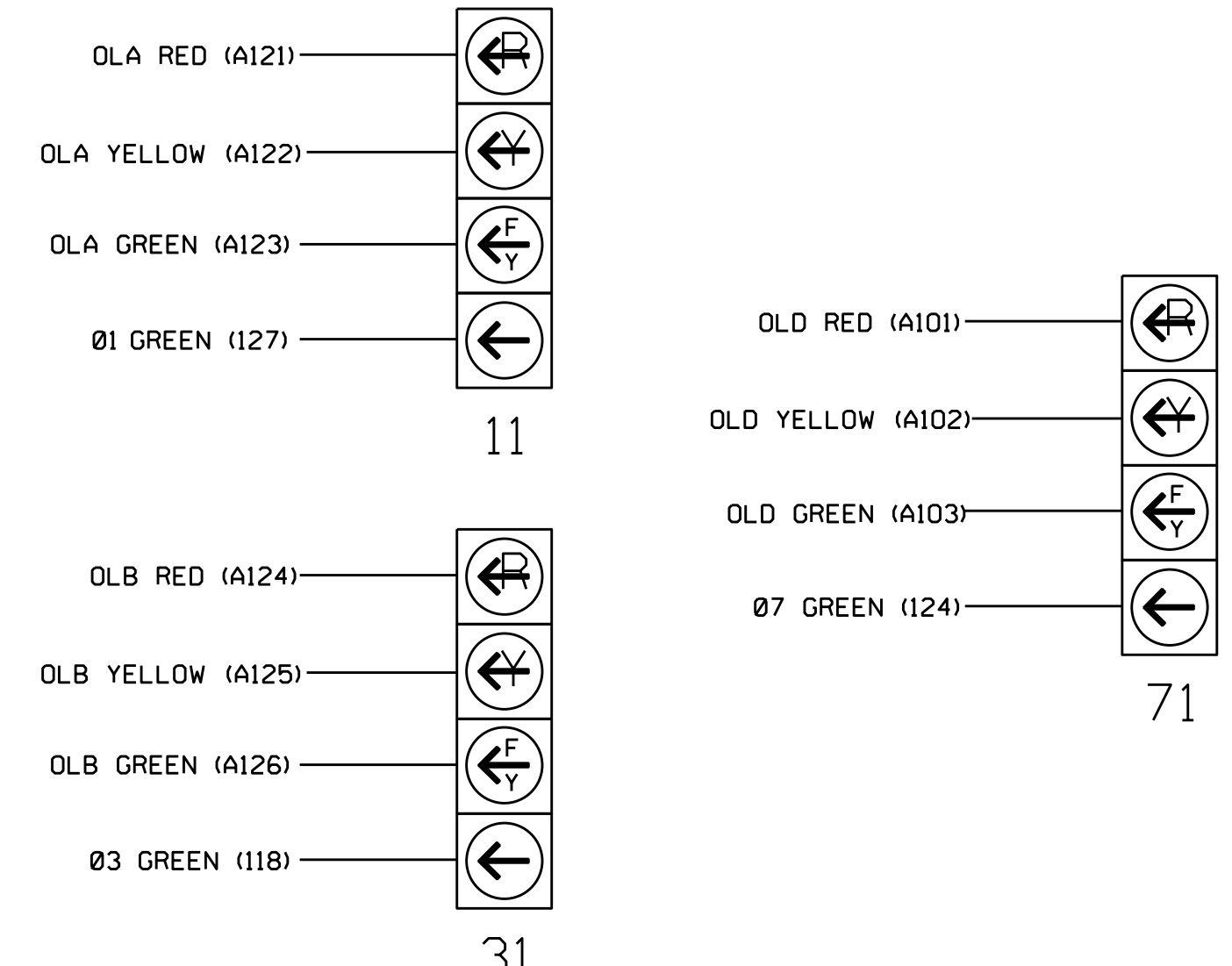
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-1104
 DESIGNED: April 2015
 SEALED: 5/7/15
 REVISED: N/A

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE:
 The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

Electrical Detail - Sheet 1 of 6

Electrical and Programming Details for: SR 4315 (S. Main Street) at SR 2648 (Old Winston Road) / Shopping Center Driveway

Division 9 Forsyth County Kernersville

PLAN DATE: April 2015 REVIEWED BY: T. Joyce

PREPARED BY: B. Simmons REVIEWED BY:

REVISIONS: INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal of George C. Brown, Professional Engineer, License No. 022013

Sig. Inventory No. 09-1104

18-MAY-2015 13:06
 S:\MITSU\15\SIGNAL\working\09-1104\09-1104_smc_elec_xxxx.dgn
 B:\simmons\working\09-1104\09-1104_smc_elec_xxxx.dgn
 B:\simmons