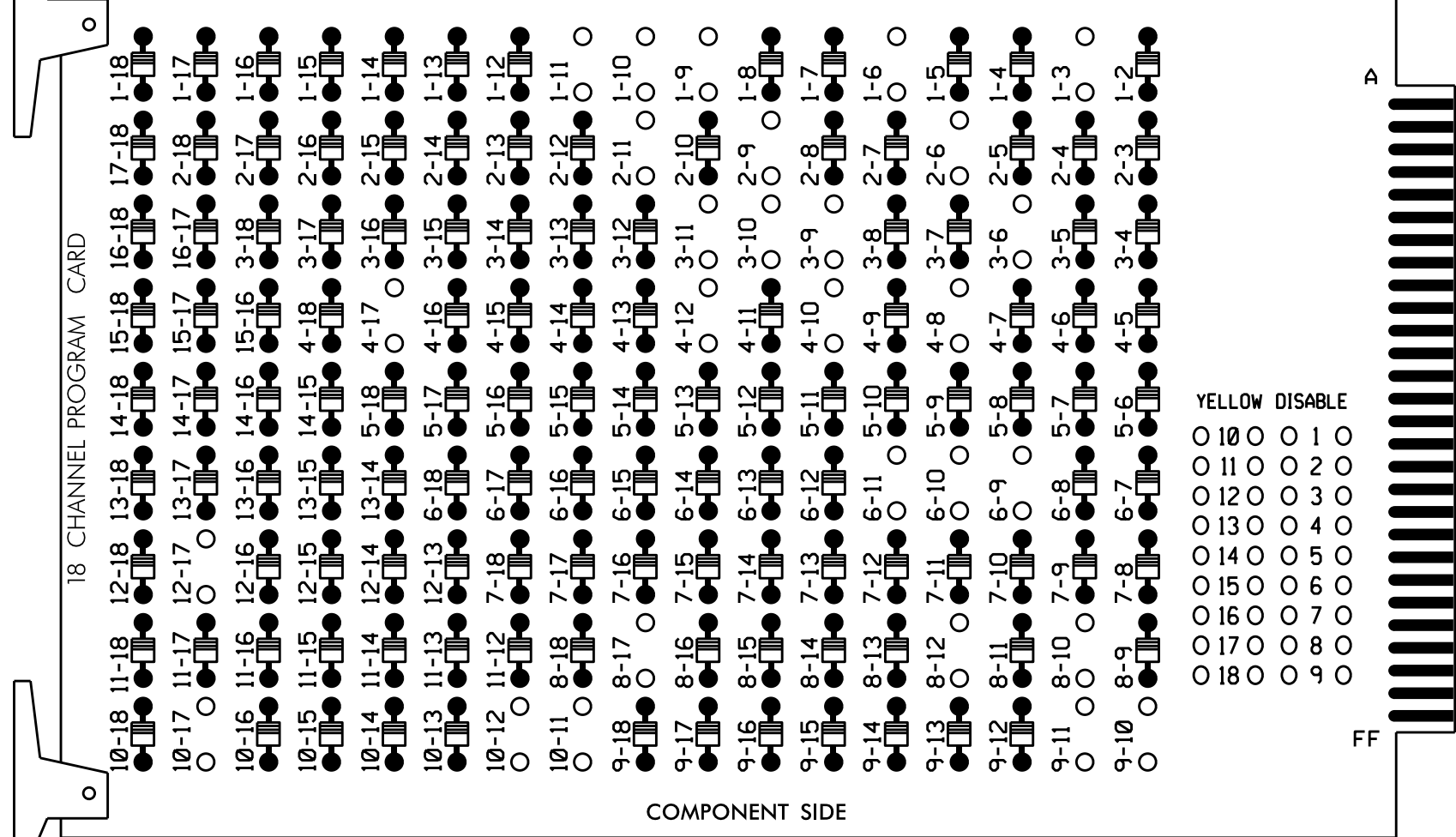


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

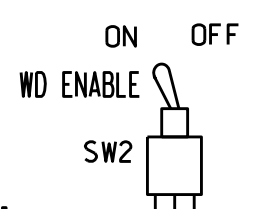
(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-3, 1-6, 1-9, 1-10, 1-11, 2-6, 2-9, 2-11, 3-6, 3-9, 3-10, 3-11, 4-8, 4-10, 4-12, 4-17, 6-9, 6-10, 6-11, 8-10, 8-12, 8-17, 9-10, 9-11, 10-11, 10-12, 10-17 and 12-17.



REMOVE JUMPERS AS SHOWN

- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash and overlaps 1, 2 and 5 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S8,S11,AUX S1,AUX S2,
 AUX S3,AUX S4,AUX S5
 PHASES USED.....1,2,4,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....1+4+8
 OVERLAP "C".....6
 OVERLAP "D".....4
 OVERLAP "E".....8
 OVERLAP "F".....1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OLP	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	OLE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	22,23	NU	12,13	42,43	NU	NU	61,62	NU	NU	82,83	NU	11	12,13	81	21	41	NU
RED		128			101			134			107			A124				
YELLOW	*	129		*	102			135			108							
GREEN		130			103			136			109							
RED ARROW														A121	A111	A114	A101	
YELLOW ARROW														A122	A125	A112	A115	A102
FLASHING YELLOW ARROW														A123	A126	A113	A116	A103
GREEN ARROW	127				118													

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

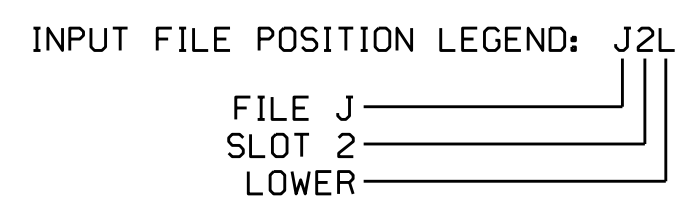
FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 2	∅ 5	∅ 4	∅ 4	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5
I	1A	1B	2A	2C	∅ 5	4A	4C	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	FS
L	NOT USED	∅ 1	∅ 2	NOT USED	∅ 5	4A	NOT USED	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	∅ 5	DC ISOLATOR
U	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8
J	6A	6A	6A	6A	6A	8A	8A	8A	8A	8A	8A	8A	8A	8A
L	6B	6B	6B	6B	6B	8B	8B	8B	8B	8B	8B	8B	8B	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

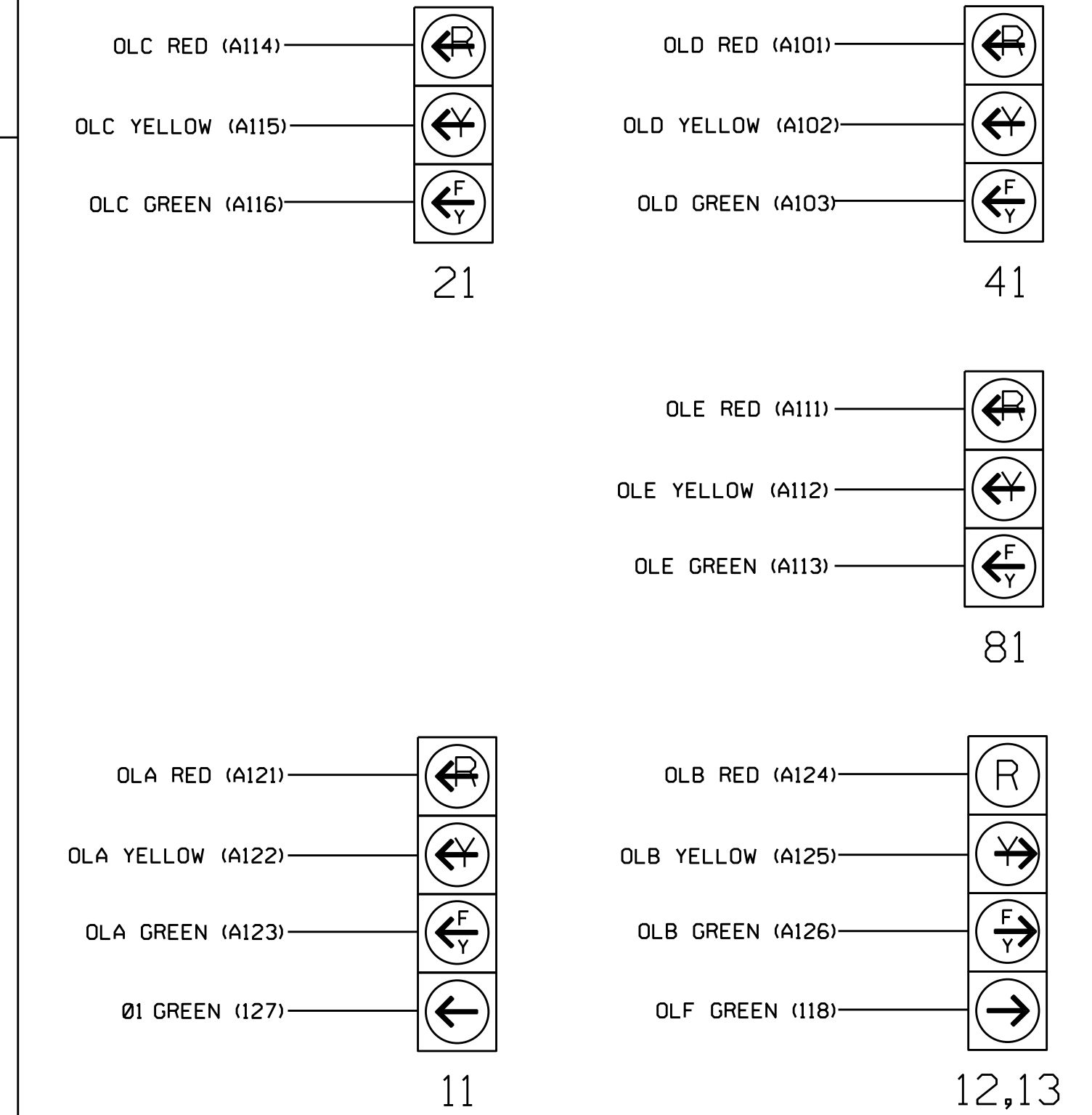
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
		J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
1C	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
2C	TB4-1,2	I4U	47	9	22	2	Y	Y	Y		3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
4C	TB6-1,2	I7U	65	27	34	4	Y	Y			15
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10

¹Add jumper from I1-W to J4-W, on rear of input file.



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

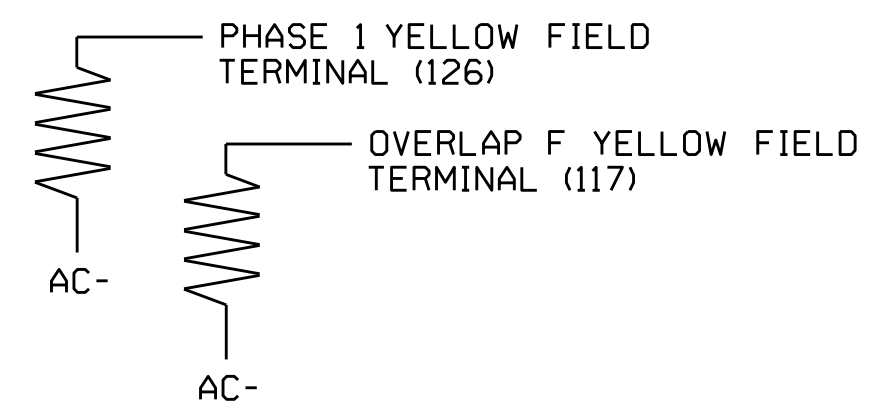


NOTE
 1. The sequence display for signal heads 11,12 and 13 require special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0871
 DESIGNED: October 2015
 SEALED: 1/5/2016
 REVISED: N/A

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY ADMINISTRATION
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Clemmons Road) at SR 3000 (Idols Road) and SR 1117 (Old Sides Mill Road)

Division 9 Forsyth County Clemmons
 PLAN DATE: December 2015 REVIEWED BY: DTJ
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS: INIT. DATE

Seal of Keith M. Mims, Professional Engineer, No. 036880, State of North Carolina.

SIG. INVENTORY NO. 09-0871

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