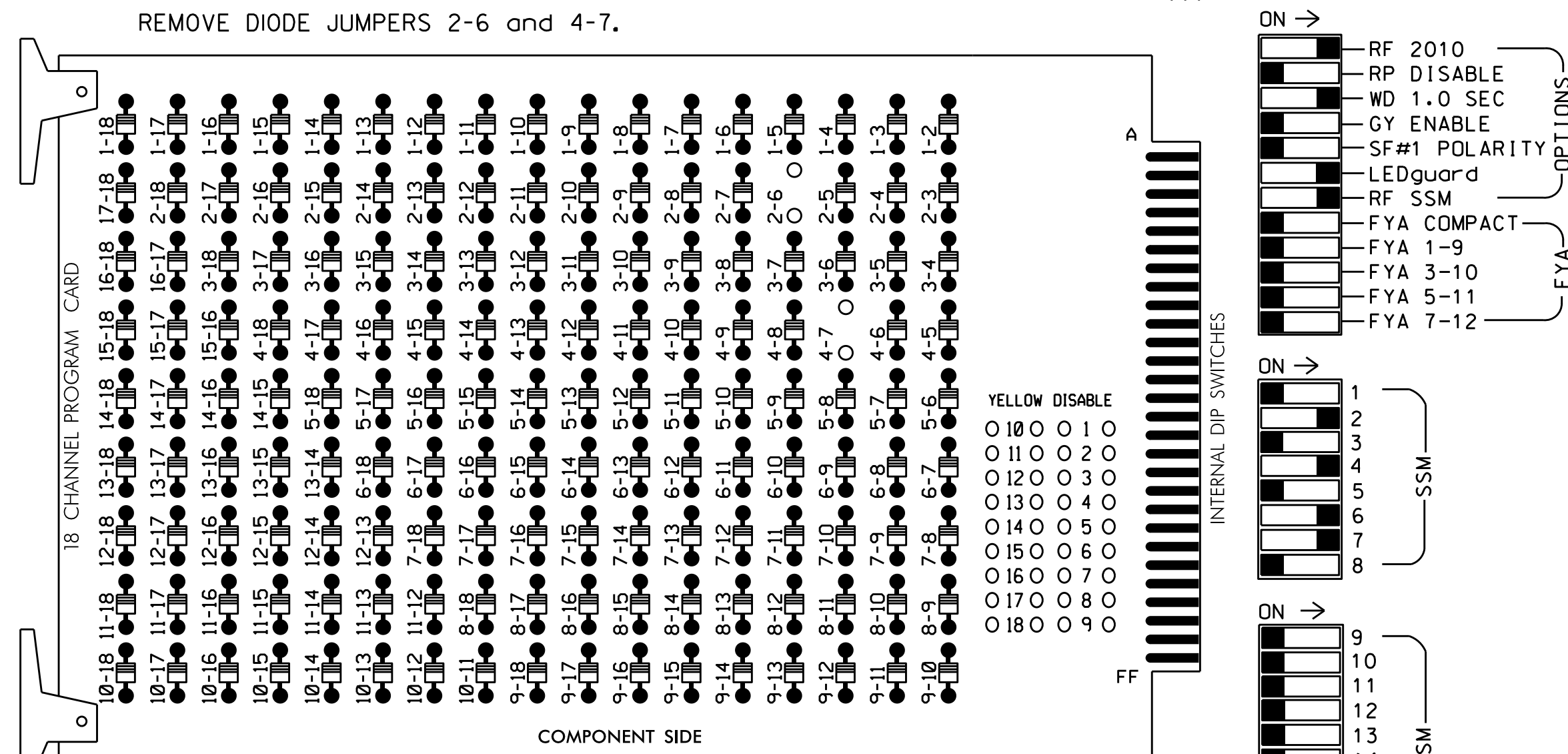


**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 2 and 6 for Dual Entry.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Gap Reduction.
4. Program phases 2, 4 and 6 for Red Rest.
5. Program phase 2 for Startup Red Clear.
6. Program phase 2 as First Phases.

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	OLA	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	NU	61,62	NU	43,44	NU	NU
RED		128			101			134		122		
YELLOW		129			102			135		123		
GREEN		130			103			136		124		
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

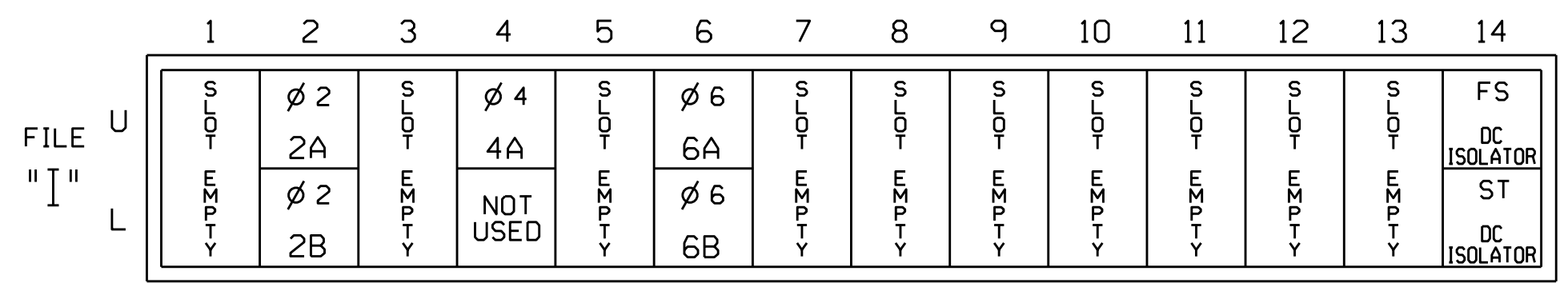
NU = Not Used

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
 CABINET.....336  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....POLE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S2,S5,S8,S10  
 PHASES USED.....2,4,6  
 OVERLAP A.....4

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**OVERLAP PROGRAMMING DETAIL**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
 PHASE: :12345678910111213141516  
 VEH OVL PARENTS: : X  
 VEH OVL NOT VEH: :  
 VEH OVL NOT PED: :  
 VEH OVL GRN EXT: :  
 STARTUP COLOR: - RED - YELLOW - GREEN  
 FLASH COLORS: - RED - YELLOW - GREEN  
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
 FLASH YELLOW IN CONTROLLER FLASH?...N  
 GREEN EXTENSION (0=255 SEC)...25  
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...3.0  
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...1.4  
 OUTPUT AS PHASE # (0=NONE, 1-16)...7

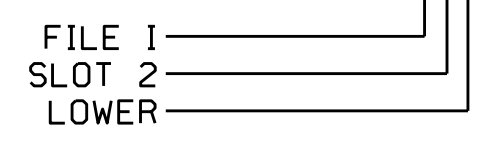
OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-1286T1&T2  
 DESIGNED: November 2015  
 SEALED: 11/23/2015  
 REVISED: N/A

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB21-3,4	I2U	39	1	2	2		Y			
2B	TB23-3,4	I2L	43	5	12	2	Y	Y		2.0	5
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			
6A	TB21-11,12	I6U	40	2	6	6		Y			
6B	TB23-11,12	I6L	44	6	16	6	Y	Y		2.0	5

INPUT FILE POSITION LEGEND: I2L



Electrical Detail- Temp 1 & 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical AND PROGRAMMING DETAILS FOR: **NC 151 (Pisgah Highway) at SR 1103 (Davis Creek Road)**

Prepared In the Offices of: **TRANSPORTATION MOBILITY AND SAFETY CONSULTANTS, INC.**  
 750 N. Greenfield Pkwy, Garner, NC 27529

Division 13 Buncombe County Candier

PLAN DATE: November 2015 REVIEWED BY:  
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: **George C. Brown** 12/1/2015  
 F12001ED08E8434 DATE

SIG. INVENTORY NO. 13-1286T1&T2

23-1004-2015-16-529  
 S:\MITSAS\13-1286T1&T2\SIGNAL\work\hgr\edp\sig\Map\511-1286-sm-le-xxx.dgn  
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 UNIT: Meter  
 PROJECTION: UTM  
 ZONE: 18N