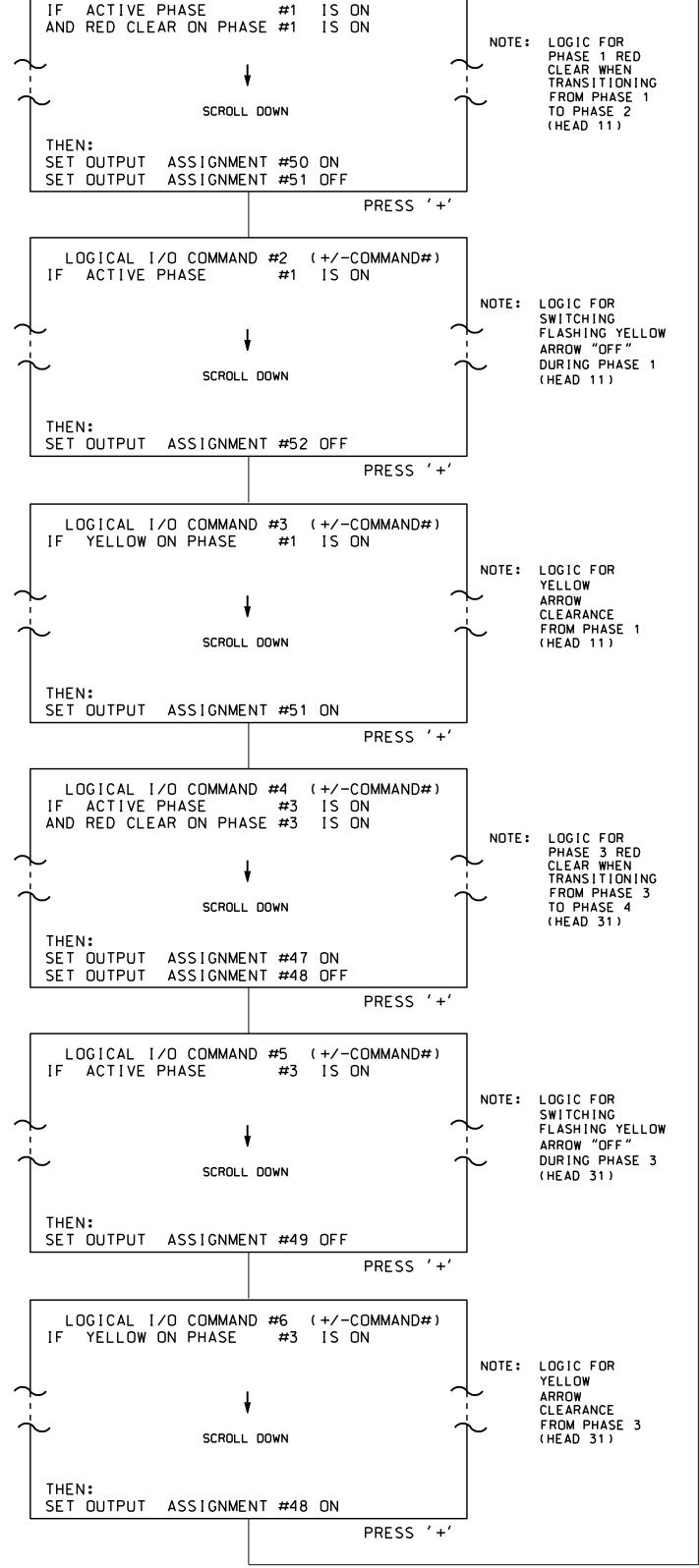
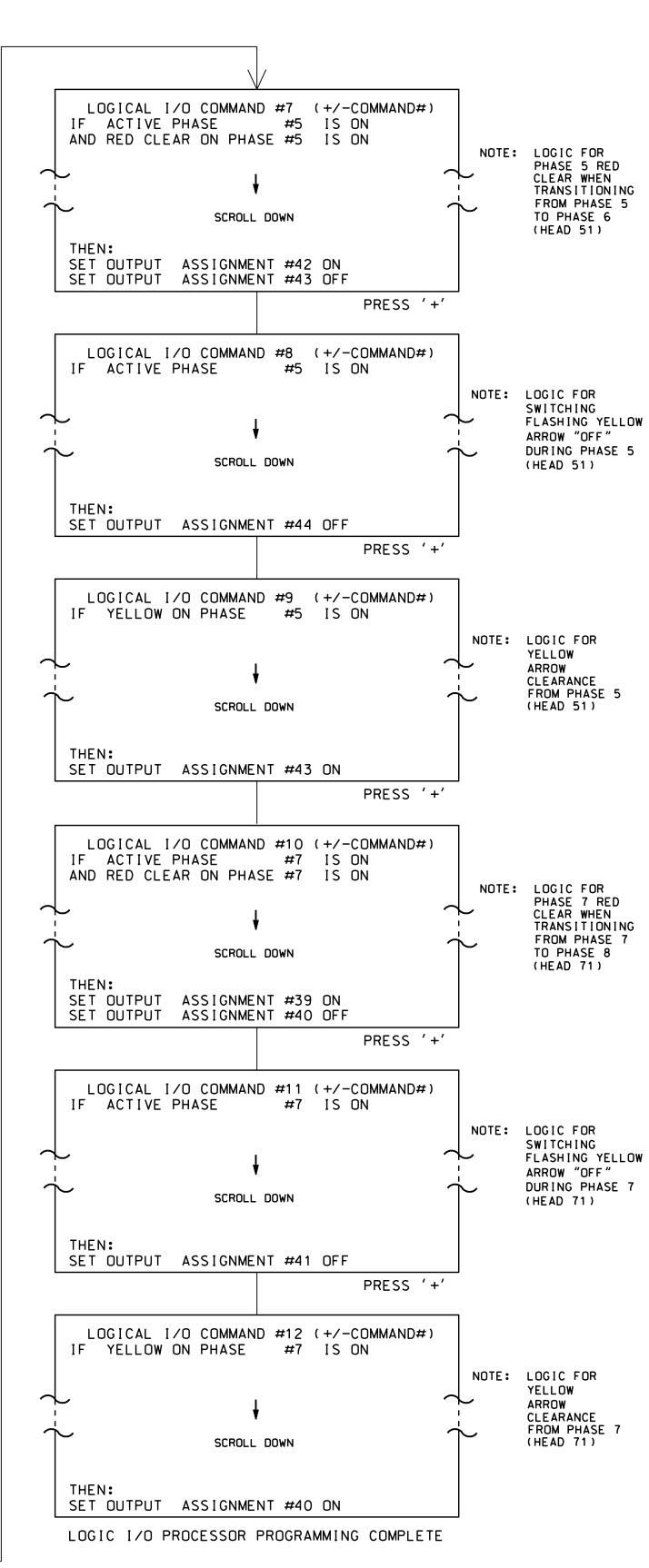
(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1,2,3,4,5 6,7,8,9,10,11 AND 12.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

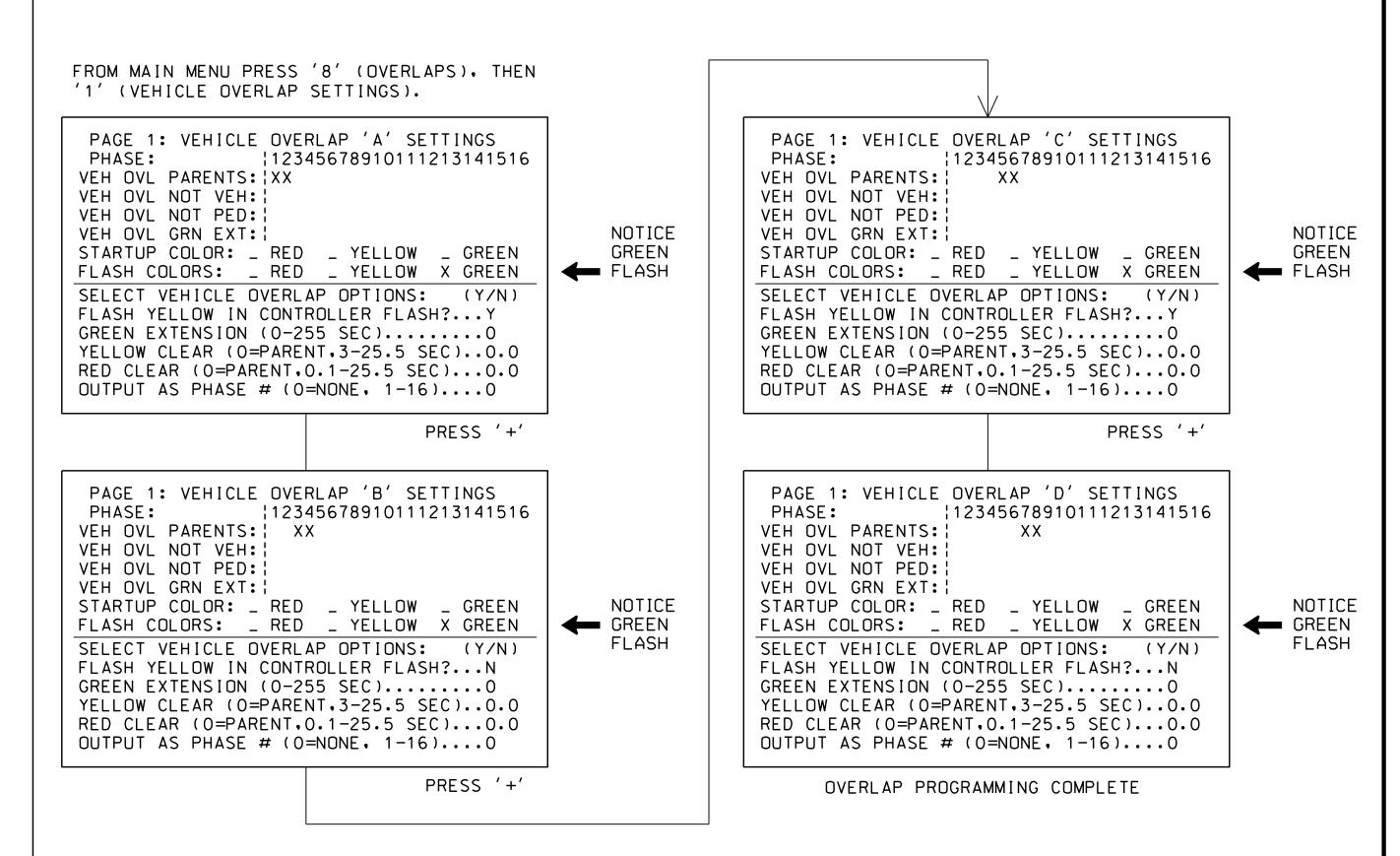
LOGICAL I/O COMMAND #1 (+/-COMMAND#)





OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

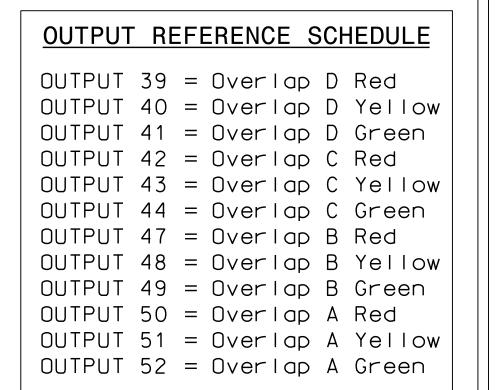


FLASHER CIRCUIT MODIFICATION NOTES

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

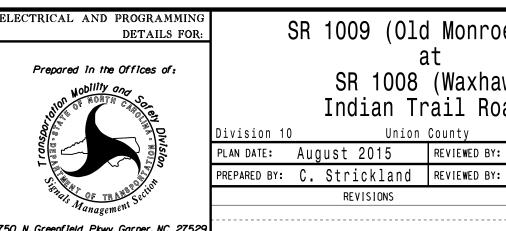
THE CHANGES LISTED BELOW TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-0611 DESIGNED: July 2015 SEALED: 8/27/2015 REVISED: N/A

Electrical Detail - Sheet 2 of 3



SR 1009 (Old Monroe Road)

SR 1008 (Waxhaw -Indian Trail Road) Indian Trail GCB

022013

SEAL

INIT. DATE George C. Brown 8/28/2015 SIG. INVENTORY NO. 10-0611

PROJECT REFERENCE NO.

B-5243

Sig. 3.2

750 N. Greenfield Pkwy, Garner, NC 27529