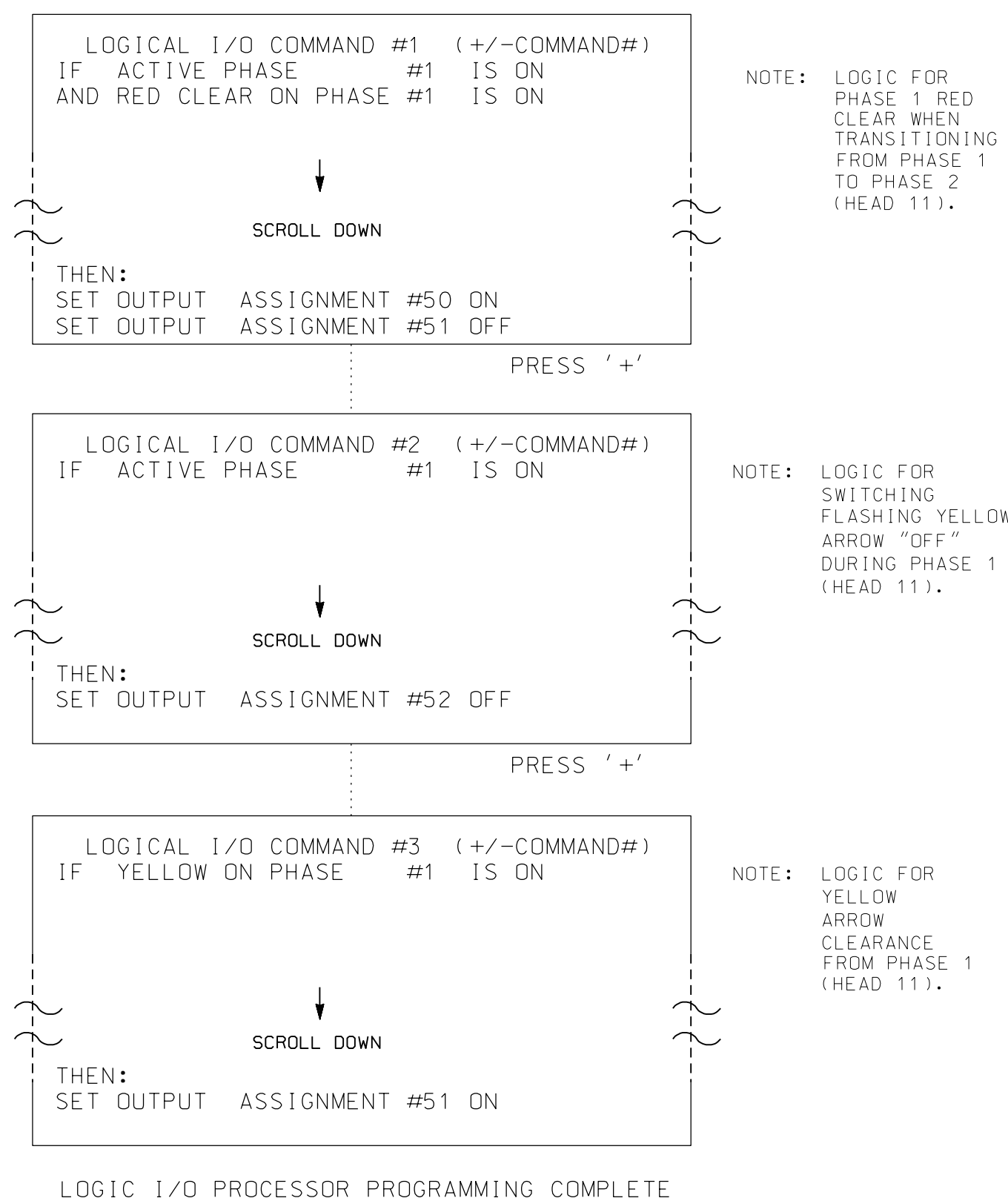


## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



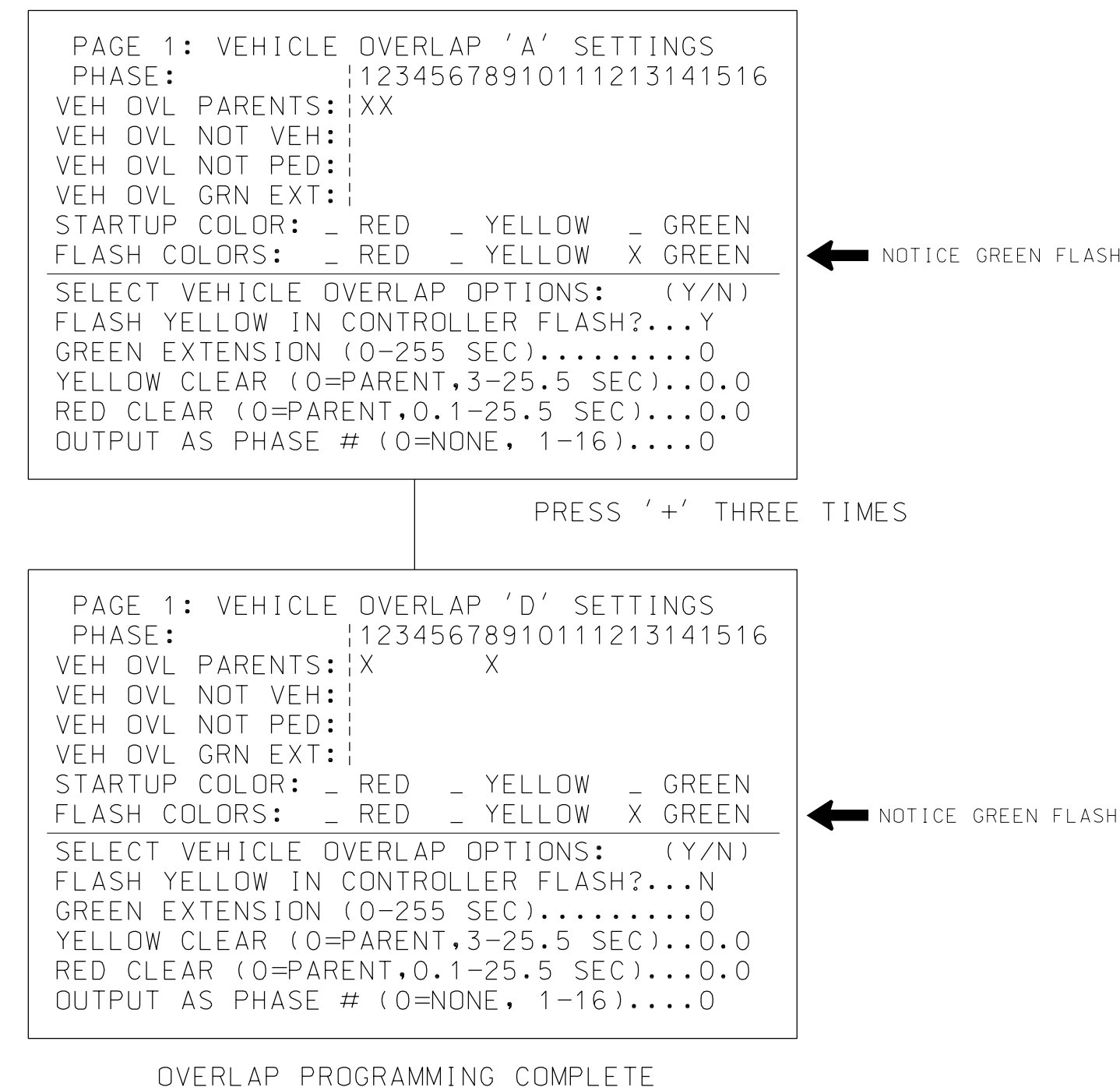
### OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red  
 OUTPUT 51 = Overlap A Yellow  
 OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 03-1080  
 DESIGNED: JUNE 2014  
 SEALED: December 19, 2014  
 REVISED:

13-JUL-2015 10:33  
 D:\Transportation\Projects\03-1080\03-1080.dgn  
 WILEZ054 AT 0259140

**ATKINS** 1616 EAST MILLBROOK ROAD, SUITE 310  
 RALEIGH, NORTH CAROLINA 27609  
 (919) 876-6888 NCBEES #F-0326

Signal Upgrade - Final Design (Electrical Detail Sheet 2 of 2)

ELECTRICIAL AND PROGRAMMING DETAILS FOR:  Prepared for the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	<b>US 17 Business (Market Street)                  at                  Cinema Drive</b> Division 03 New Hanover County Wilmington	SEAL  MELISSA B. TOTH ENGINEER
PLAN DATE: June 2014 REVIEWED BY: LM Moon PREPARED BY: AJ Davis REVIEWED BY: MB Toth		INIT. DATE _____
REVISIONS:		_____
12/19/2014 DATE		_____
03-1080 SIG. INVENTORY NO.		_____