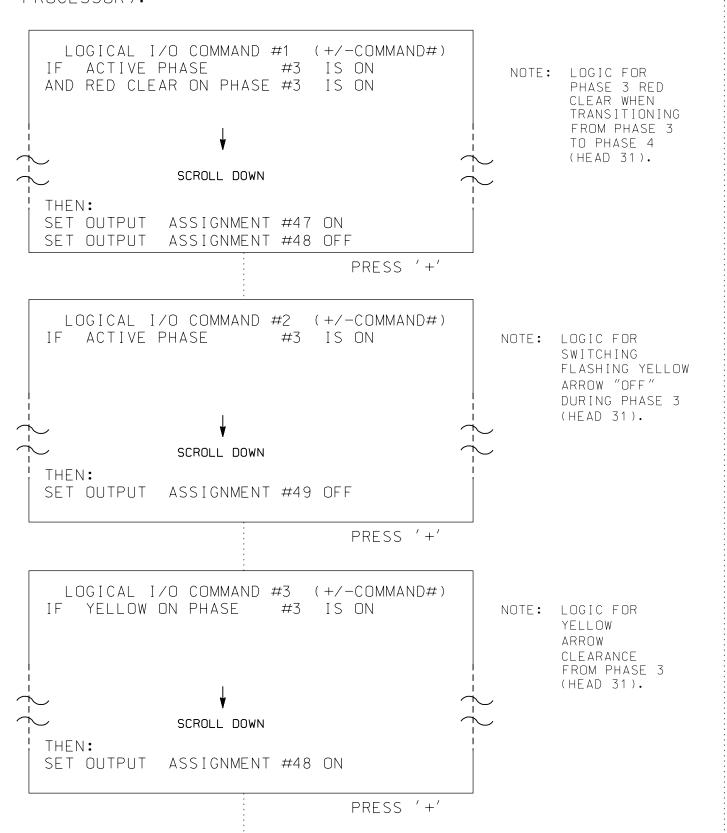
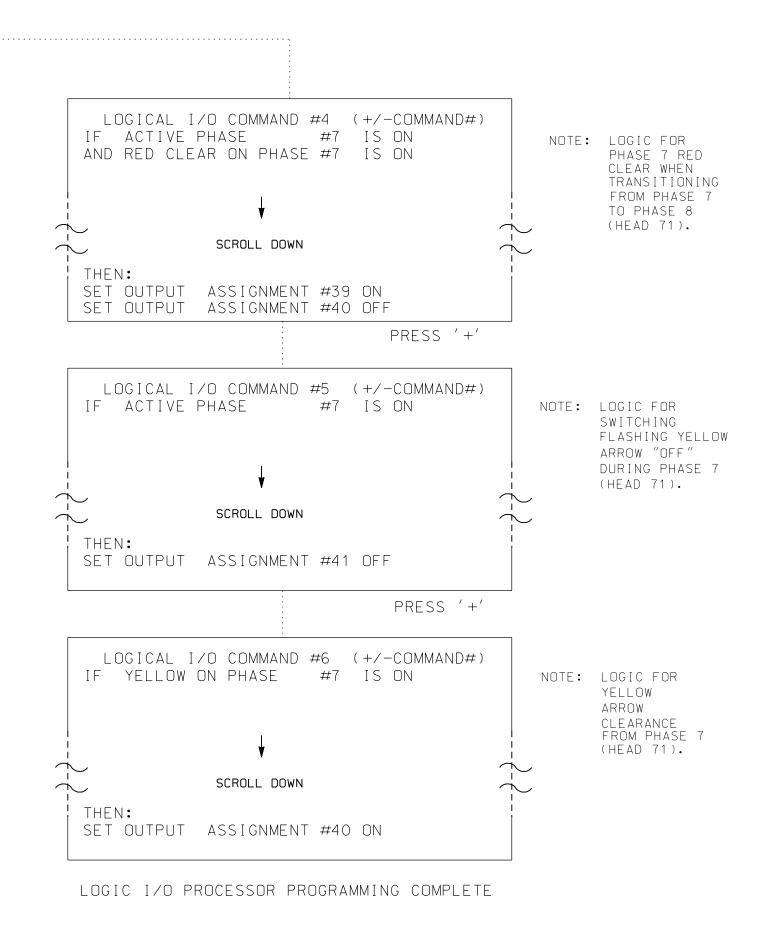
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





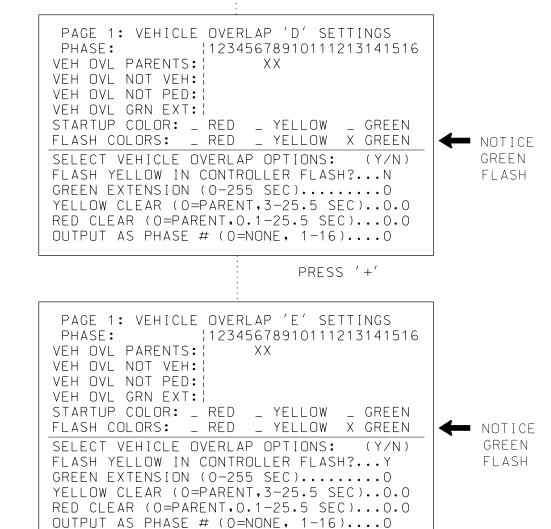
OUTPUT REFERENCE SCHEDULE

OUTPUT 39 = Overlap D Red OUTPUT 40 = Overlap D Yellow OUTPUT 41 = Overlap D Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' PAGE 1: VEHICLE OVERLAP 'B' SETTINGS ¦12345678910111213141516 PHASE: VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0 PRESS '+' PAGE 1: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0



PAGE 1: VEHICLE OVERLAP 'F' SETTINGS 12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) GREEN FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+'

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

PRESS '+'

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Signal Upgrade - Electrical Detail Sheet 2 of 3

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0786 DESIGNED: June 2014 SEALED: December 19, 2014 REVISED:

ELECTRICAL AND PROGRAMMING US 74 (Martin Luther King, Jr. Pkwy.) Prepared for the Offices of: SR 1175 (N. Kerr Avenue) Division 03 New Hanover County Wilmington PLAN DATE: June 2014 REVIEWED BY: LM Moon

REVISIONS

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SEAL

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