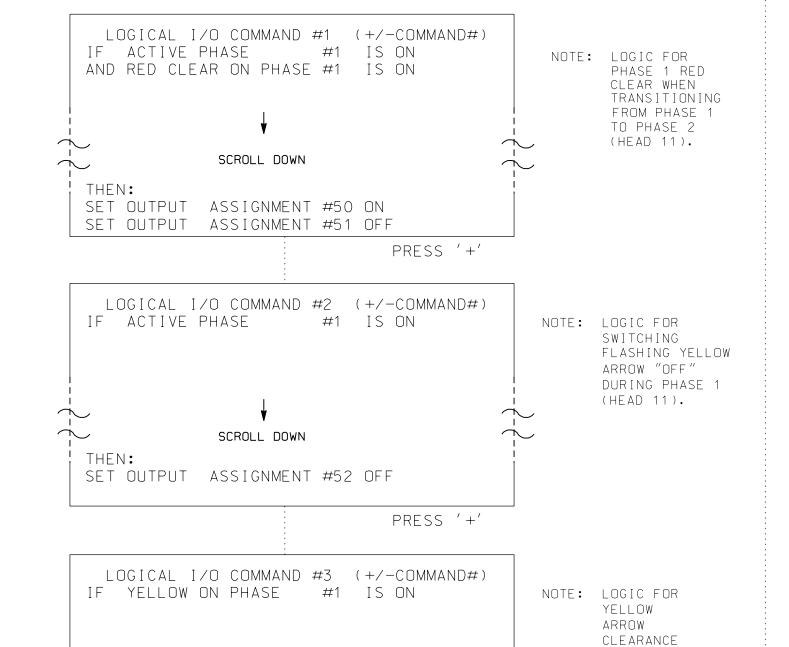
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

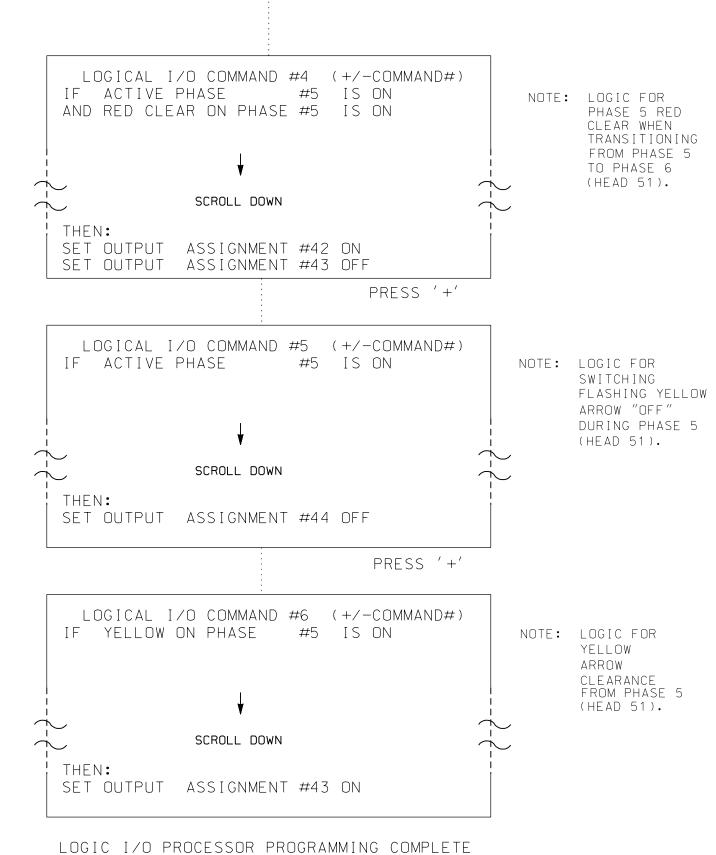
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



PRESS '+'

SCROLL DOWN

SET OUTPUT ASSIGNMENT #51 ON



### OUTPUT REFERENCE SCHEDULE

FROM PHASE 1

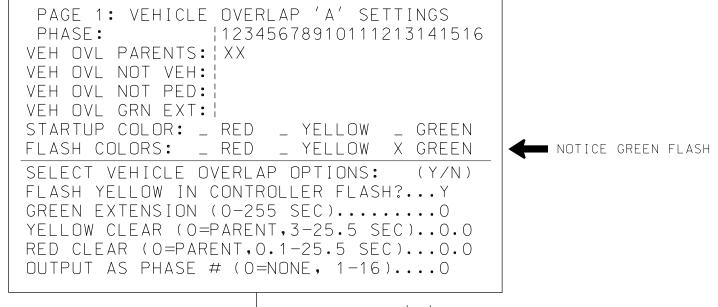
(HEAD 11).

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

### OVERLAP PROGRAMMING DETAIL

#### (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



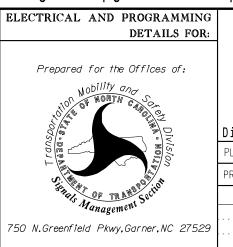
PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø3-1Ø83T1&T2 DESIGNED: June 2014 SEALED: December 19,2014 REVISED:

Signal Upgrade - Temporary Design 1 & 2 (Electrical Detail Sheet 2 of 2)



# SR 1175 (Kerr Avenue) Birchwood Drive / Wilmington Avenue Extension Division 03 New Hanover County

Wilmington PLAN DATE: June 2014 REVIEWED BY: LM Moon PREPARED BY: AM Encarnacion Reviewed BY: MB Toth REVISIONS INIT. DATE

Melissa B. Toth 12/19/2014 IG. INVENTORY NO.03-1083T1&T

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