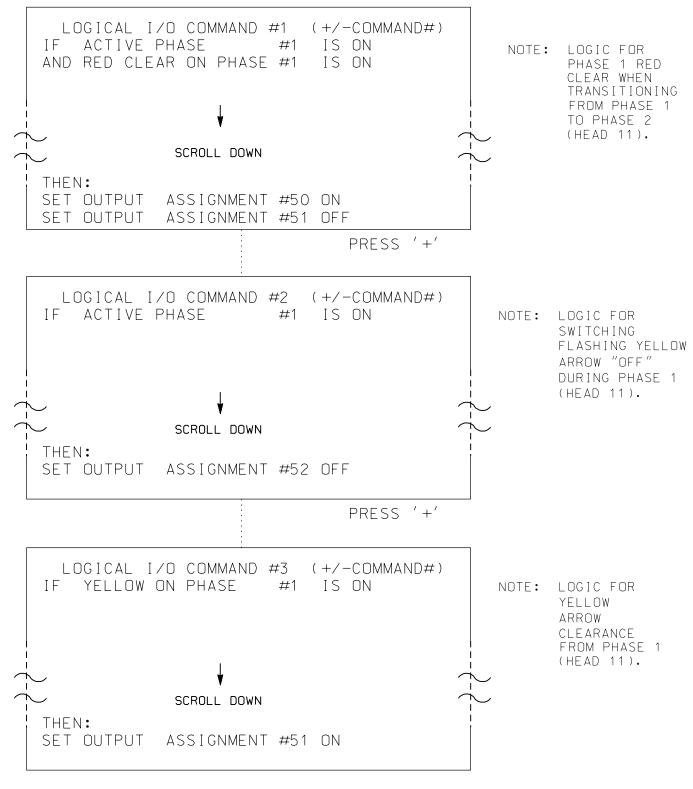
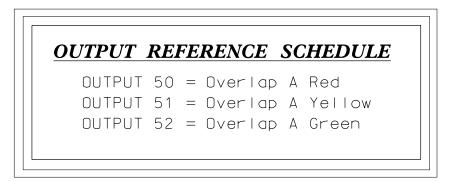
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

## (program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE



ATKINS 1616 EAST MILLBROOK ROAD, SUITE 310 RALEIGH, NORTH CAROLINA 27609 (919) 876-6888 NCBEES #F-0326



			PROJECT REFERENCE NO. U-3338B	SHEET NO.
OVERLAP PROGRAMMING DE	AIL			
(program controller as shown below)				
FROM MAIN MENU PRESS '8' (OVERLAPS) '1' (VEHICLE OVERLAP SETTINGS).	, THEN			
PAGE 1: VEHICLE OVERLAP 'A' SETTI PHASE:  12345678910111213				
VEH OVL PARENTS: XX VEH OVL NOT VEH:				
VEH OVL NOT PED:   VEH OVL GRN EXT:				
STARTUP COLOR: _ RED _ YELLOW _ FLASH COLORS: _ RED _ YELLOW X		TICE GREEN	FLASH	
SELECT VEHICLE OVERLAP OPTIONS: FLASH YELLOW IN CONTROLLER FLASH?.	Y/N) • Y			
GREEN EXTENSION (0-255 SEC) YELLOW CLEAR (0=PARENT,3-25.5 SEC)	• • 0			
RED CLEAR ( $O$ =PARENT, 0.1-25.5 SEC). OUTPUT AS PHASE # ( $O$ =NONE, 1-16).	.0.0			
OVERLAP PROGRAMMING COMPLETE				
			_	
THIS E	LECTRICAL DETAIL	IS FOR		
	GNAL DESIGN: Ø3 <sup>.</sup> ED: June 2014	-1082T3		
	:D: June 2014 : December 19,2	Ø14		
REVISE				
Signal Upgrade - Temporary Design ELECTRICAL AND PROGRAMMING SP 11				
DETAILS FOR: SK 11	'5 (Kerr Ave at	nue)	SEAL	
	.nema Drive/		RTH CAR RTH CAR RTH CAR	NAT T
	g Center Dr:		SEAL	2
Division 03 PLAN DATE: June 2	New Hanover County014REVIEWED BY:	Wilmi LM Moon	Ington Kornee	R. H. H.
PREPARED BY: AM Encar		MB Toth	DATE	
B10 Figs Ver TRANS Social REVISIONS T50 N.Greenfield Pkwy, Garner, NC 27529			Melissa B. Toth	12/19/2014
			SIG. INVENTORY NO. (	DATE )3-1082T3

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