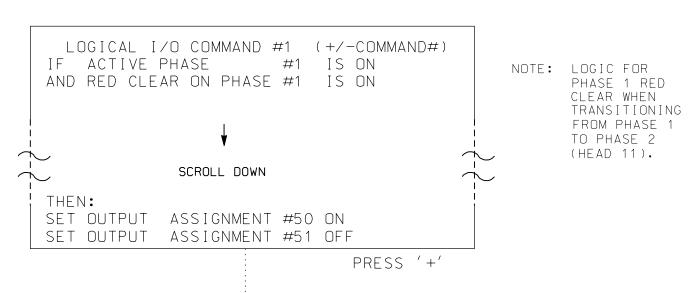
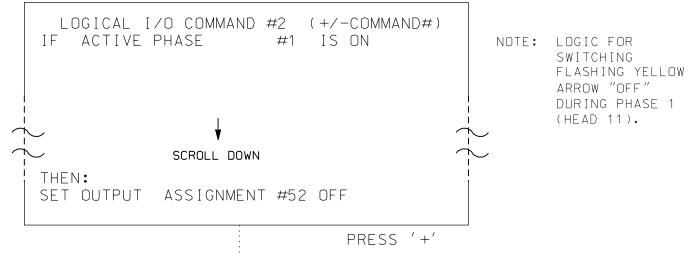
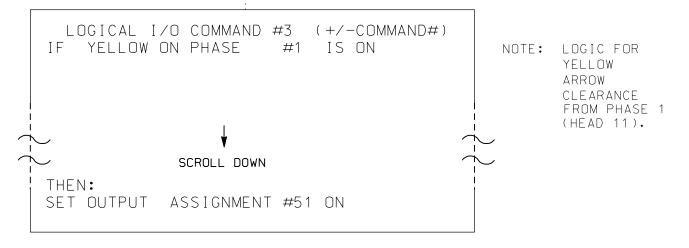
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).







LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

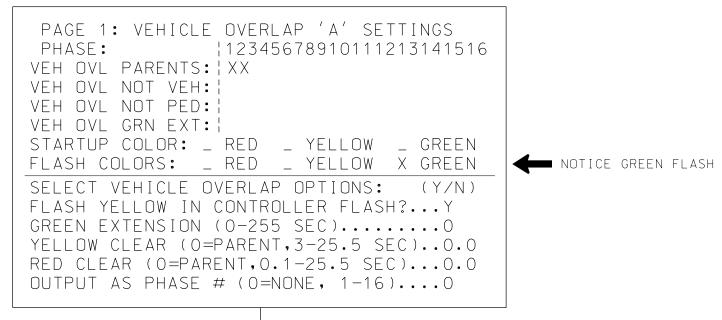
#### **OUTPUT REFERENCE SCHEDULE**

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

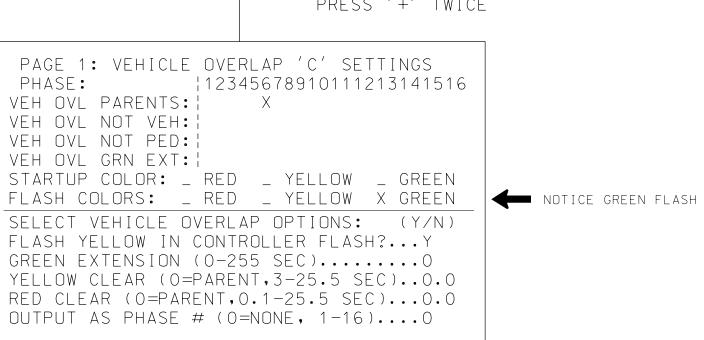
### OVERLAP PROGRAMMING DETAIL

#### (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



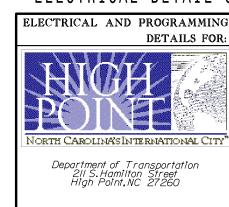
PRESS '+' TWICE



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: HPØ814 DESIGNED: July 2014 SEALED: June 5,2015 REVISED:

ELECTRICAL DETAIL SHEET 2 OF 2



# N. Centennial Street Beaucrest Avenue

Guilford County High Point PLAN DATE: July 2014 REVIEWED BY: LM Moon PREPARED BY: AM Encarnacion | REVIEWED BY: MB Toth

INIT. DATE

REVISIONS

Melissa B. Toth

postGNATURE

DATE SIG. INVENTORY NO. HP0814

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