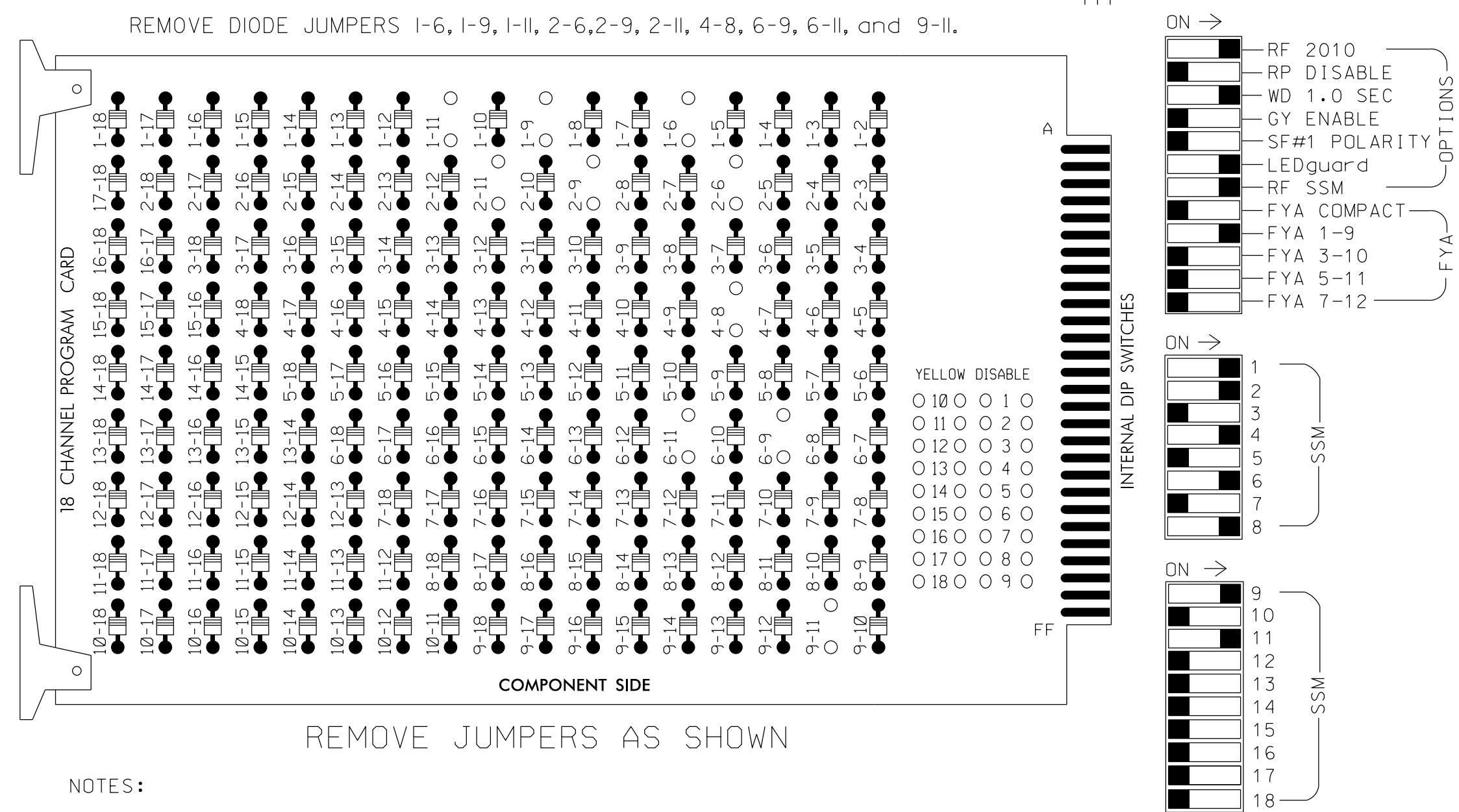


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

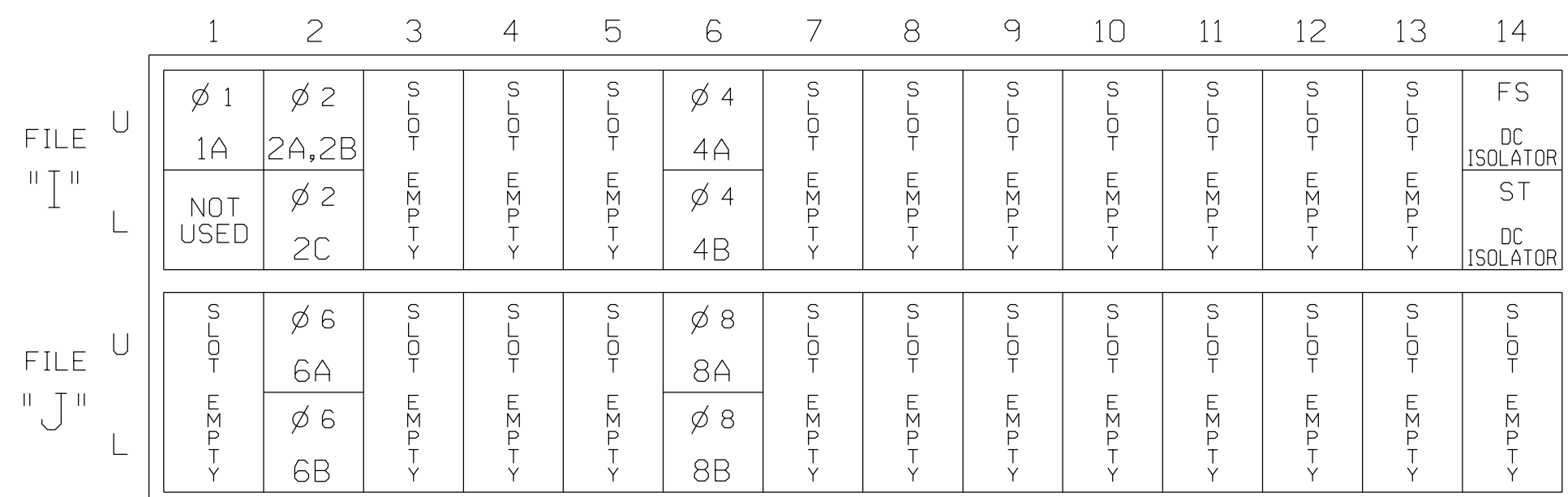


NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE POSITION LAYOUT

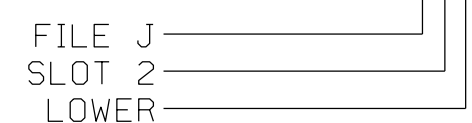
(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

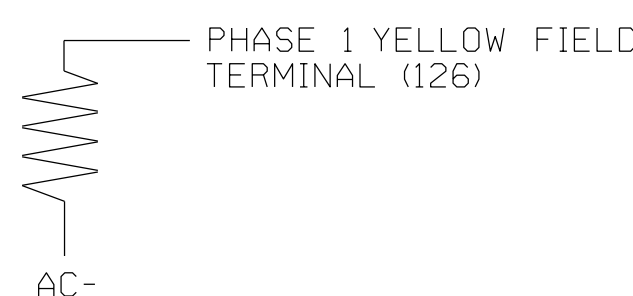
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the High Point Signal System.

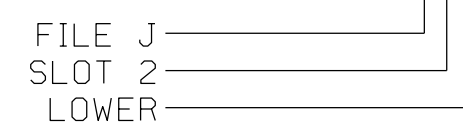
EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S8,S11,AUX S1,AUX S4.
 PHASES USED.....1,2,4,6,8.
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....NOT USED

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			15
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
2C	TB2-7,8	I2L	43	5	12	2	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

INPUT FILE POSITION LEGEND: J2L



PROJECT REFERENCE NO.	SHEET NO.
C-5558	Fig. 211.1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11*	22,23	NU	NU	41,42	NU	NU	61,62	NU	NU	81,82	NU	11*	NU	NU	21*	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121					A114
YELLOW ARROW													A122					A115
FLASHING YELLOW ARROW													A123					A116
GREEN ARROW	127																	

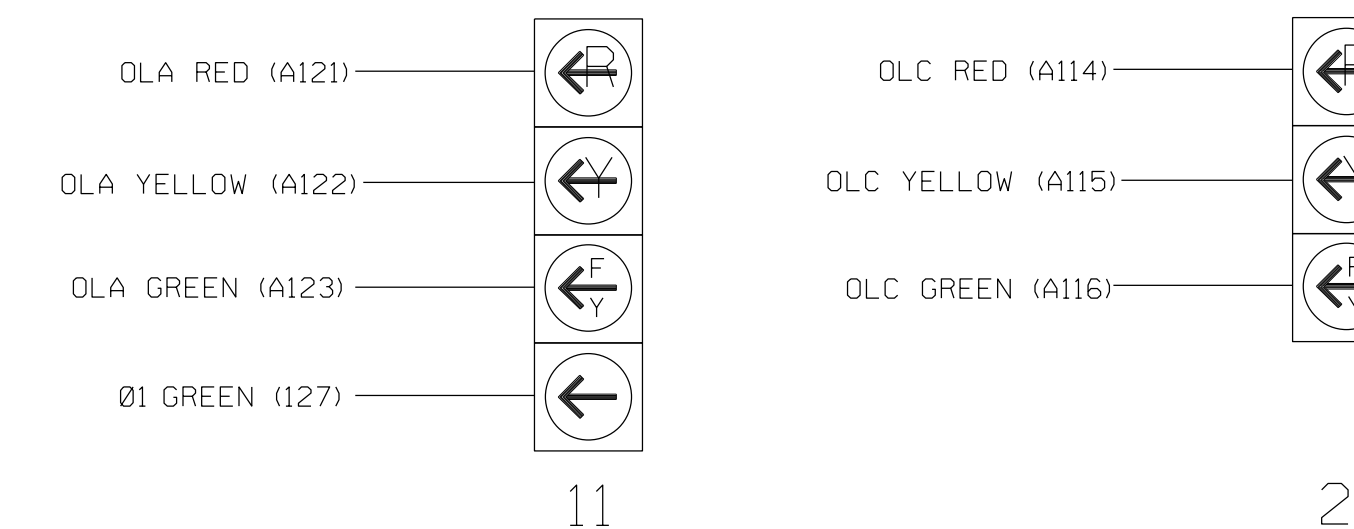
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail below.

3 & 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for signal head 11 requires special logic programming. See sheet 2 of 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: HP0814
 DESIGNED: July 2014
 SEALED: June 5, 2015
 REVISED:

ELECTRICAL DETAIL SHEET 1 OF 2

 NORTH CAROLINA INTERNATIONAL CITY Department of Transportation 211 S. Hamilton Street High Point, NC 27260	N. Centennial Street at Beaucrest Avenue		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 025892 MELISSA B. TOTH
	Division 07 PLAN DATE: July 2014 PREPARED BY: AM Encarnacion	Guilford County REVIEWED BY: LM Moon REVIEWED BY: MB Toth	

05-JUN-2015 15:54
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