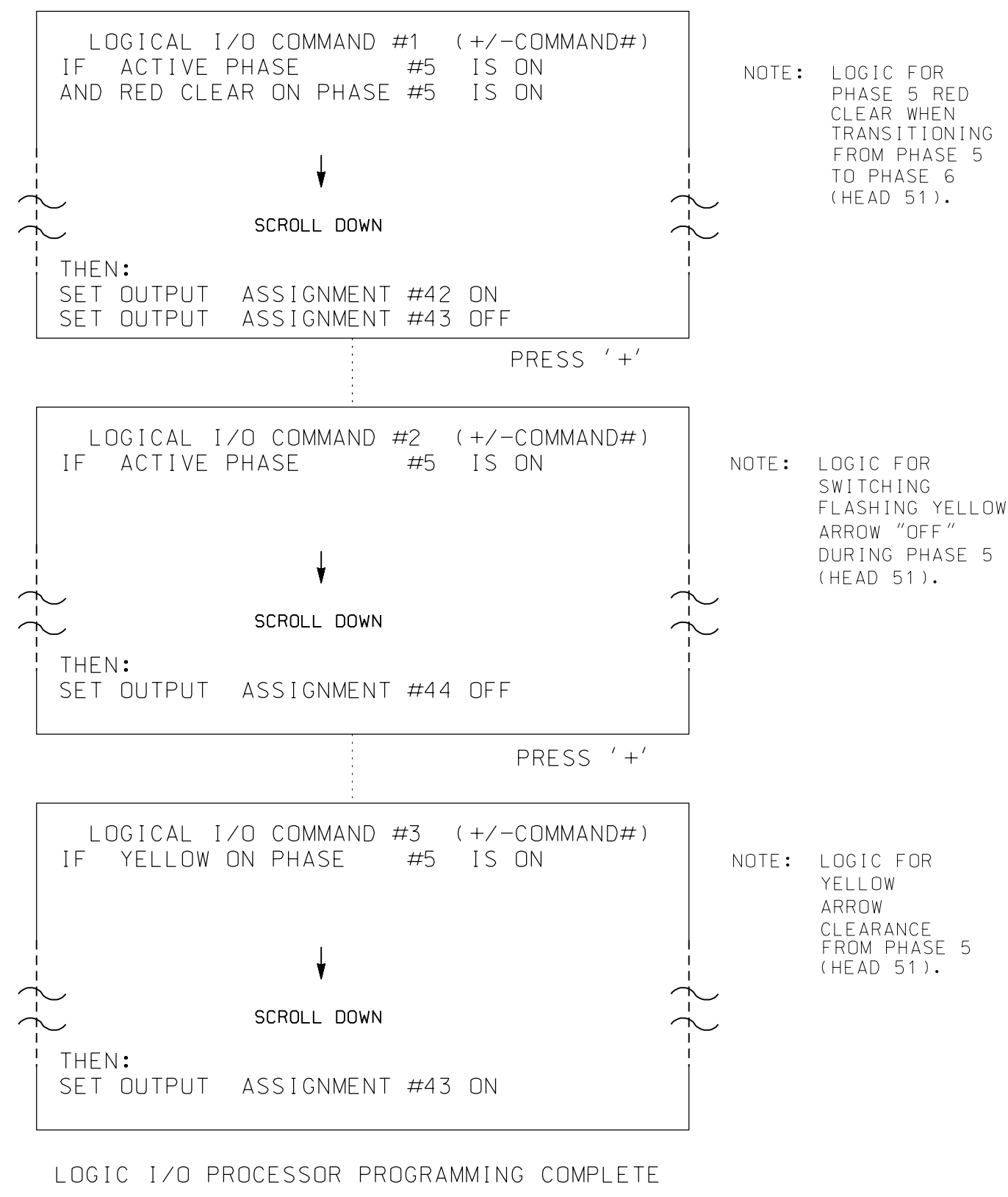


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



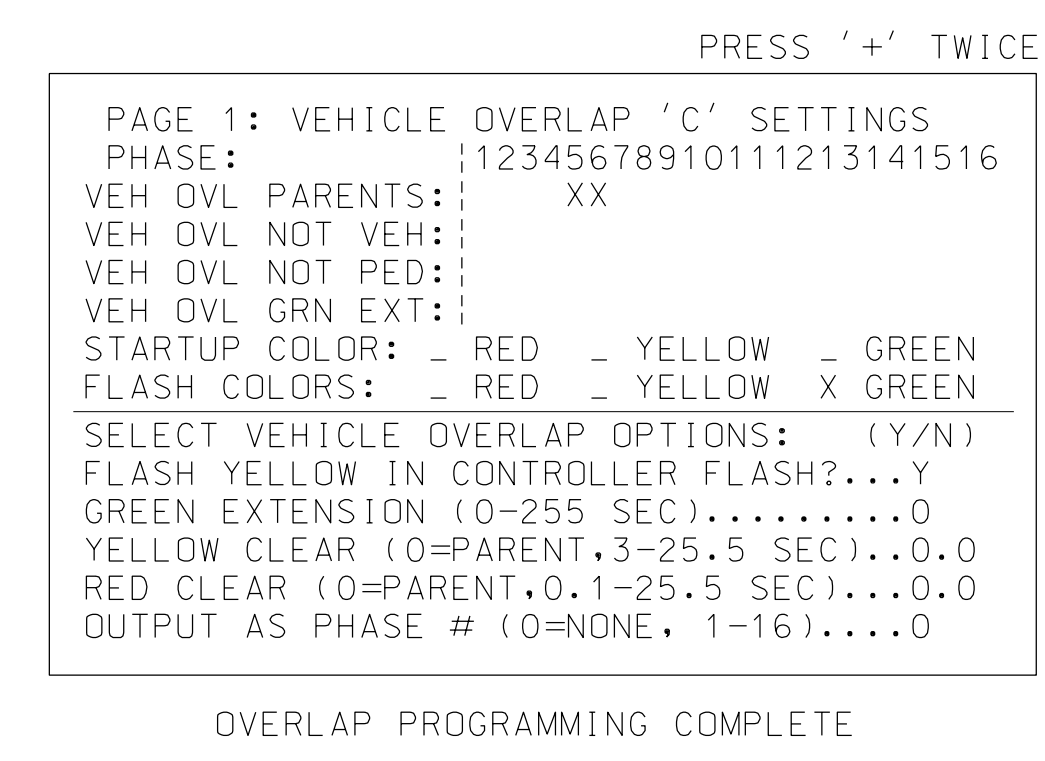
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: HP0310
DESIGNED: July 2014
SEALED: June 5, 2015
REVISED:

05-JUN-2015 15:42 D:\Transportation\Projects\Curr\10003777 - High Point St Sys\Signal\DesignPackage 2\Wiring\HP0310E.dgn WLEZ054 AT 0259140

ATKINS 1616 EAST MILLBROOK ROAD, SUITE 310
RALEIGH, NORTH CAROLINA 27609
(919) 876-6888 NCBEEES #F-0326

ELECTRICAL DETAIL SHEET 2 OF 2

	Prospect Street at Eugene Avenue		
	Division 07	Guilford County	
PLAN DATE: July 2014	REVIEWED BY: LM Moon		
PREPARED BY: AM Encarnacion	REVIEWED BY: MB Toth		
REVISIONS	INIT.	DATE	

DocuSigned by: **Melissa B. Toth** 6/5/2015
SIGNATURE DATE
SIG. INVENTORY NO. HP0310