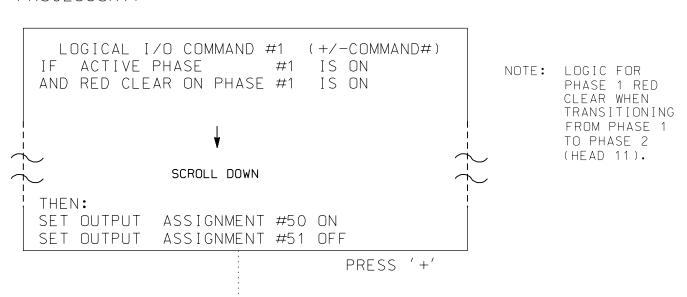
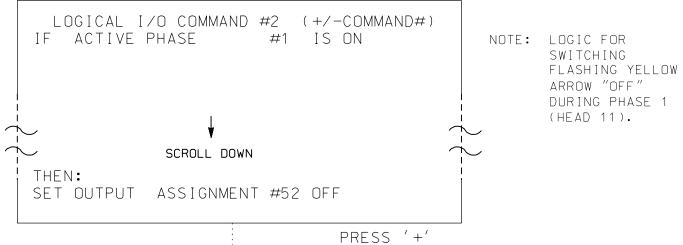
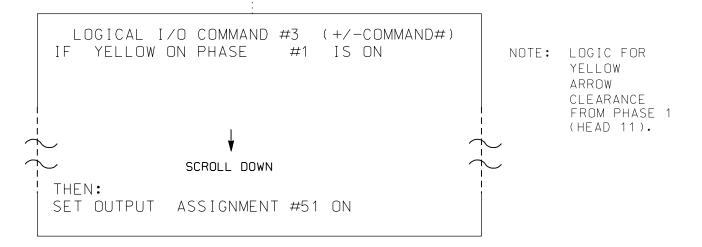
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).







LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

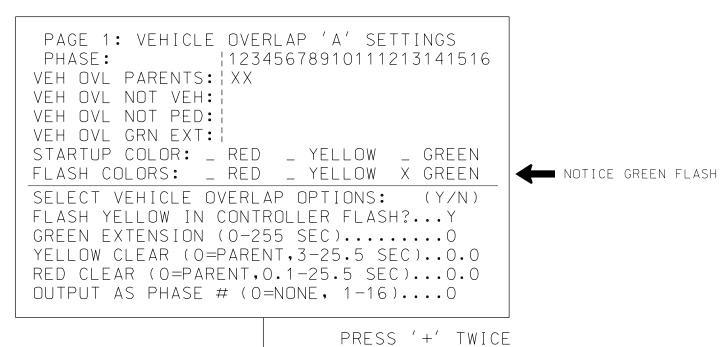
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: ¦12345678910111213141516 VEH OVL PARENTS: | X VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT, 3-25.5 SEC)..0.0

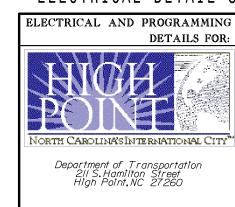
OVERLAP PROGRAMMING COMPLETE

RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: HPØ226 DESIGNED: April 2014 SEALED: June 5,2015 REVISED:

ELECTRICAL DETAIL SHEET 2 OF 2



Elm Street Surrett Drive/ Mill Avenue

Guilford County PLAN DATE: April 2014 REVIEWED BY: LM Moon PREPARED BY: AM Encarnacion | REVIEWED BY: MB Toth

INIT. DATE

REVISIONS

Melissa B. Toth 6/5/2015
SIGNATURE DATE SIG. INVENTORY NO. HP0226

ATKINS 1616 EAST MILLBROOK ROAD, SUITE 310 RALEIGH, NORTH CAROLINA 27609 (919) 876-6888 NCBEES #F-0326