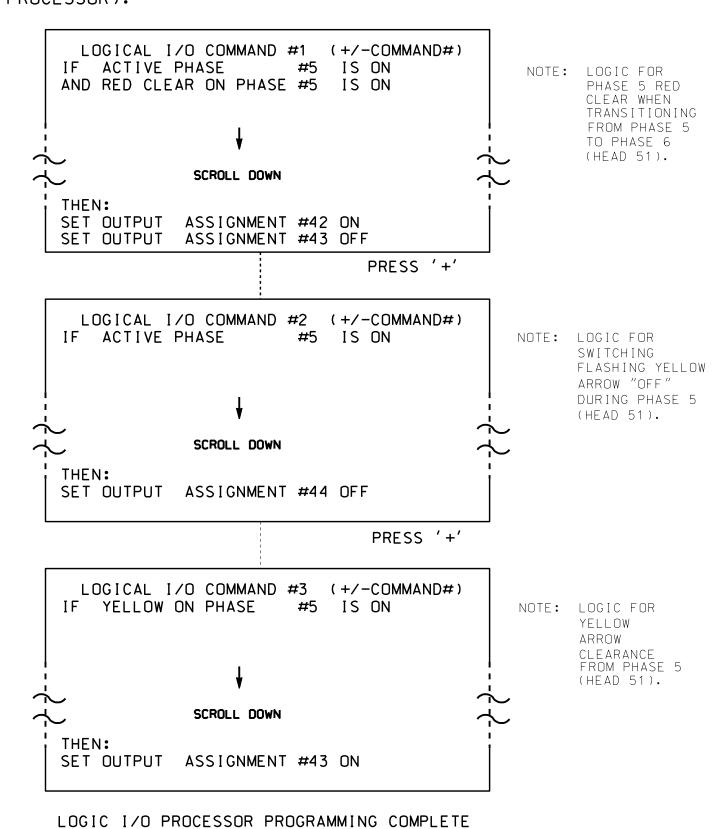
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



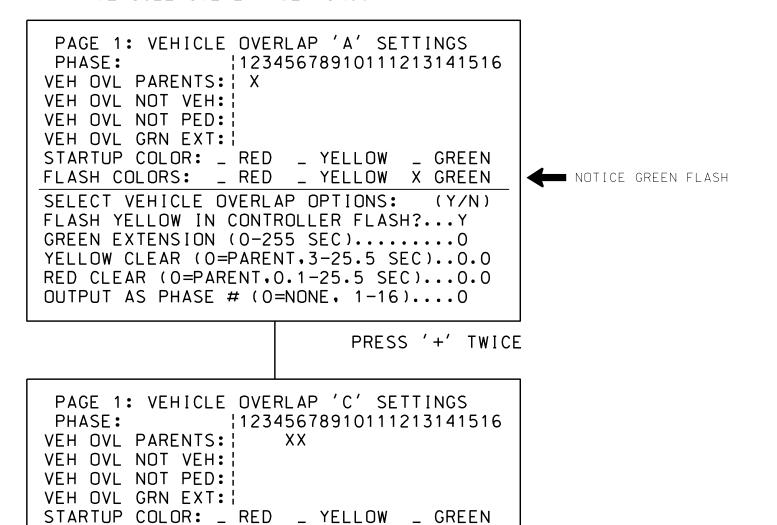
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

FLASH COLORS: _ RED _ YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)......

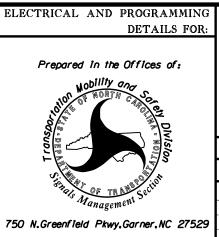
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0134
DESIGNED: April 2014
SEALED: 4/2/2015
REVISED: N/A

NOTICE GREEN FLASH

Electrical Detail - Sheet 2 of 2



NC 62 (Trindale Road) at SR 1577 (Archdale Road)

Division 8 Randolph County DS Archdale
PLAN DATE: July 2014 REVIEWED BY: 977

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS

INIT. DATE

John T. Rowe, Ja

SEAL

008453