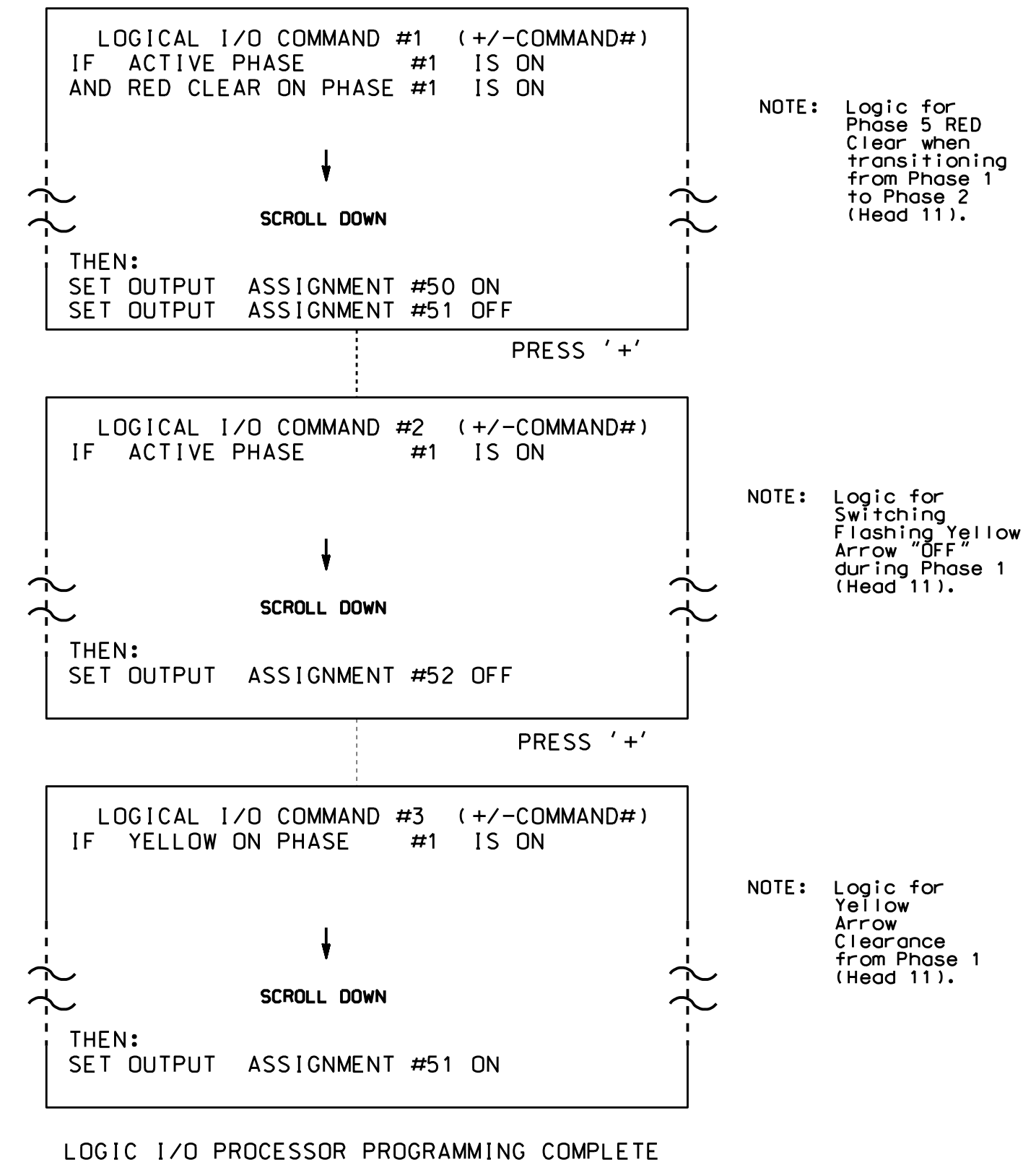


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

Press '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

Press '+' ONCE

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...N
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

In order to insure that signals flash concurrently on the same approach, make the following flasher circuit changes:

- On rear of PDA - Remove wire from Term. T2-4 and Terminate on T2-2.
- On rear of PDA - Remove wire from Term. T2-5 and Terminate on T2-3.
- Remove Flasher Unit 2.

The changes listed above ties all Phases and Overlaps to Flasher Unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2116
 DESIGNED: May 2014
 SEALED: 4/13/15
 REVISED: N/A

Electrical Detail - Sheet 2 of 2

	Barrow Road at SR 1836 (Willard Dairy Road)		
	Division 7 PLAN DATE: July 2014 PREPARED BY: B. SIMMONS	Guilford County High Point REVIEWED BY: T. Joyce REVIEWED BY:	
REVISIONS			INIT. DATE
SIG. INVENTORY NO. 07-2116			DATE

C:\Users\jrc1\OneDrive\Documents\Signal\Working\07-2116_Sig\ele...xxx.dgn
 07-2116-01-00-38
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 bis:simmons