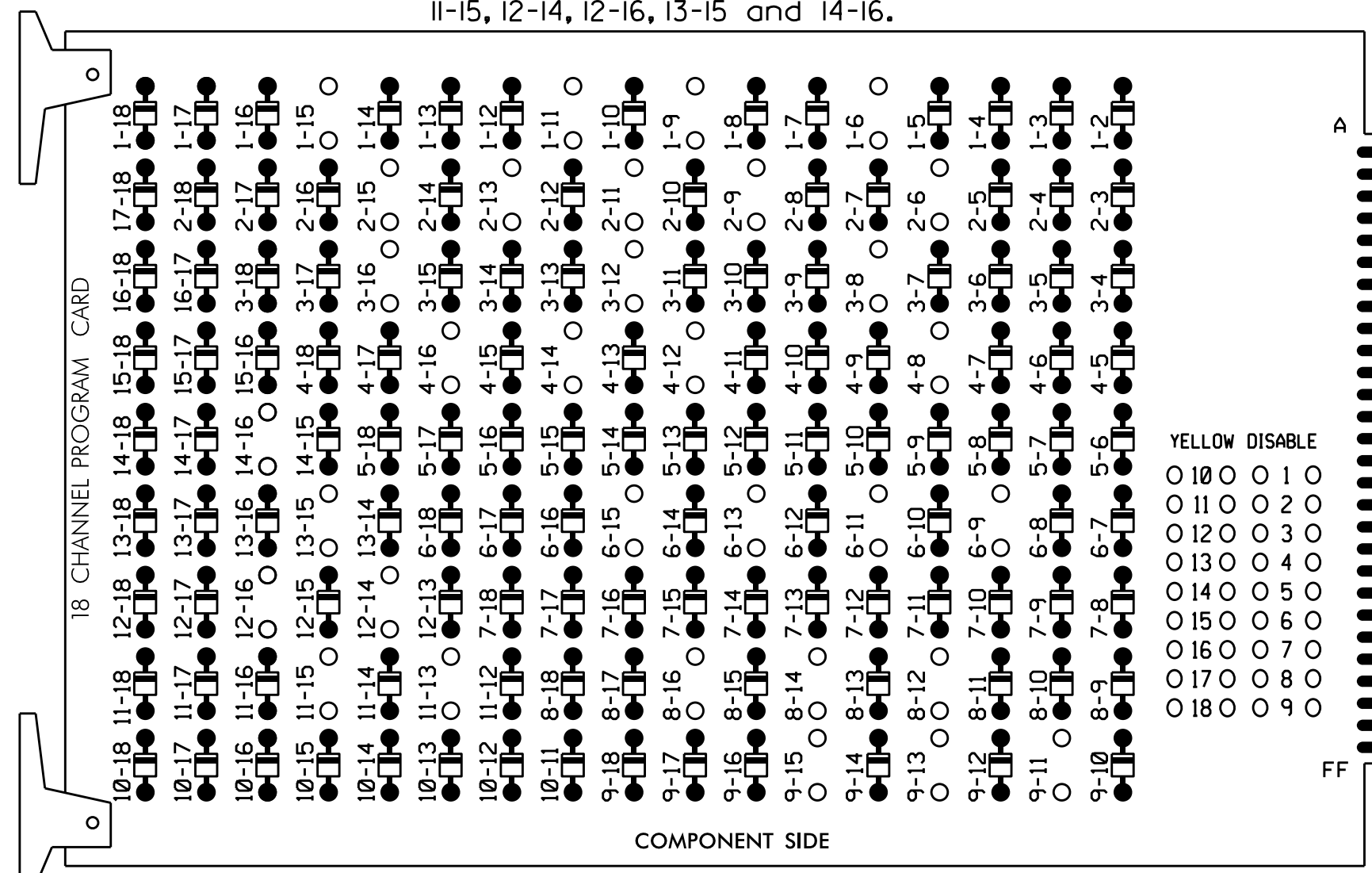


EDI MODEL 2018EClip-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 1-15, 2-6, 2-9, 2-11, 2-13, 2-15, 3-8, 3-12, 3-16, 4-8, 4-12, 4-14, 4-16, 6-9, 6-11, 6-13, 6-15, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phase 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 (12-STD; 6-AUX)
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S8,S9,S11
 S12,AUX S1,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	22,23	P21, P22	31,32	23	41,42	P41, P42	NU	61,62	P61, P62	NU	81,82	P81, P82	11	NU	NU	21	43	NU
RED	128				101			134		107									
YELLOW	*	129			102			135		108									
GREEN		130			103			136		109									
RED ARROW				116									A121			A114	A101		
YELLOW ARROW				117	117								A122			A115	A102		
FLASHING YELLOW ARROW													A123			A116	A103		
GREEN ARROW	127			118	118														
Hand			113			104		119		110									
Foot			115			106		121		112									

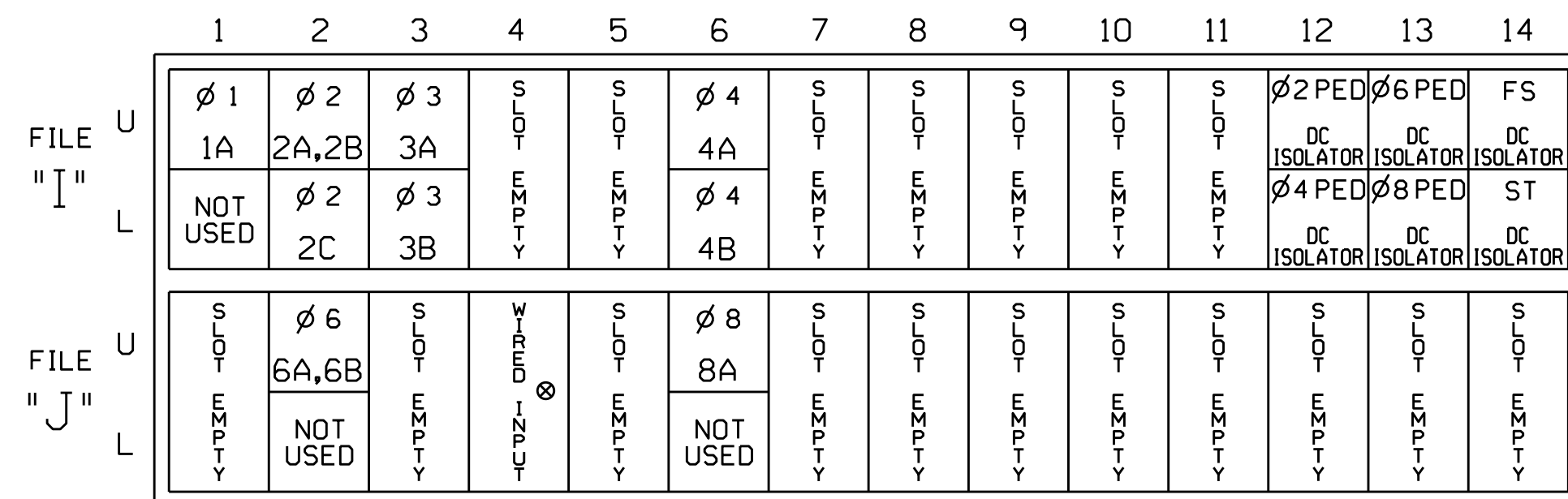
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

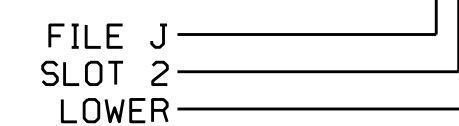
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
		J4U	48	10	26	6	Y	Y			
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
2C	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB2-9,10	I3U	63	25	32	3	Y	Y			3
3B	TB2-11,12	I3L	76	38	42	3	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

¹Add jumper from I1-W to J4-W, on rear of input file.

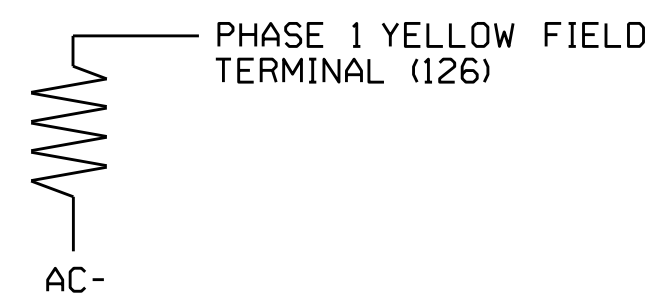
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



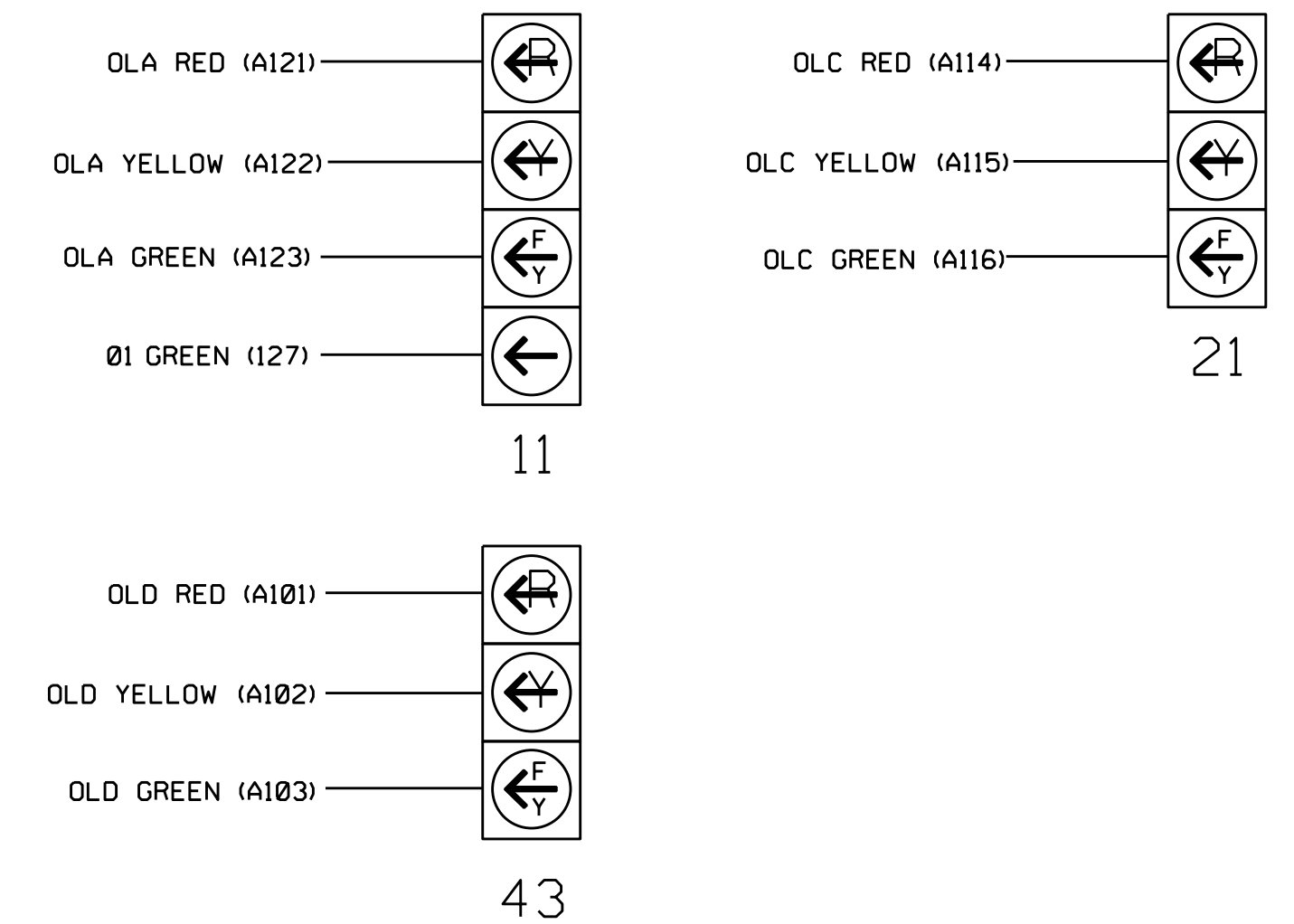
COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2116
 DESIGNED: May 2014
 SEALED: 4/13/15
 REVISED: N/A

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

Electrical Detail - Sheet 1 of 2

Electrical and Programming Details for: Barrow Road at SR 1836 (Willard Dairy Road)

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: T. Joyce

PREPARED BY: B. SIMMONS REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: George C. Brown 4/20/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: PROFESSIONAL ENGINEER, GEORGE C. BROWN, No. 022013

SIG. INVENTORY NO. 07-2116