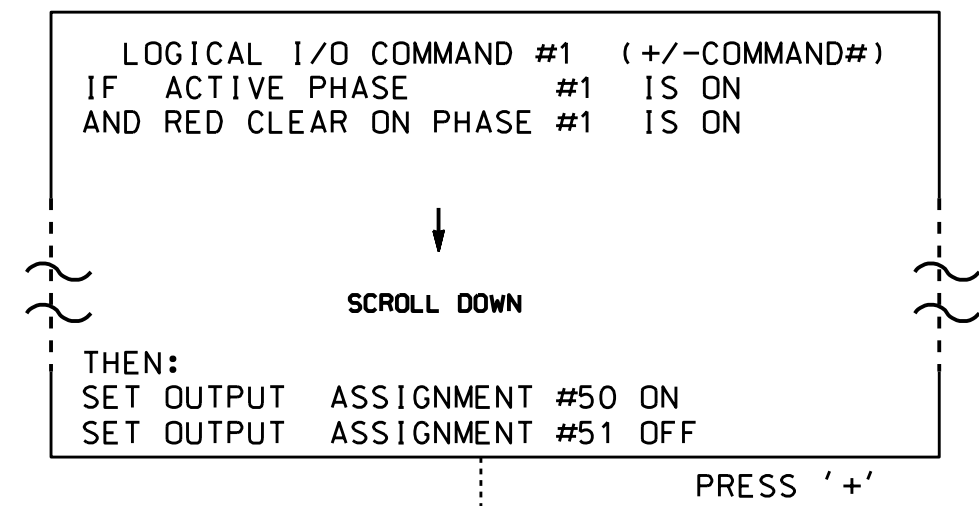


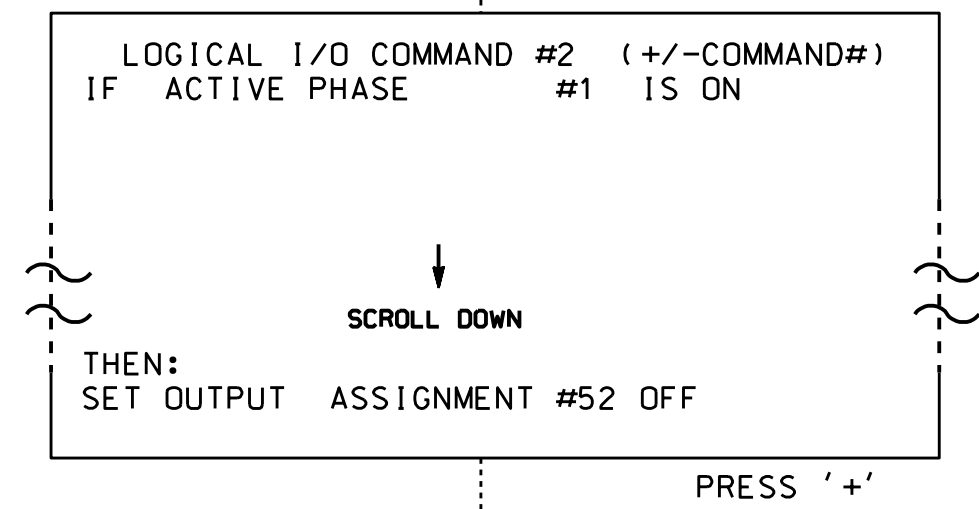
**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

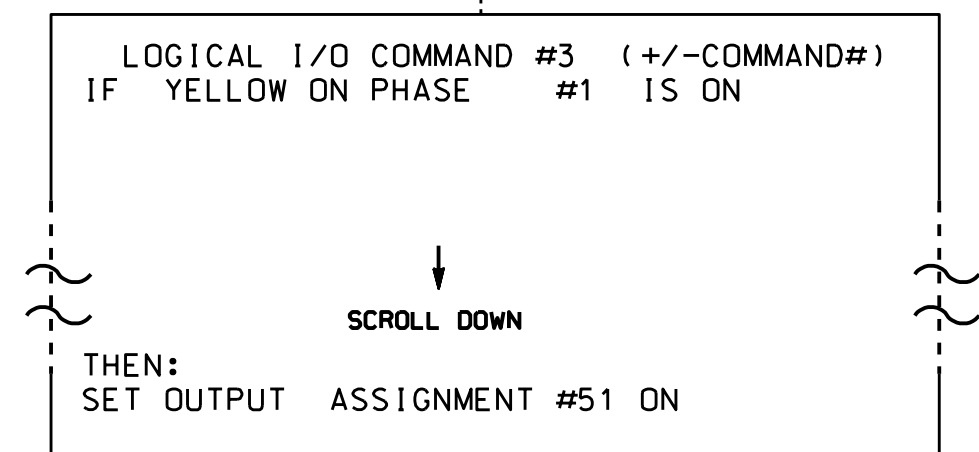
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OUTPUT REFERENCE SCHEDULE**

OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

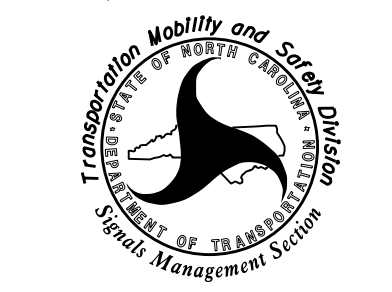
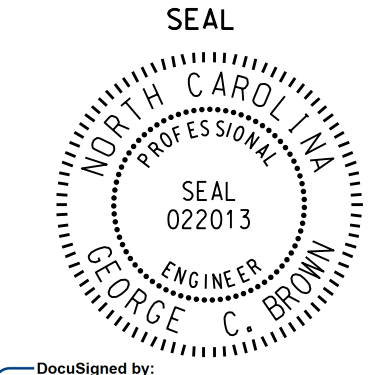
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:           12345678910111213141516
VEH OVL PARENTS:|XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR:  _ RED _ YELLOW _ GREEN
FLASH COLORS:   _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 07-2090  
DESIGNED: August 2014  
SEALED: 4/2/2015  
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of:  750 N. Grantfield Pkwy, Garner, NC 27529	NC 62 (Liberty Road) at I-85 Southbound Ramps		SEAL 
	Division 7      Guilford County      High Point	PLAN DATE: August 2014      REVIEWED BY: T. Joyce	PREPARED BY: C. Strickland      REVIEWED BY:
REVISIONS	INIT.	DATE	SIG. INVENTORY NO. 07-2090