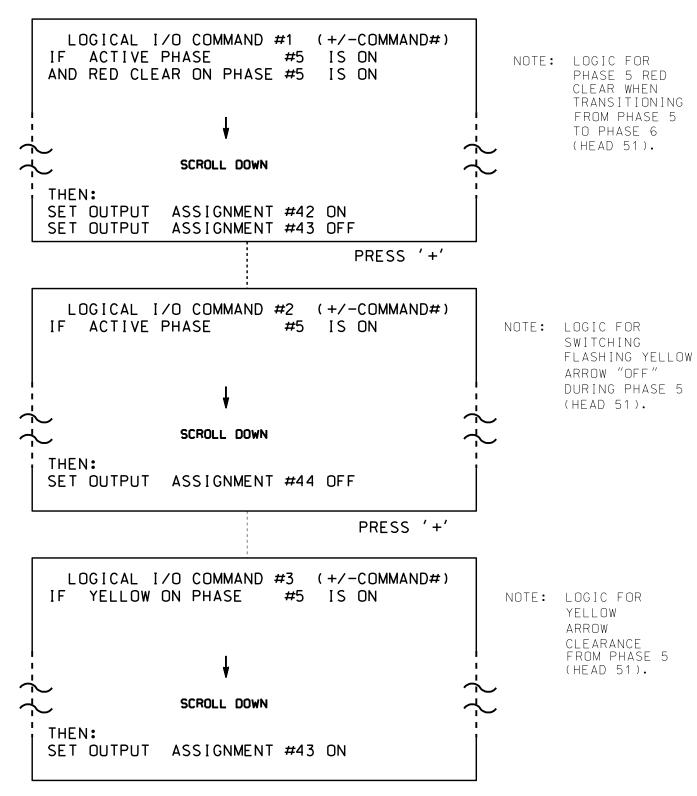
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

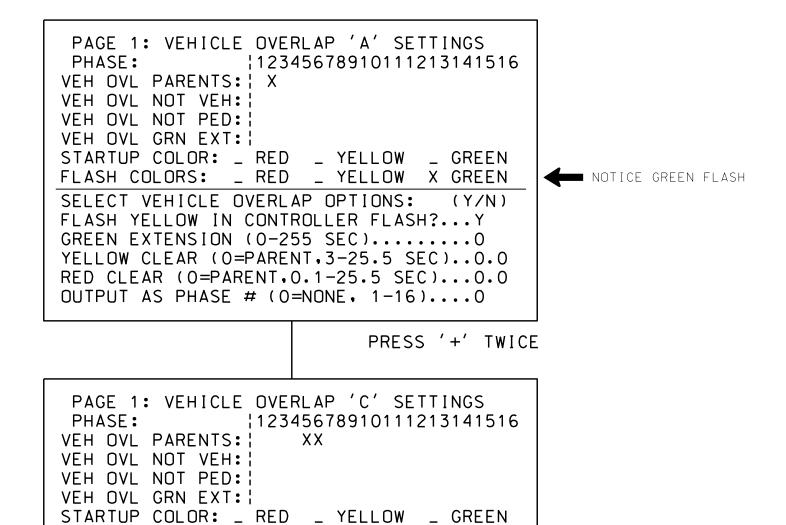
OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

PROJECT REFERENCE NO. C-5558 Sig. 164.2

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

FLASH COLORS: _ RED _ YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....0

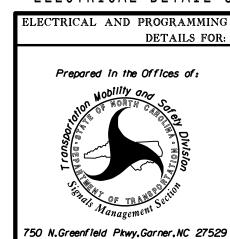
YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0

RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2057 DESIGNED: May 2014 SEALED: 3-13-15 REVISED: N/A

NOTICE GREEN FLASH

ELECTRICAL DETAIL SHEET 2 OF 2



SR 1009 (N. Main Street)

Westover Drive Guilford County

ivision 7 PLAN DATE: May 2014 REVIEWED BY: PREPARED BY: James Peterson | REVIEWED BY: REVISIONS INIT. DATE John T. Rowe, Jr. 3/17/2015

SIG. INVENTORY NO. 07-2057

SEAL