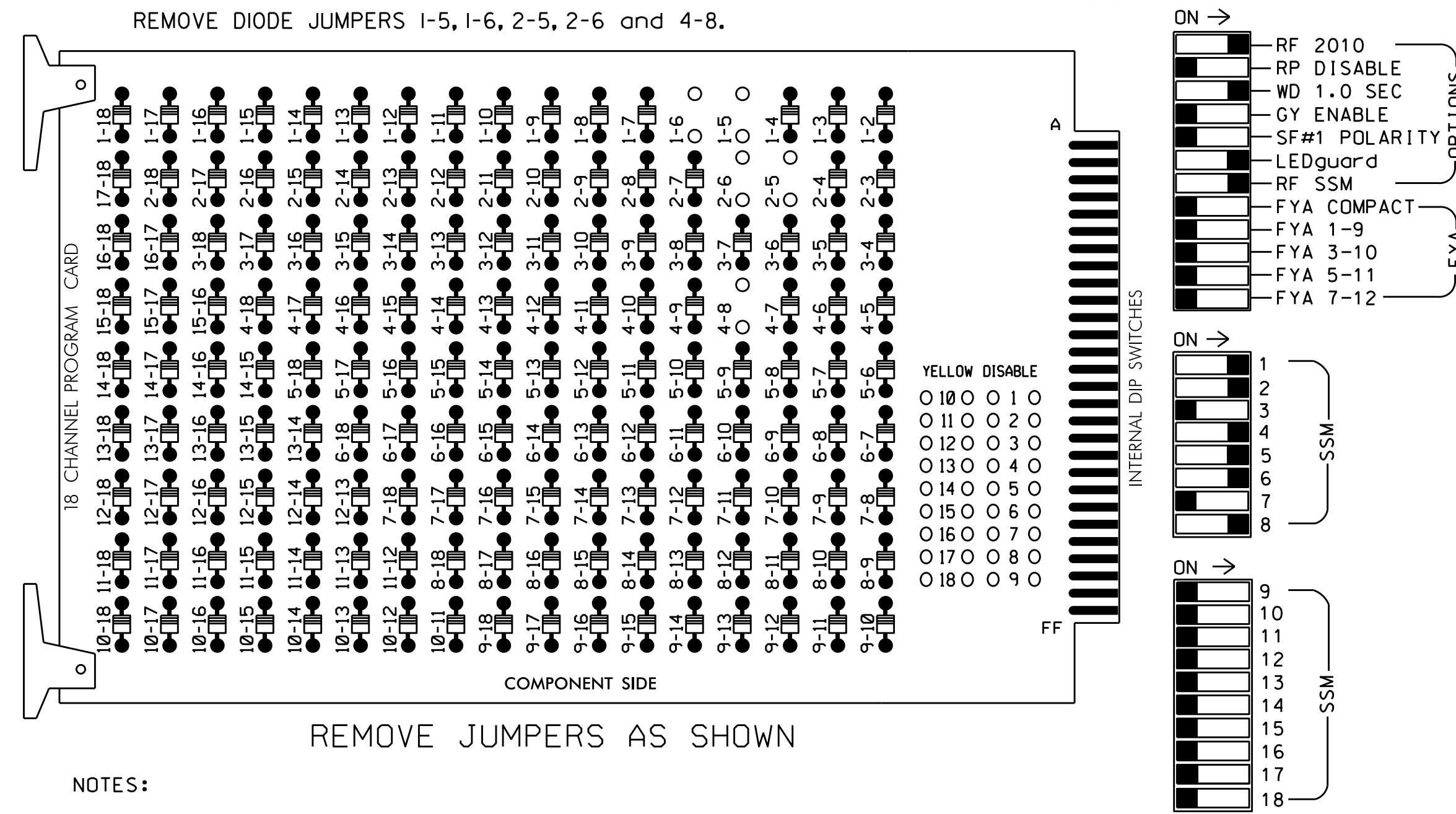


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 2 and 6 for Yellow Flash.
7. The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
 CABINET.....332  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAPS.....NONE

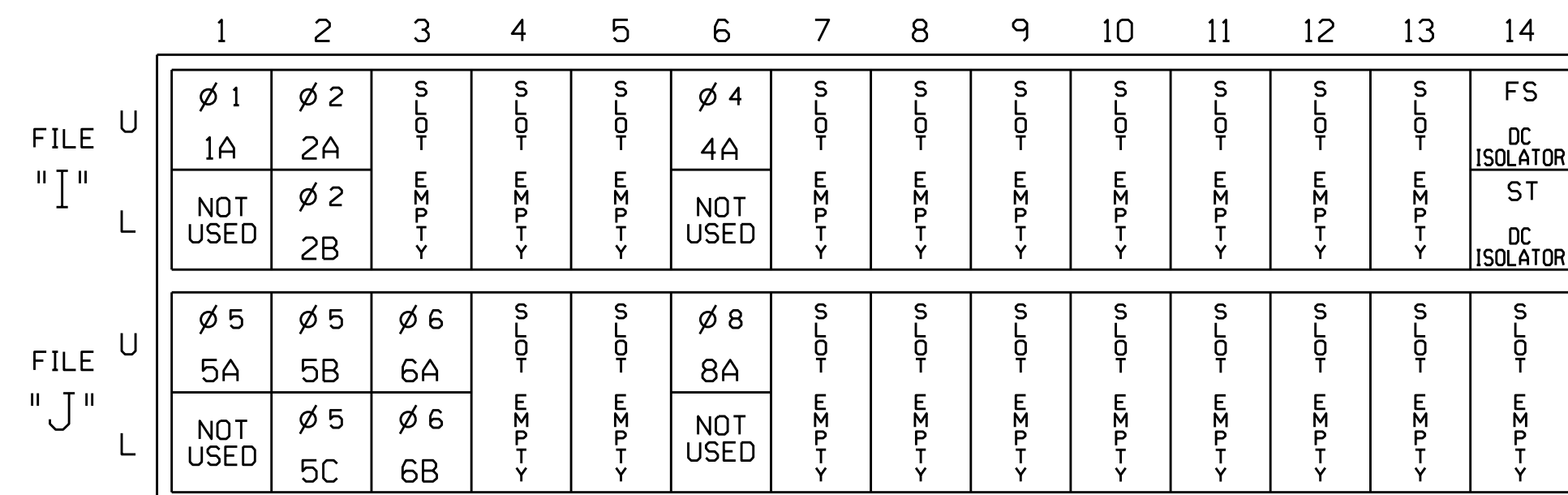
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	NU	42	51,52	61,62	NU	NU	81,82	NU
RED		128			101				134			107	
YELLOW		129			102				135			108	
GREEN		130			103				136			109	
RED ARROW	125								131				
YELLOW ARROW	126							132	132				
GREEN ARROW	127							133	133				

NU = Not Used

**INPUT FILE POSITION LAYOUT**

(front view)



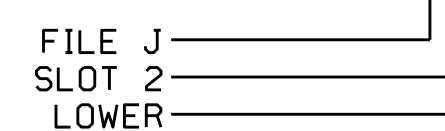
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			
5C	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

**INPUT FILE POSITION LEGEND: J2L**



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1889  
 DESIGNED: May 2014  
 SEALED: 3-27-15  
 REVISED: N/A

**Electrical Detail**

Electrical and Programming Details for: SR 1546 (Guilford College Rd.) at SR 1541 (Wendover Ave.) EB Ramps and SR 4179

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: JTR

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal: JOHN T. ROWE, JR., PROFESSIONAL ENGINEER, STATE OF NORTH CAROLINA, SEAL 008453

DocuSigned by: John T. Rowe, Jr. 3/31/2015

SIG. INVENTORY NO. 07-1889

3/31/2015 1:41:17 S:\IT\55141\T5\Sigma\work\kg\oups\51g\_Mon\ Peterson\071889\_sml.ele\_xxx.dgn J Peterson