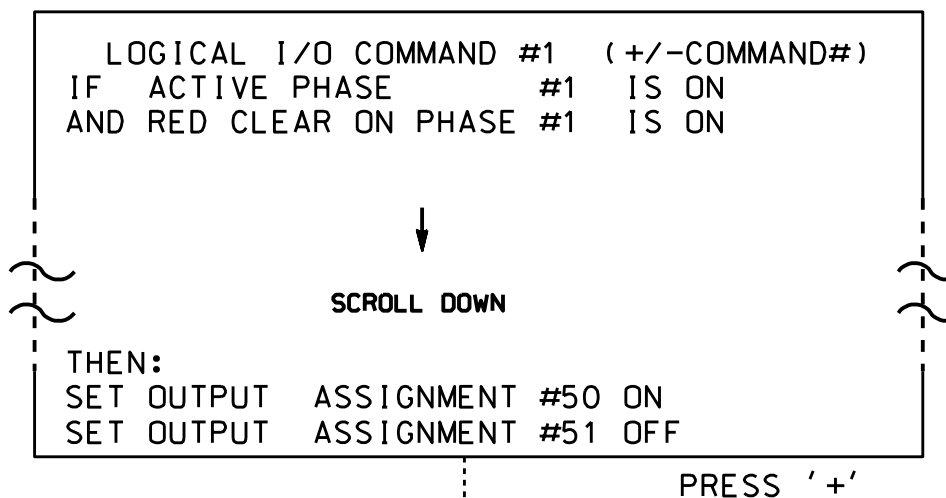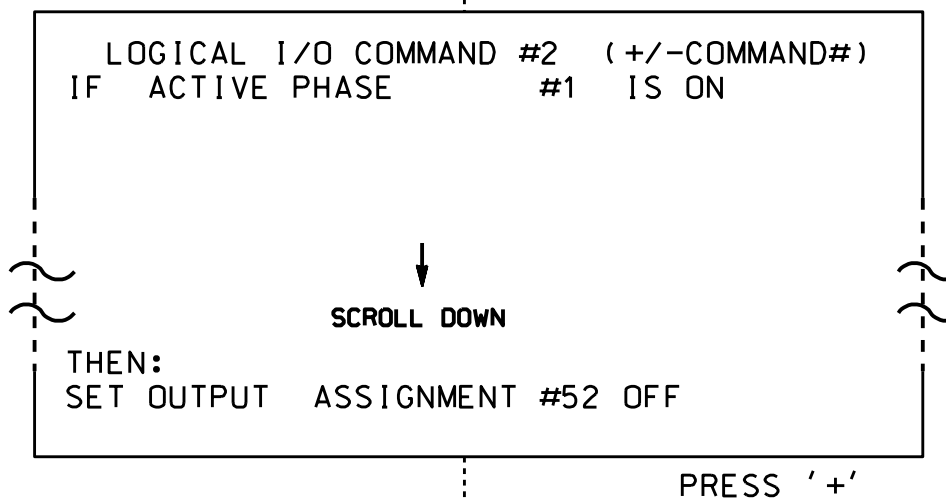## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO
PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE
AND FOR MICROWAVE DETECTOR INPUT PROCESSING
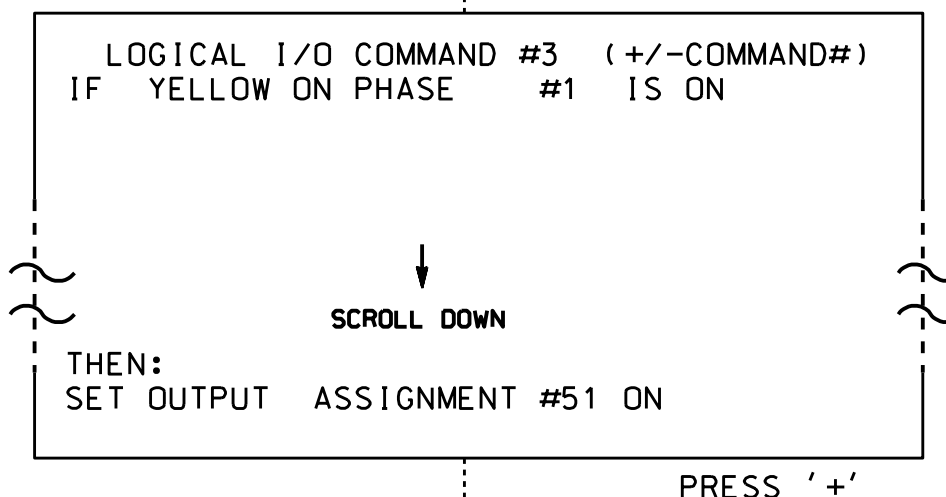
(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, AND 5.

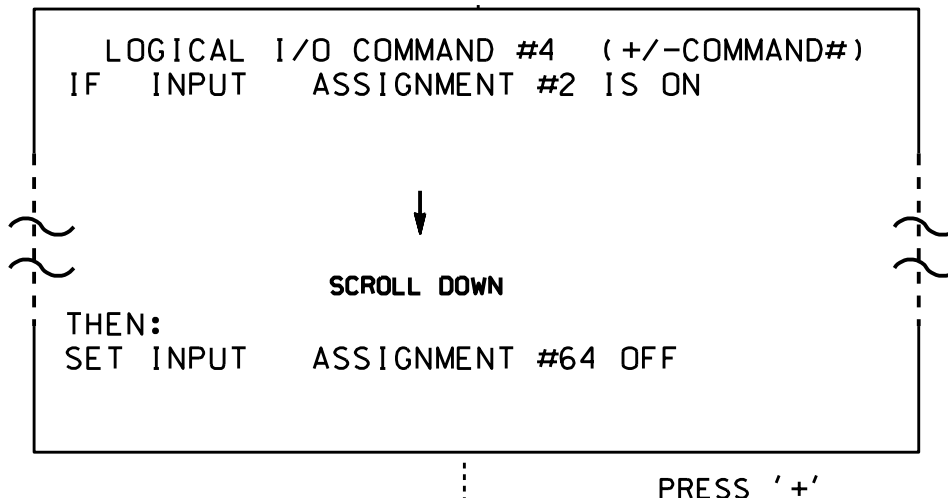2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE      #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON


         ↓
      SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
                       PRESS '+'
```
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE      #1  IS ON


         ↓
      SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
                       PRESS '+'
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE   #1  IS ON


         ↓
      SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #51 ON

                       PRESS '+'
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  INPUT    ASSIGNMENT #2 IS ON


         ↓
      SCROLL DOWN

THEN:
SET INPUT    ASSIGNMENT #64 OFF
                       PRESS '+'
```
NOTE: DETECTOR CONTACTS ARE CLOSED, SO NO CALL IS 'DETECTED'.
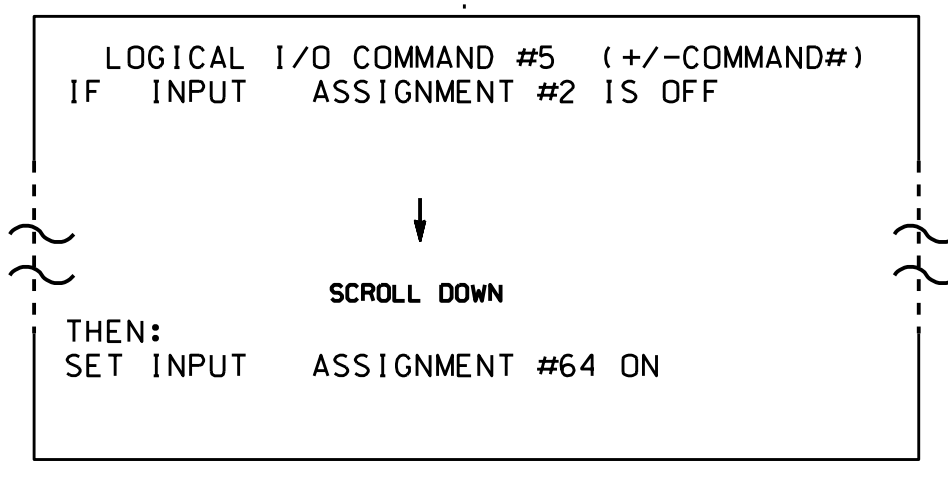
```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  INPUT    ASSIGNMENT #2 IS OFF


         ↓
      SCROLL DOWN

THEN:
SET INPUT    ASSIGNMENT #64 ON
```
NOTE: DETECTOR CONTACTS ARE OPEN, SO A CALL IS 'DETECTED'.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

```
INPUT   2 = Detector Physical Input (not enabled)
INPUT  64 = Dummy Detector Input (Detector 6)
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green
```

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

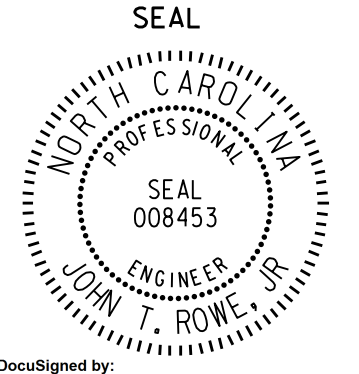FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE:        123456789101112131415116
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN

SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1578
DESIGNED: March 2014
SEALED: 4/10/15
REVISED: N/A
```

Electrical Detail - Sheet 2 of 3