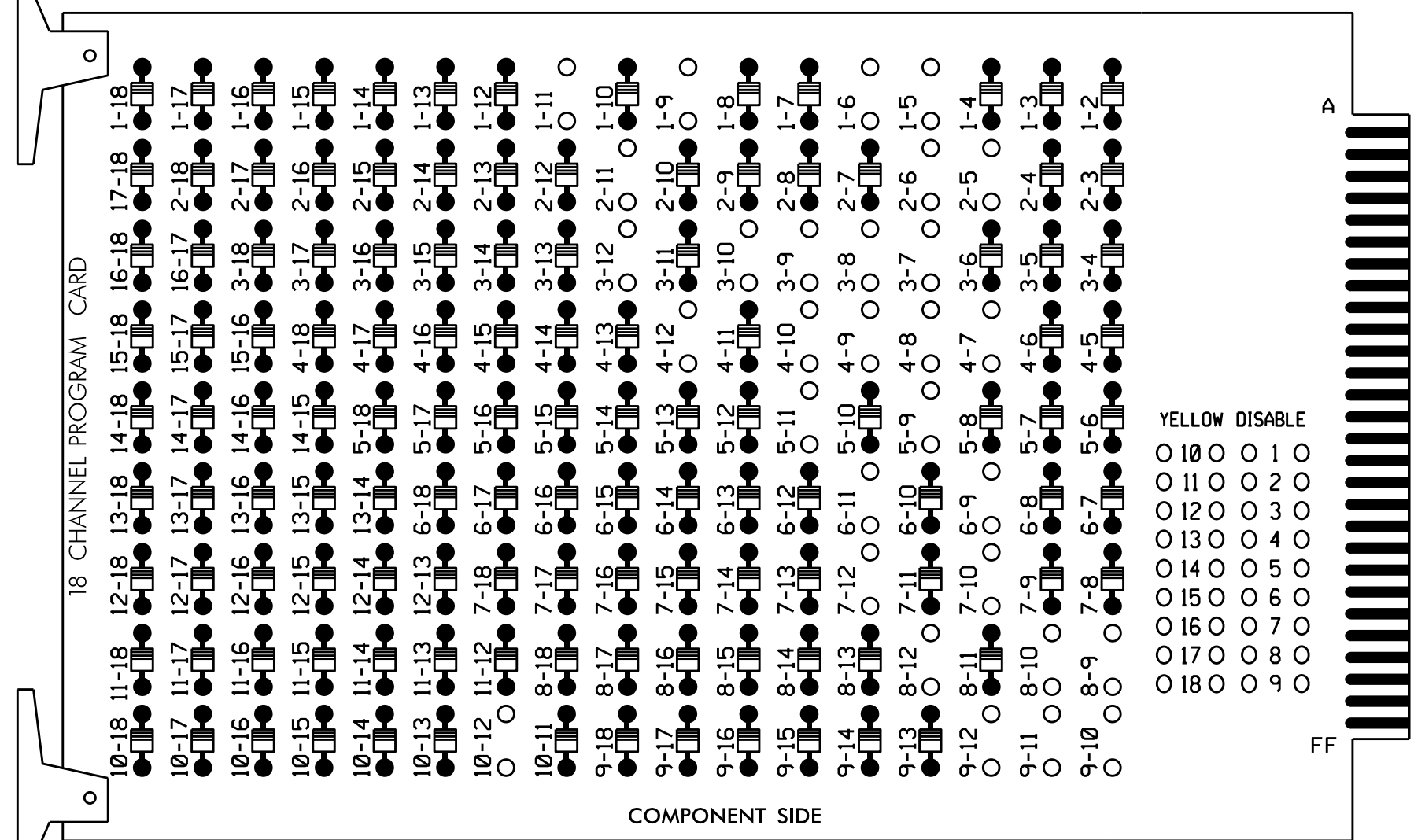


**EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

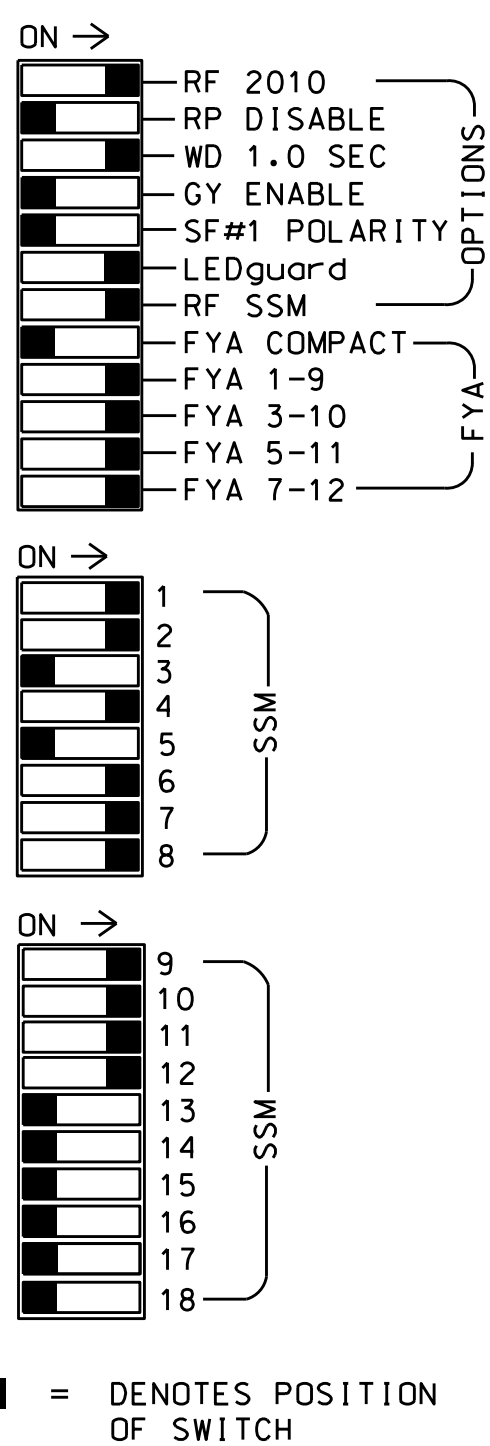
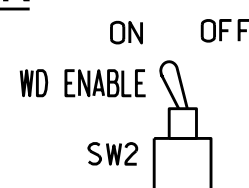
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-11, 3-7, 3-8, 3-9, 3-10, 3-12, 4-7, 4-8, 4-9, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-9, 8-10, 8-12, 9-10, 9-11, 9-12 and 10-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,AUX S1,  
 AUX S2,AUX S4,AUX S5  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAP "A".....1+8  
 OVERLAP "B".....3+4  
 OVERLAP "C".....5+6  
 OVERLAP "D".....7+8

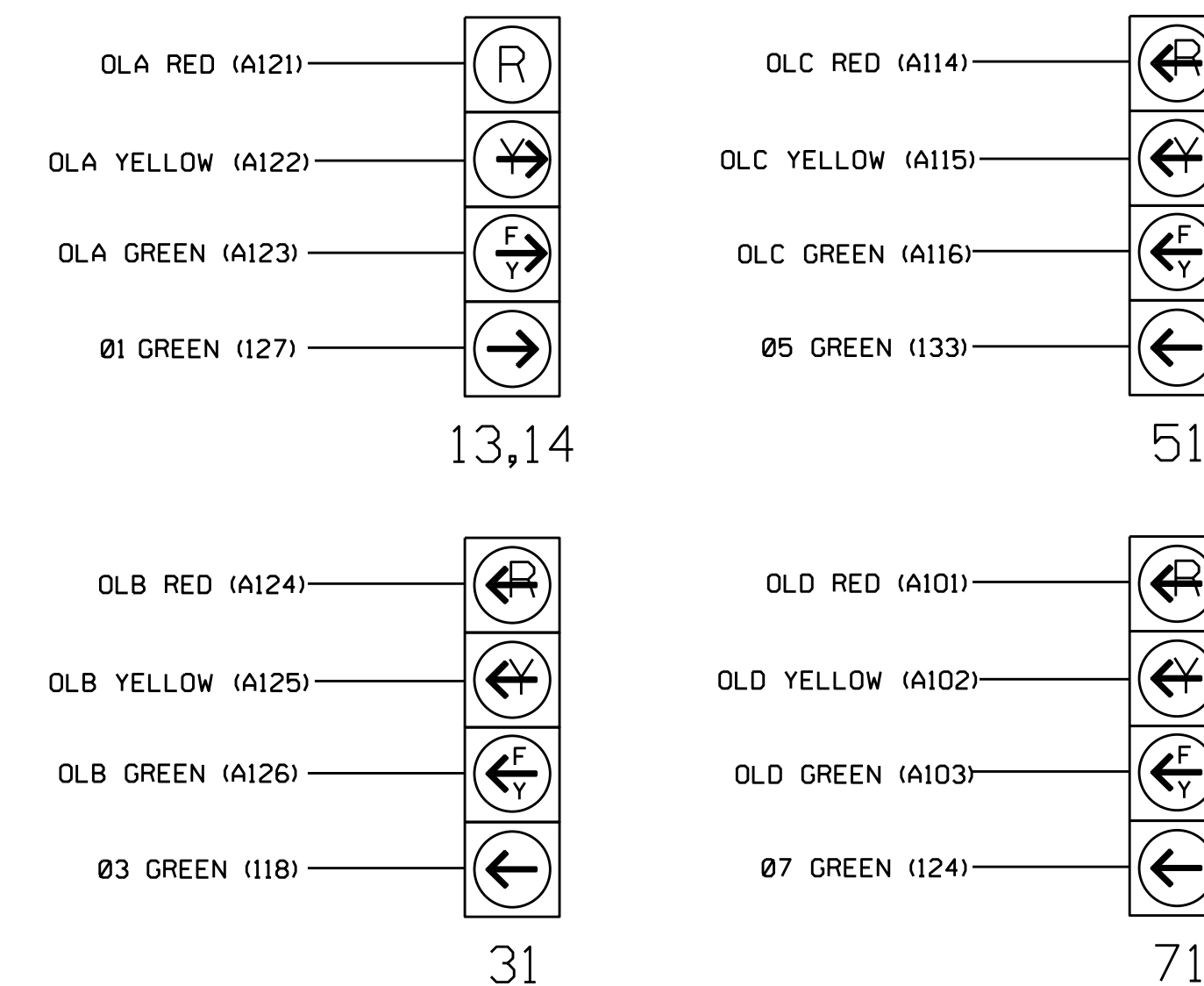
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11,12	13,14	22,23	NU	31	41,42	NU	51	61, 62,63	NU	63	71	81,84	NU	13,14	31	NU	51	71
RED		128			101			134		*		107							
YELLOW		129		*	102		*	135				108							
GREEN		130			103			136				109							
RED ARROW	125												A121	A124		A114	A101		
YELLOW ARROW	126										123		A122	A125		A115	A102		
FLASHING YELLOW ARROW													A123	A126		A116	A103		
GREEN ARROW	127	127			118			133		124	124								

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

**4 SECTION FYA PPLT SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

- The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	1A	1C	2A,2B	3A	4B	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A
U	∅ 1	∅ 1	NOT USED	NOT USED	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	1B	1D	NOT USED	NOT USED	4C	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A
U	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	∅ 18
L	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A	14A	15A	16A	17A	18A
U	NOT USED	NOT USED	NOT USED	NOT USED	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17
L	NOT USED	NOT USED	NOT USED	NOT USED	8B	9A	10A	11A	12A	13A	14A	15A	16A	17A

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

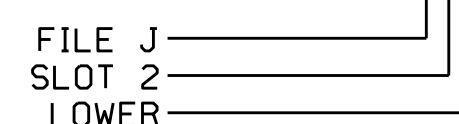
**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	J1U	56	18	1	1	Y	Y			
1B	TB2-3,4	J1L	56	18	1	1	Y	Y			
1C	TB2-5,6	J2U	39	1	2	1	Y	Y			15
1D	TB2-7,8	J2L	43	5	12	1	Y	Y			15
2A,2B	TB2-9,10	J3U	63	25	32	2	Y	Y			
3A <sup>1</sup>	TB4-5,6	J5U	58	20	3	3	Y	Y			15
	-	J8U	50	12	28	8	Y	Y			
4B	TB4-9,10	J6U	41	3	4	4	Y	Y			
4C	TB4-11,12	J6L	45	7	14	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	J4U	47	9	22	2	Y	Y	Y		3
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A <sup>3</sup>	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	J8U	49	11	24	4	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
* S1	TB6-9,10	J9U	60	22	11	SYS					
* S2	TB6-11,12	J9L	62	24	13	SYS					

\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

- Add jumper from J5-W to J8-W, on rear of input file.
- Add jumper from J1-W to J4-W, on rear of input file.
- Add jumper from J5-W to J8-W, on rear of input file.

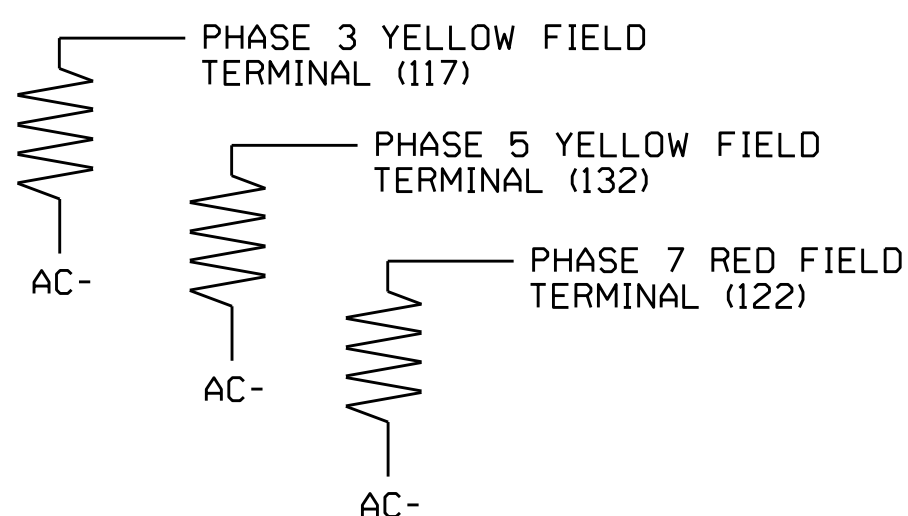
INPUT FILE POSITION LEGEND: J2L



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1527  
 DESIGNED: September 2014  
 SEALED: 4/22/2015  
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

Prepared In the Offices of:  
 Transportation Mobility and Safety  
 Division 7 Guilford County High Point  
 PLAN DATE: September 2014 REVIEWED BY: T. Joyce  
 PREPARED BY: C. Strickland REVIEWED BY:  
 REVISIONS INIT. DATE  
 SEAL  
 NC 68 (Eastchester Drive) at SR 1536 (Penny Road)  
 SEAL 022013  
 GEORGE C. BROWN  
 4/29/2015  
 SIG. INVENTORY NO. 07-1527