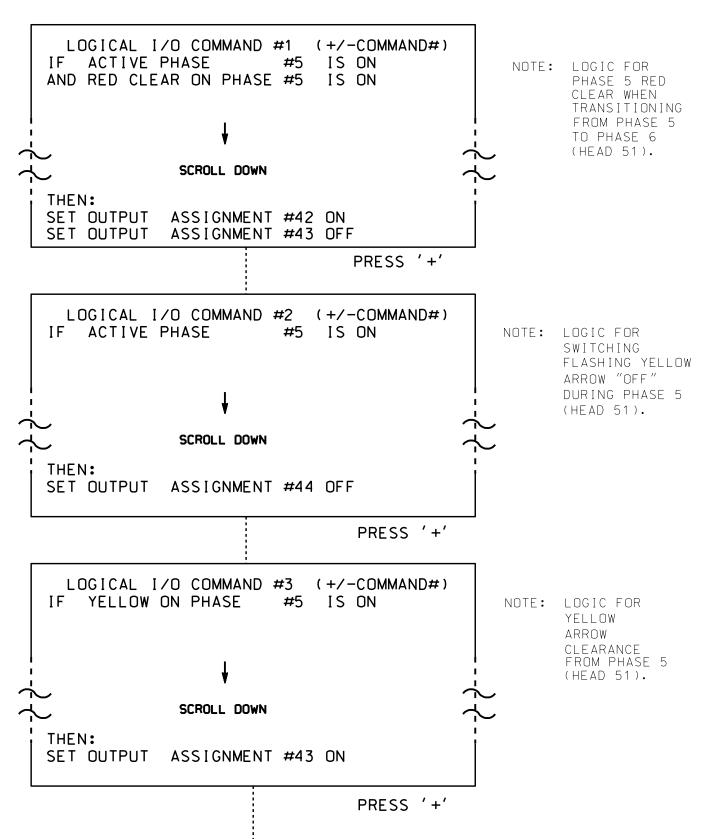
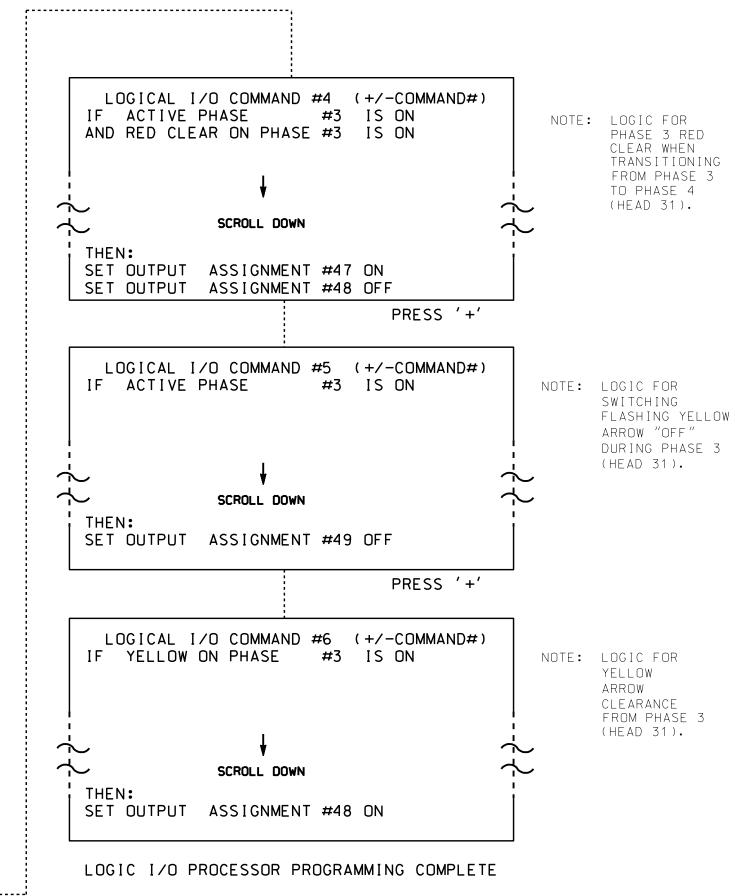
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

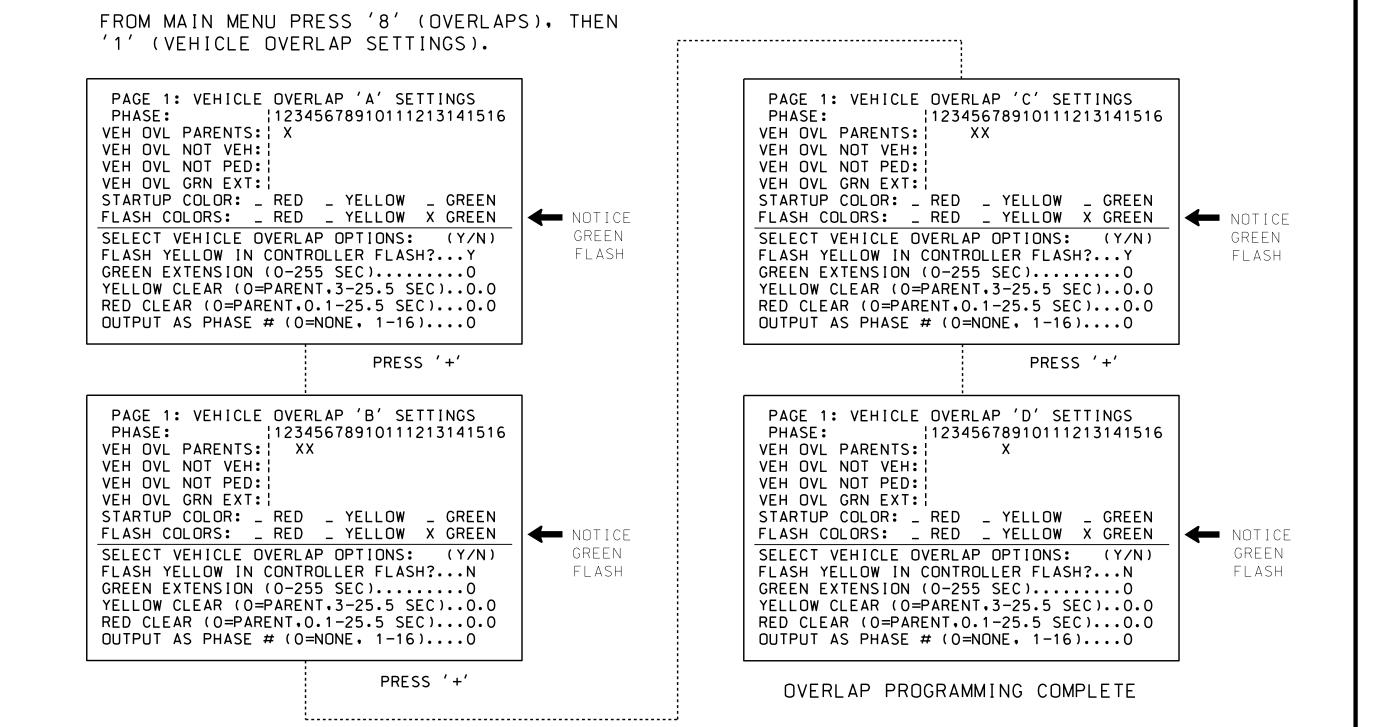




OUTPUT REFERENCE SCHEDULE OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

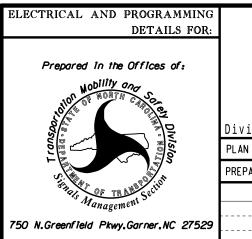
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1483 DESIGNED: July 2014 SEALED: 4/20/2015 REVISED: N/A

PROJECT REFERENCE NO.

C-5558

|Sig. 121.2

Electrical Detail - Sheet 2 of 2



Brentwood Street SR 1300 (E. Green Drive)

ivision 7 Guilford Countv PLAN DATE: September 2014 REVIEWED BY: QTR

PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE

John T. Rowe, Jr. 4/21/2015 SIG. INVENTORY NO. 07-1483

SEAL

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