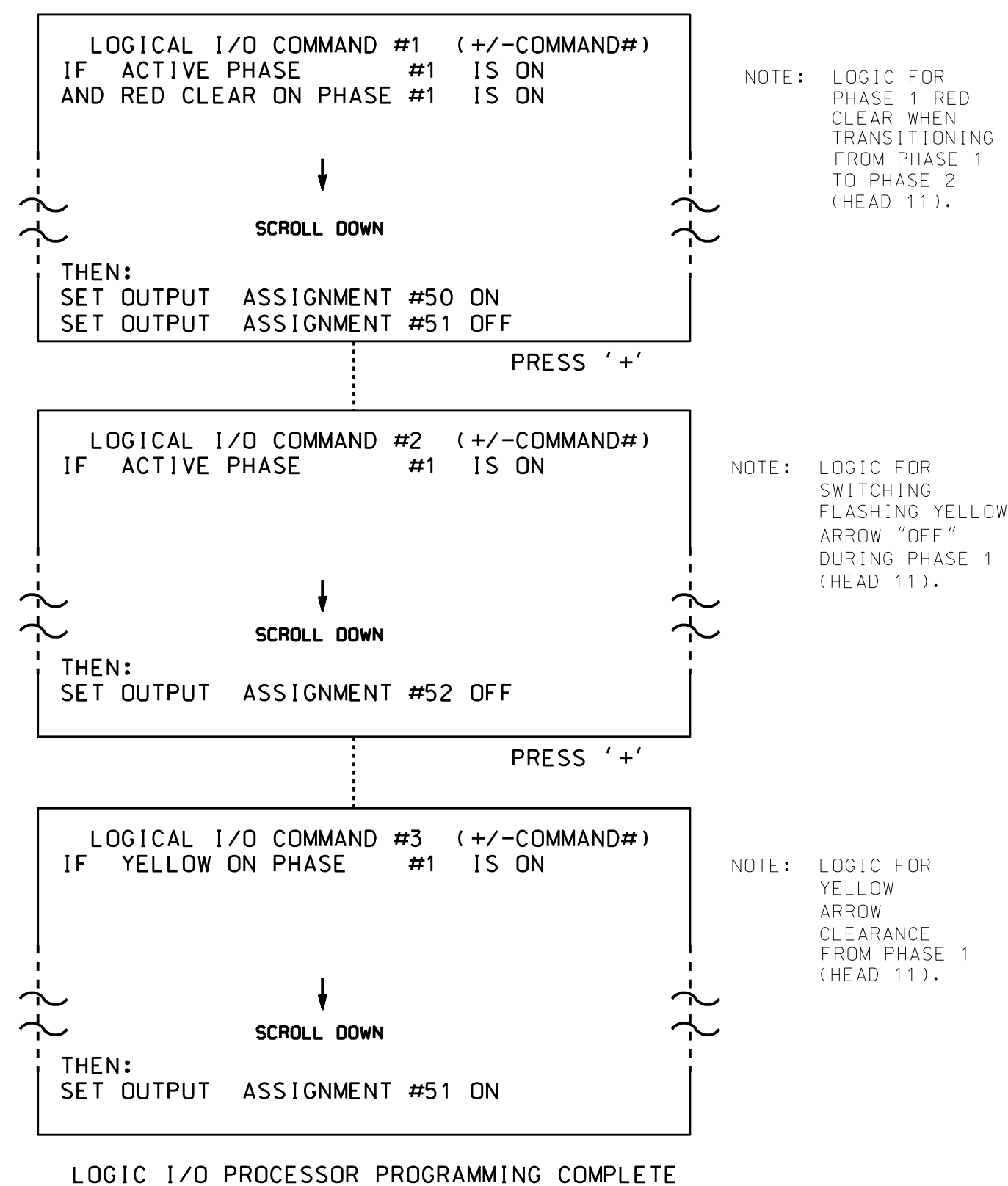


## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



<b>OUTPUT REFERENCE SCHEDULE</b>
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

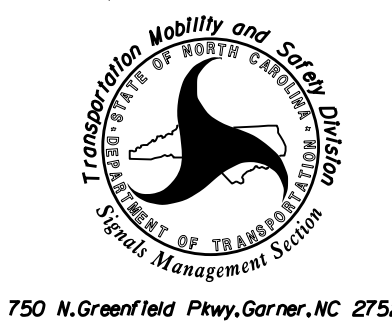
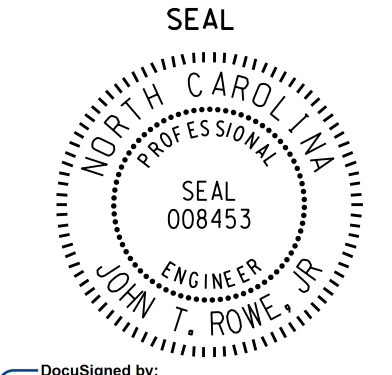
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS	
PHASE:	12345678910111213141516
VEH OVL PARENTS:	XX
VEH OVL NOT VEH:	
VEH OVL NOT PED:	
VEH OVL GRN EXT:	
STARTUP COLOR:	_ RED _ YELLOW _ GREEN
FLASH COLORS:	_ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS:	(Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...	Y
GREEN EXTENSION (0-255 SEC).....	0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..	0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)..	0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....	0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1461
DESIGNED: April 2014
SEALED: 3-31-15
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

<p>Prepared In the Offices of:</p>  <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p><b>SR 1486 (W. Main Street) at SR 1332 (Scientific Street)</b></p> <p>Division 7 Guilford County, NC          PLAN DATE: August 2014    REVIEWED BY: <i>JTR</i>          PREPARED BY: James Peterson    REVIEWED BY: _____</p> <table border="1" style="width: 100%; text-align: center;"> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </table>	REVISIONS	INIT.	DATE				<p>SEAL</p>  <p>DocuSigned by: <i>John T. Rowe, Jr.</i> 4/10/2015 DATE</p> <p>SIG. INVENTORY NO. 07-1461</p>
REVISIONS	INIT.	DATE						

08-1461-2015\_08-19  
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J. Peterson