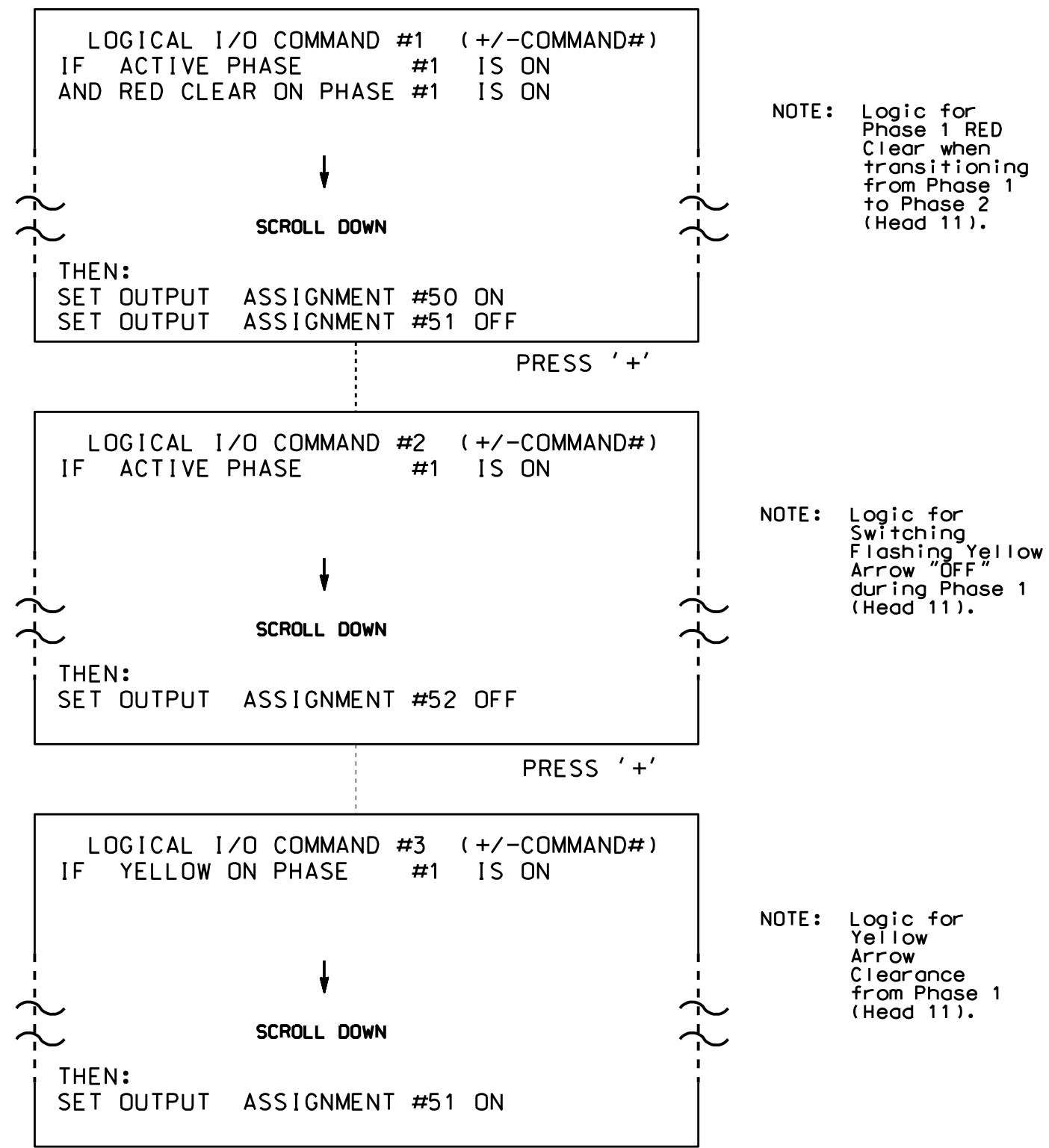


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



NOTE: Logic for Phase 1 RED Clear when transitioning from Phase 1 to Phase 2 (Head 11).

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 1 (Head 11).

NOTE: Logic for Yellow Arrow Clearance from Phase 1 (Head 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: | X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1437
DESIGNED: May 2014
SEALED: 4/7/2015
REVISED: N/A

Electrical Detail - Sheet 2 of 2

	ELECTRICAL AND PROGRAMMING DETAILS FOR:	SR 1486/4121 (Main Street) at SR 1352 (Oakdale Road)	SEAL
	Division 7 Guilford County Jamestown	PLAN DATE: September 2014 REVIEWED BY: T. Joyce	PREPARED BY: C. Strickland REVIEWED BY:
REVISIONS		INIT. DATE	SIG. INVENTORY NO. 07-1437

I:\0486-2014_1352_Sig\1352SIG\1352SIG.dwg
 S:\1352SIG\1352SIG.dwg
 C:\Users\cstrickland\Documents\1352SIG.dwg