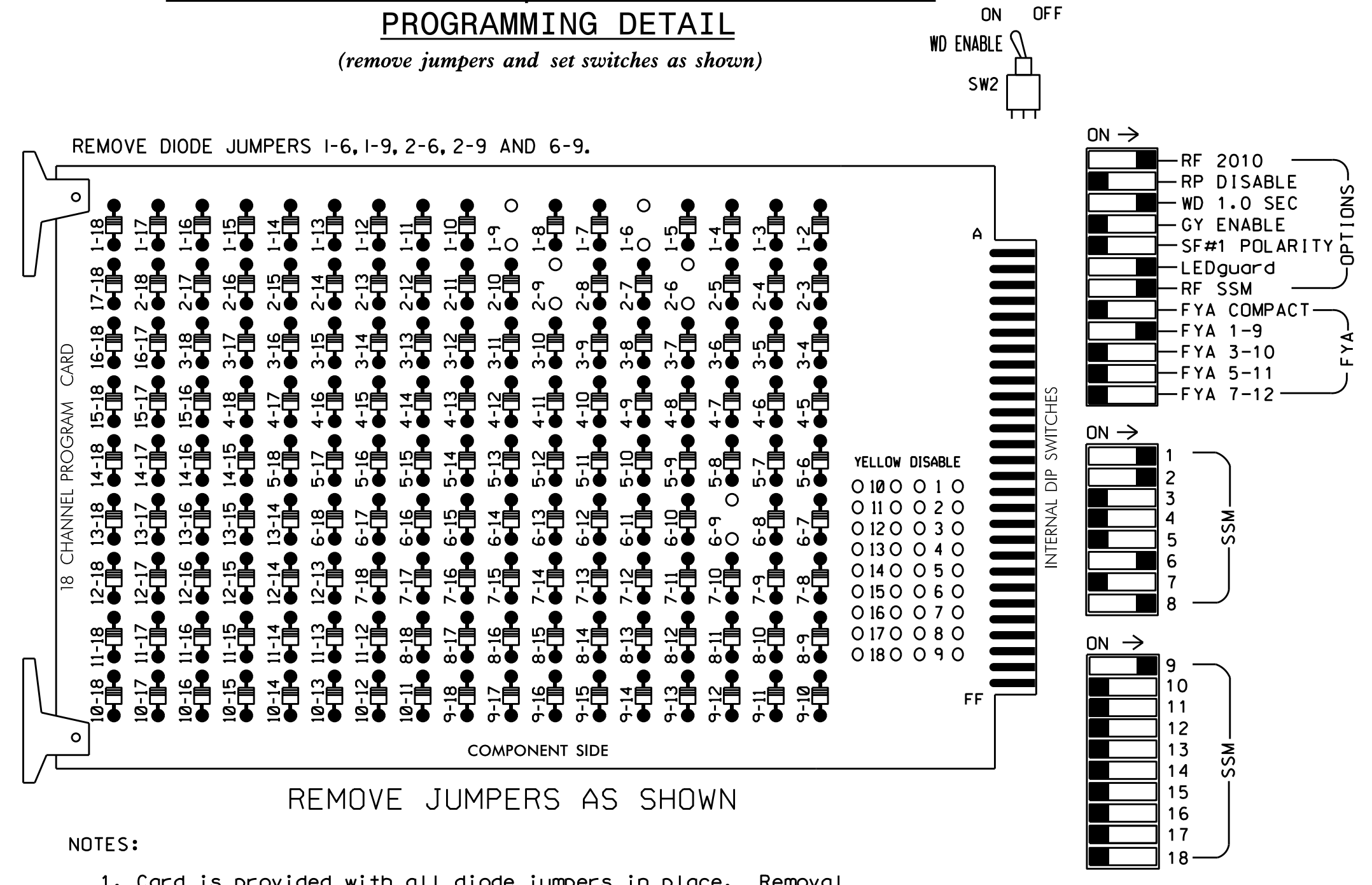


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR
PROGRAMMING DETAIL
(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Integrate monitor with Ethernet network in cabinet.

- NOTES**
- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
 - Enable Simultaneous Gap-Out for all phases.
 - Program phases 2 and 6 for Variable Initial and Gap Reduction.
 - Program phases 2 and 6 for Start Up In Green.
 - Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
 - The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S1.
 PHASES USED.....1,2,6,8.
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

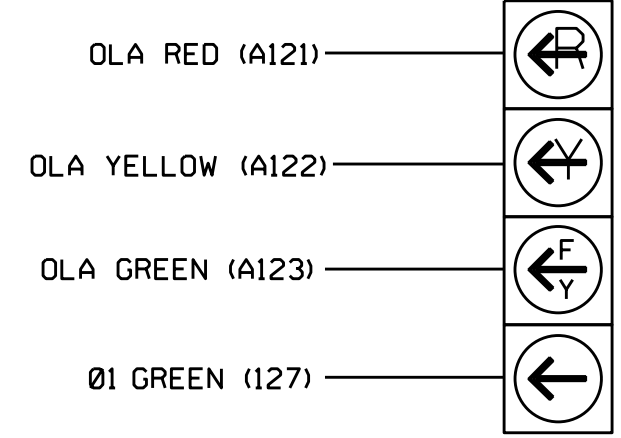
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	NU	NU	NU	NU	62,63	NU	NU	81,82	NU	11	NU	NU	NU	NU	NU
RED	*		128					134			107							
YELLOW			129					135			108							
GREEN			130					136			109							
RED ARROW													A121					
YELLOW ARROW			126										A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127	127																

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



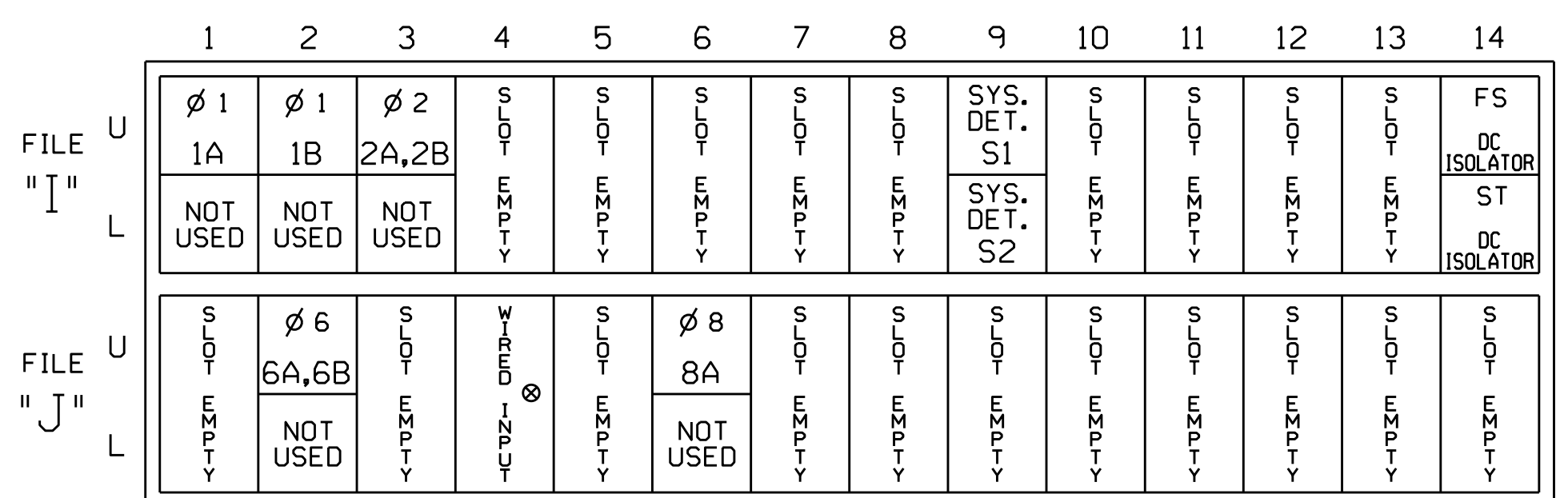
11

NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

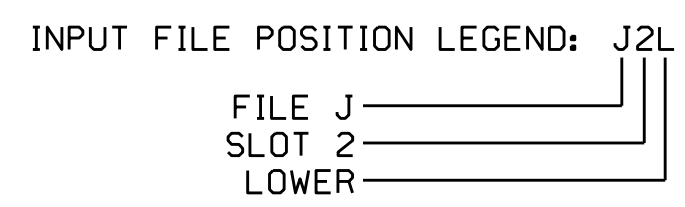
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

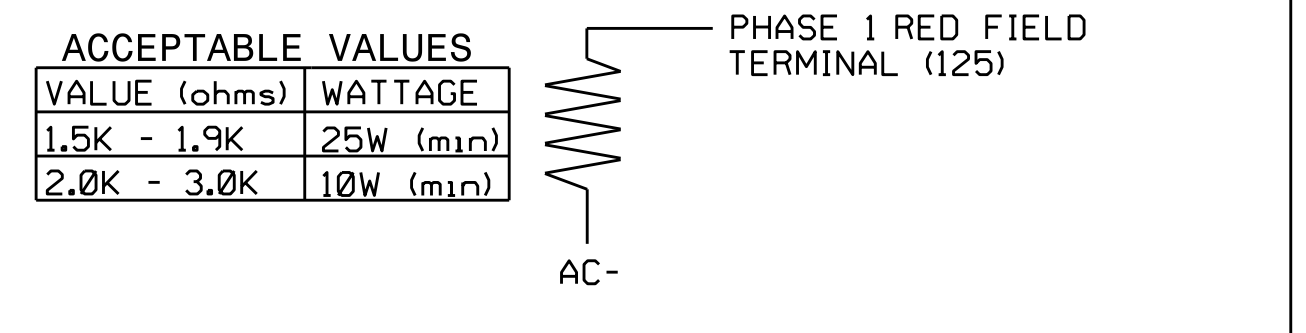
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
1B	TB2-5,6	I2U	48	10	26	6	Y	Y	Y		3
2A,2B	TB2-9,10	I3U	39	1	2	1	Y	Y			15
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3

- ¹Add jumper from I1-W to J4-W, on rear of input file.
 * System detector only. Remove the vehicle phase assigned to this detector in the default programming.



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



ELECTRICAL DETAIL SHEET 1 OF 2

Electrical and Programming Details For: **NC 68 (Westchester Drive) at Phillips Avenue**

Division 7 Guilford County High Point

PLAN DATE: November 2014 REVIEWED BY: *JTR*

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS: _____ INIT. DATE

DocuSigned by: **John T. Rowe, Jr.** 3/17/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 07-1374

17-MAR-2015 14:17
 S:\IT\SSD\175_Sig\175\work\pdp\sig\Map\ Peterson\071374_sml.ele_xxx.dgn
 J. Peterson