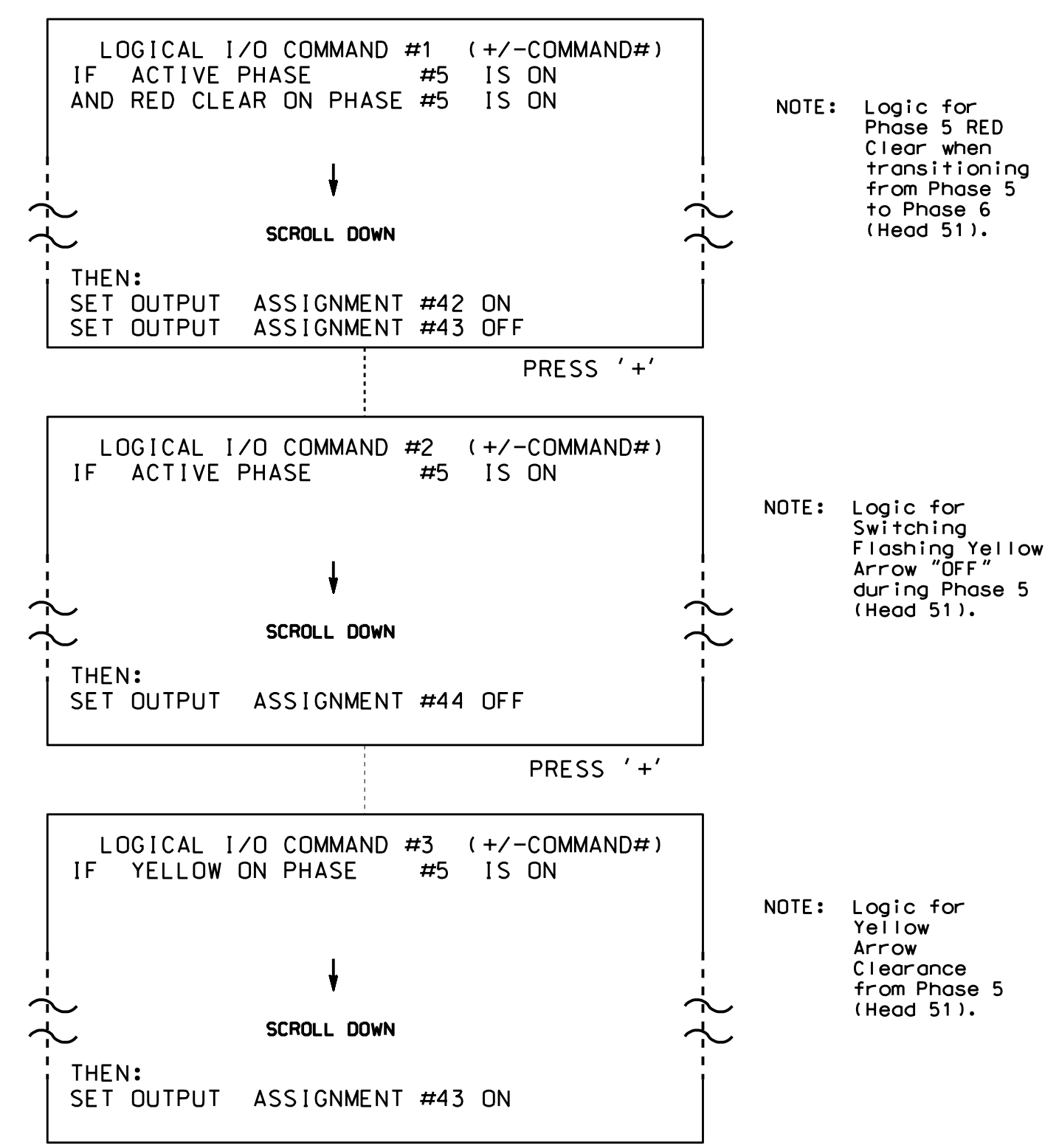


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS), Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

| OUTPUT REFERENCE SCHEDULE | |
|---------------------------|--------------------|
| OUTPUT 42 | = Overlap C Red |
| OUTPUT 43 | = Overlap C Yellow |
| OUTPUT 44 | = Overlap C Green |

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

    PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
    PHASE:      12345678910111213141516
    VEH OVL PARENTS: XX
    VEH OVL NOT VEH:
    VEH OVL NOT PED:
    VEH OVL GRN EXT:
    STARTUP COLOR: _ RED _ YELLOW _ GREEN
    FLASH COLORS:  _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0.0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1363
DESIGNED: June 2014
SEALED: 3/26/15
REVISED: N/A

Electrical Detail - Sheet 2 of 2

| | | | |
|---|---|--|--|
| ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: | Brentwood Street at I-85 Bus./US 29 SB-US 70 WB Ramps | | SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN |
| | Division 7 PLAN DATE: July 2014 PREPARED BY: B. SIMMONS | GUILFORD COUNTY High Point REVIEWED BY: T. JOYCE REVIEWED BY: | |
| REVISIONS | | INIT. | DATE |
| SIG. INVENTORY NO. 07-1363 | | | |

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