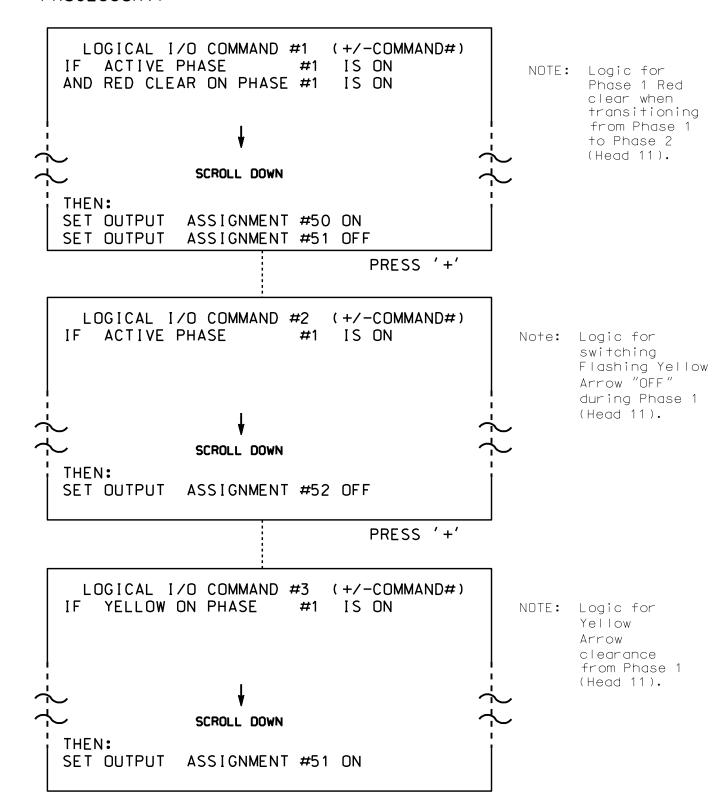
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

## TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- 2. From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

## **OUTPUT REFERENCE SCHEDULE**

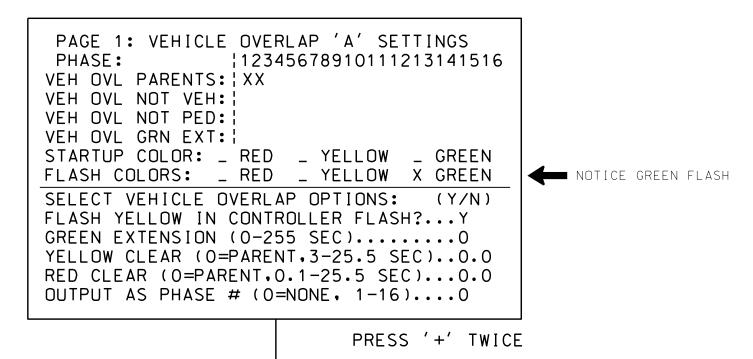
OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

PROJECT REFERENCE NO. Sig. 104.2 C-5558

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).



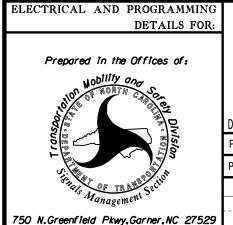
PAGE 1: VEHICLE OVE PHASE:   123 VEH OVL PARENTS:   VEH OVL NOT VEH:   VEH OVL NOT PED:   VEH OVL GRN EXT:   STARTUP COLOR: _ RED FLASH COLORS: _ RED	45678910111213141516 X _ YELLOW _ GREEN	NOTICE GREEN FLASH
SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)0.0 OUTPUT AS PHASE # (0=NONE. 1-16)0		

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1272 DESIGNED: May 2014 SEALED: 4/23/15 REVISED: N/A

REVISIONS

Electrical Detail - Sheet 2 of 2



SR 1486 (Greensboro Road)

Enterprise Dr./Spencer Street

ivision 7 Guilford County High Point PLAN DATE: May 2014 REVIEWED BY: T. Joyce PREPARED BY: B. SIMMONS REVIEWED BY:

INIT. DATE

SIG. INVENTORY NO. 07-1272