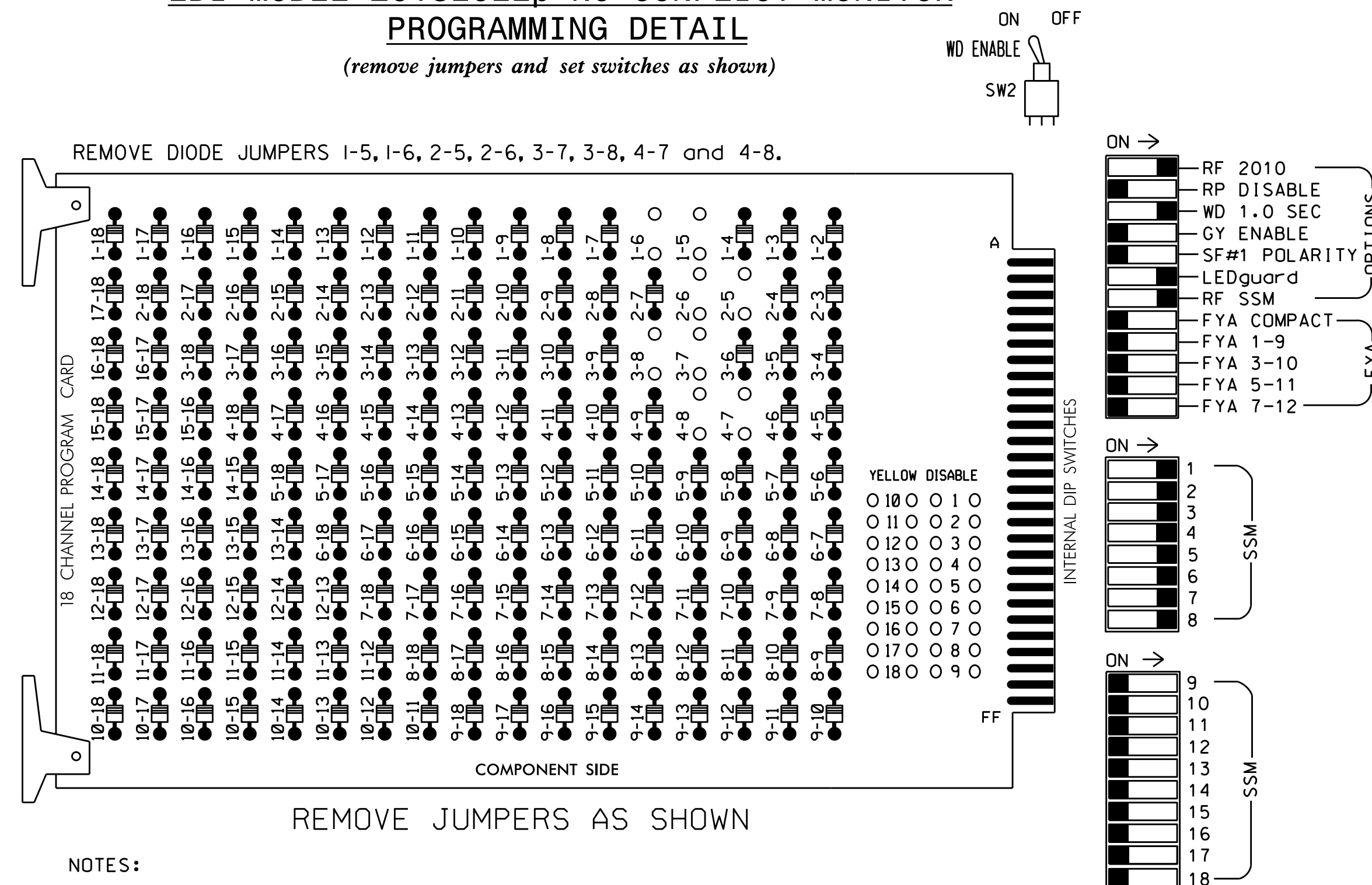


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

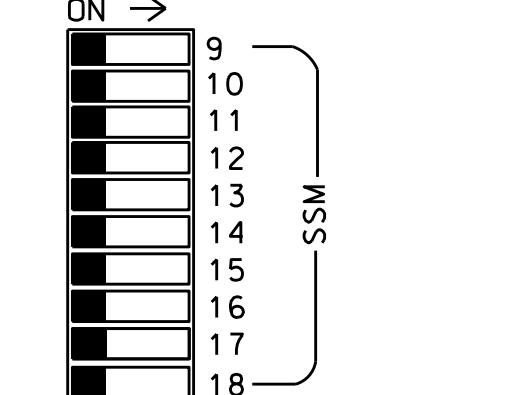
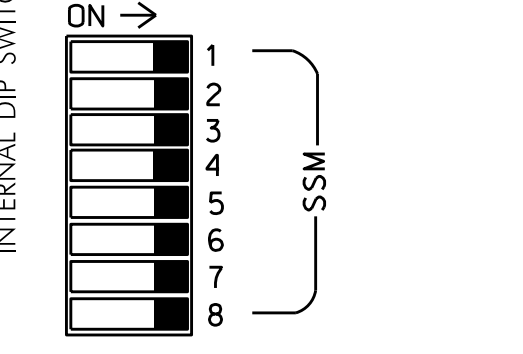
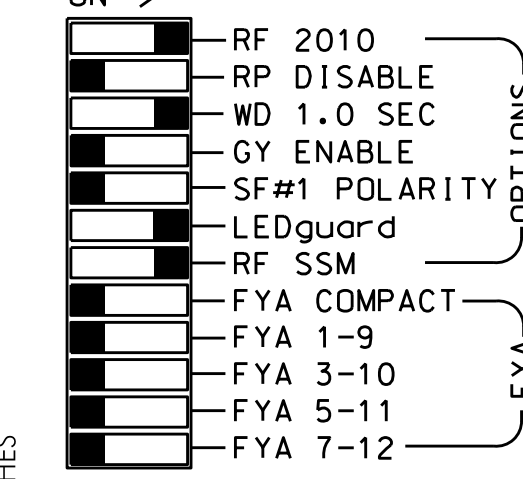


REMOVE DIODE JUMPERS 1-5, 1-6, 2-5, 2-6, 3-7, 3-8, 4-7 and 4-8.

REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.
6. The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070L  
 CABINET.....332  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11  
 PHASES USED.....1,2,3,4,5,6,7,8  
 OVERLAPS.....NONE

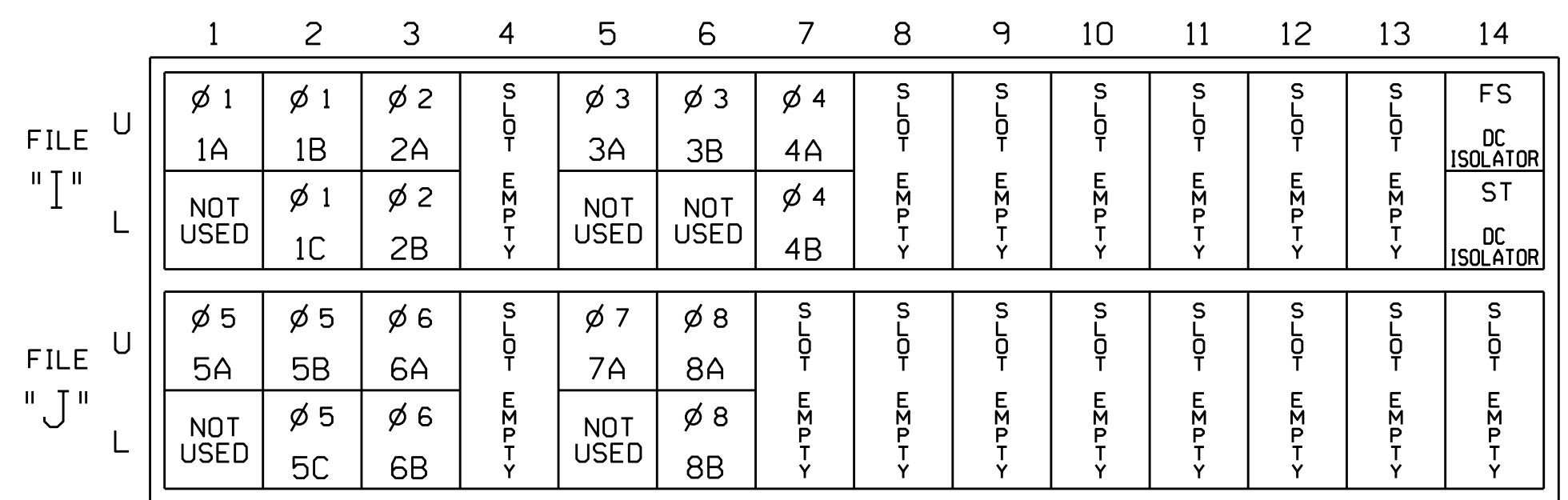
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12			
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16			
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED			
SIGNAL HEAD NO.	11,12	82	21,22	NU	22	31,32	41,42	NU	42	51,52	61,62	NU	71	81,82	NU
RED		128				101				134				107	
YELLOW			129			102				135				108	
GREEN			130			103				136				109	
RED ARROW	125					116			131				122		
YELLOW ARROW	126	126			117	117			132	132			123		
GREEN ARROW	127	127			118	118			133	133			124		

NU = Not Used

**INPUT FILE POSITION LAYOUT**

(front view)



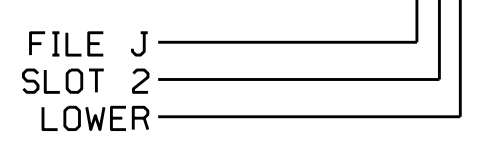
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			
1C	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			
3B	TB4-9,10	I6U	41	3	4	3	Y	Y			
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			
5C	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
7A	TB5-5,6	J5U	57	19	7	7	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

**INPUT FILE POSITION LEGEND: J2L**



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1217  
 DESIGNED: April 2014  
 SEALED: 3-31-15  
 REVISED: N/A

**Electrical Detail**

Electrical and Programming Details For: SR 1546 (Guilford College Road) at SR 1424 (Hilltop Road) and SR 4229 (Piedmont Parkway)

Prepared In the Offices of: **TRANSPO-MOBILITY AND SAFETY SOLUTIONS**

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: JTR

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS: INIT. DATE

DocuSigned by: **John T. Rowe, Jr.** 4/10/2015

SIG. INVENTORY NO. 07-1217

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