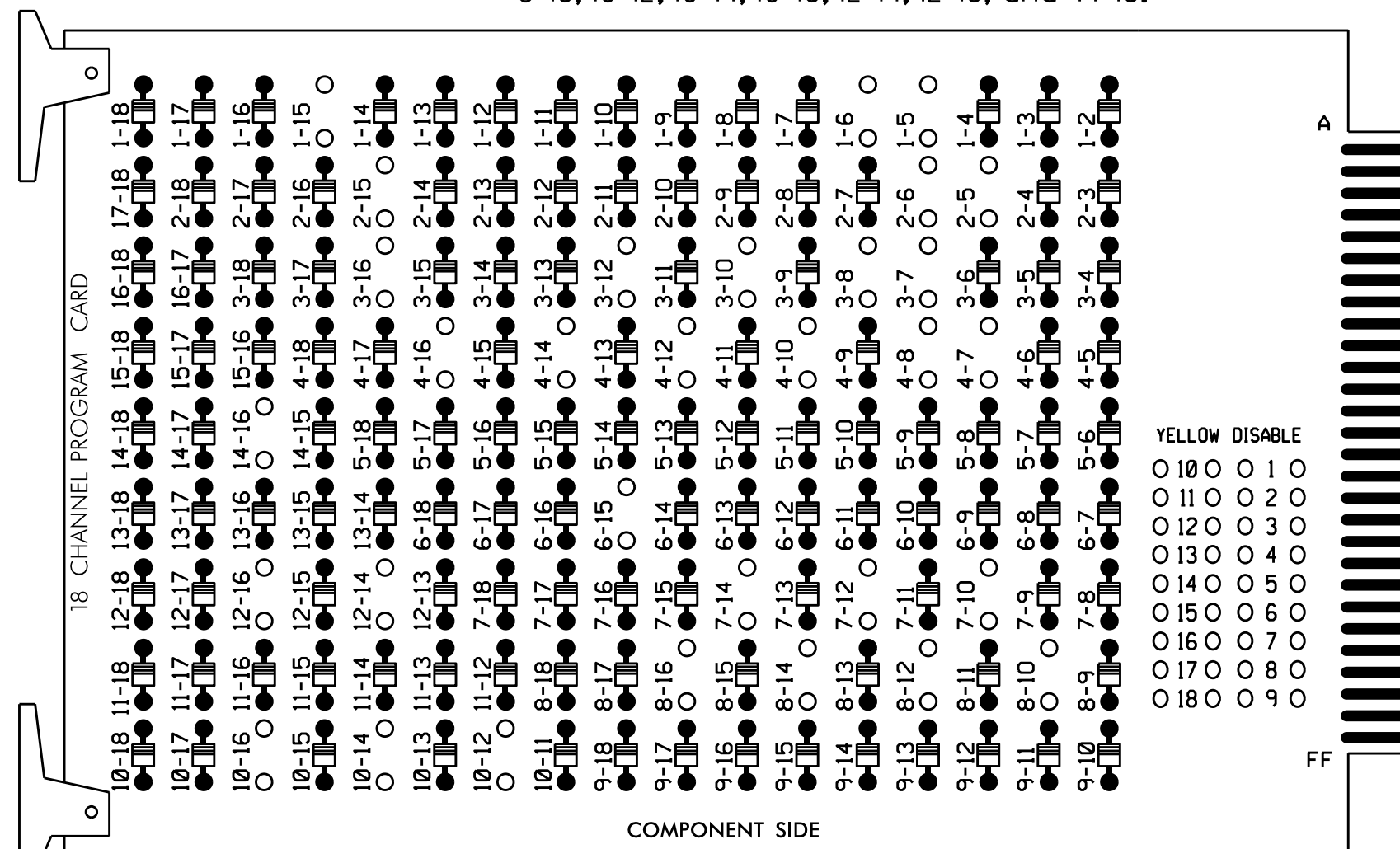


**EDI MODEL 2018EClip-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

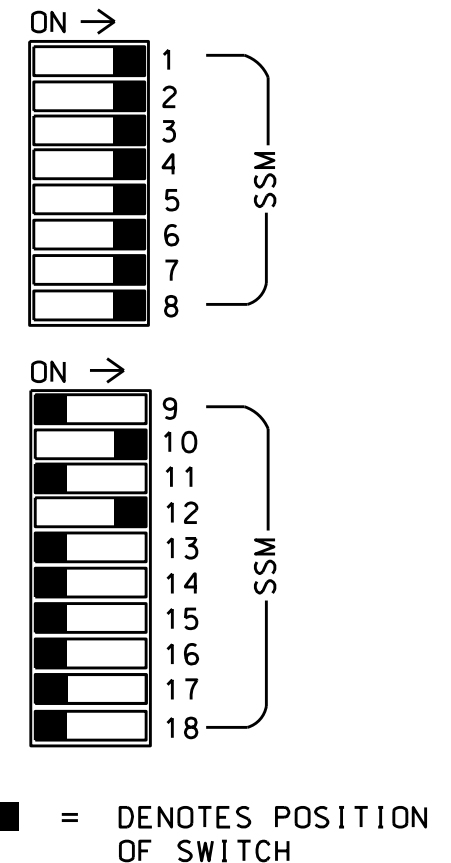
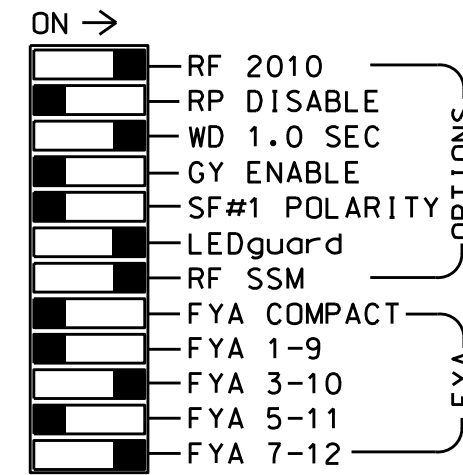
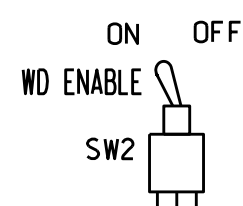
REMOVE DIODE JUMPERS 1-5, 1-6, 1-15, 2-5, 2-6, 2-15, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 10-12, 10-14, 10-16, 12-14, 12-16, and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 4, 6 and 8 for 'STARTUP PED CALL'.
7. Program phases 2 and 6 for Yellow Flash, and overlap 2 as Wag Overlaps.
8. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S6,S7,S8,S9,S10,
 S11,S12,AUX S2,AUX S5
 PHASES USED.....1,2,3,4,4PED,5,6,6PED,7,8,8PED
 OVERLAP "A".....NOT USED
 OVERLAP "B".....3+4
 OVERLAP "C".....NOT USED
 OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11,12	21,22	NU	22	31	41,42	P41, P42	51	61,62	P61, P62	62	71	81,82	P81, P82	NU	31	NU	71	NU
RED		128		*	101			134		*	107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW	125							131						A124				A101	
YELLOW ARROW	126			117				132		123				A125				A102	
FLASHING YELLOW ARROW														A126				A103	
GREEN ARROW	127			118	118			133		124	124								
Hand icon								104		119			110						
Person icon								106		121			112						

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(from view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅1	∅2	∅3	∅4	∅5	∅6	∅7	∅8	∅9	∅10	∅11	∅12	∅13	∅14
L	1A	2A,2B	3A	4A	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A	14A
U	∅1	SYS. DET. S1	NOT USED	∅4	∅5	∅6	∅7	∅8	∅9	∅10	∅11	∅12	∅13	∅14
L	1B	S1	NOT USED	4B	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A	14A
U	NOT USED	NOT USED	NOT USED	∅8	∅5	∅6	∅7	∅8	∅9	∅10	∅11	∅12	∅13	∅14
L	NOT USED	NOT USED	NOT USED	8B	5A	6A,6B	7A	8A	9A	10A	11A	12A	13A	14A

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

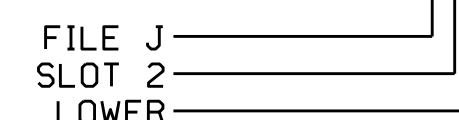
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-5,6	I2U	39	1	2	1	Y	Y			
1B	TB2-7,8	I2L	43	5	12	1	Y	Y			
2A,2B	TB2-9,10	I3U	63	25	32	2	Y	Y			
3A ¹	TB4-5,6	I5U	58	20	3	3	Y	Y			15
		J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A ²	TB5-5,6	J5U	57	19	7	7	Y	Y			15
		I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
* S1	TB2-11,12	I3L	76	38	42	SYS					
* S2	TB6-9,10	I9U	60	22	11	SYS					
* S3	TB6-11,12	I9L	62	24	13	SYS					
* S4	TB7-9,10	J9U	59	21	15	SYS					
* S5	TB7-11,12	J9L	61	23	17	SYS					
PED PUSH BUTTONS											
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- ¹Add jumper from I5-W to J8-W, on rear of input file.
- ²Add jumper from J5-W to I8-W, on rear of input file.

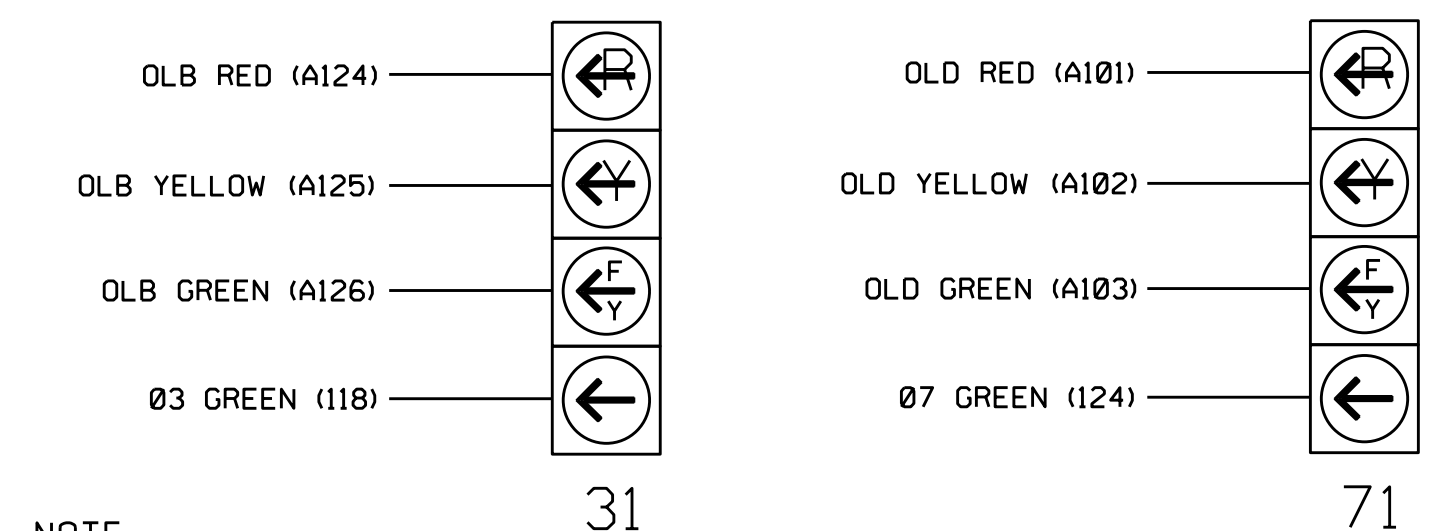
* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

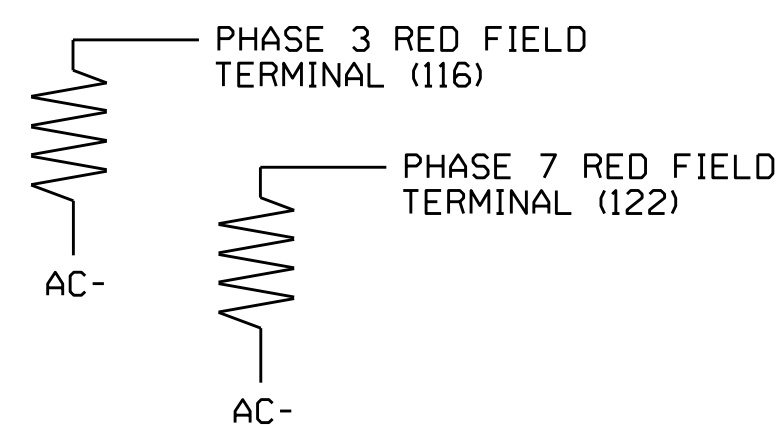
The sequence display for signal heads 31 and 71 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0916
 DESIGNED: July 2014
 SEALED: 4/2/2015
 REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: SR 1278 (N. College Dr.) at SR 1486 (E. Lexington Ave.)

Prepared In the Offices of: **Transporatio Mobility and Safety Solutions**

Division 7 Guilford County High Point

PLAN DATE: September 2014 REVIEWED BY: *[Signature]*

PREPARED BY: S. Armstrong REVIEWED BY: *[Signature]*

REVISIONS: _____ INIT. DATE

DocuSigned by: **John T. Rowe, Jr.** 4/8/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: PROFESSIONAL ENGINEER JOHN T. ROWE, JR. SEAL 008453

SIG. INVENTORY NO. 07-0916