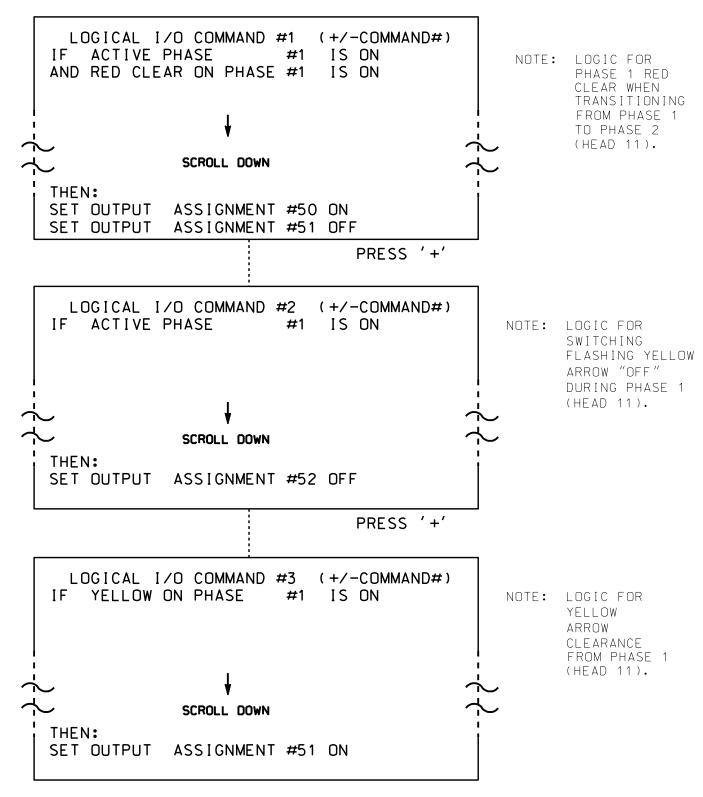
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



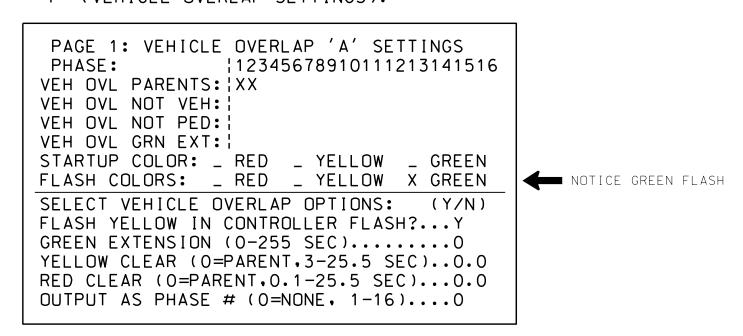
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

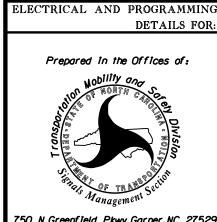
FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0814 DESIGNED: May 2015 SEALED: 5/12/2015 REVISED: N/A

Electrical Detail - Sheet 2 of 2



SR 1486 (E. Lexington Ave.)

Johnson Street

ivision 7 Guilford County _____ High Point PLAN DATE: May 2015 REVIEWED BY: 977 PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS

INIT. DATE John T. Rowe, Jr. 5/12/2015

SEAL

008453

750 N.Greenfield Pkwy.Garner.NC 27529

SIG. INVENTORY NO. 07-0814