

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA SIGNAL SEQUENCE**

*(program controller as shown below)*

- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON

      ↓
      SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
      PRESS '+'
  
```

NOTE: Logic for Phase 1 RED Clear when transitioning from Phase 1 to Phase 2 (Head 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON

      ↓
      SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #52 OFF
      PRESS '+'
  
```

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 1 (Head 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON

      ↓
      SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #51 ON
  
```

NOTE: Logic for Yellow Arrow Clearance from Phase 1 (Head 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

<p><b>OUTPUT REFERENCE SCHEDULE</b></p> <p>OUTPUT 50 = Overlap A Red          OUTPUT 51 = Overlap A Yellow          OUTPUT 52 = Overlap A Green</p>
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**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

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PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:           12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

<p>THIS ELECTRICAL DETAIL IS FOR          THE SIGNAL DESIGN: 07-0809          DESIGNED: August 2014          SEALED: 4/1/15          REVISED: N/A</p>
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Electrical Detail - Sheet 2 of 2

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	<p>Division 7      Guilford County      High Point</p> <p>PLAN DATE: December 2014      REVIEWED BY: T. Joyce</p> <p>PREPARED BY: B. SIMMONS      REVIEWED BY:</p>	<table border="1"> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </table>	REVISIONS	INIT.	DATE			
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