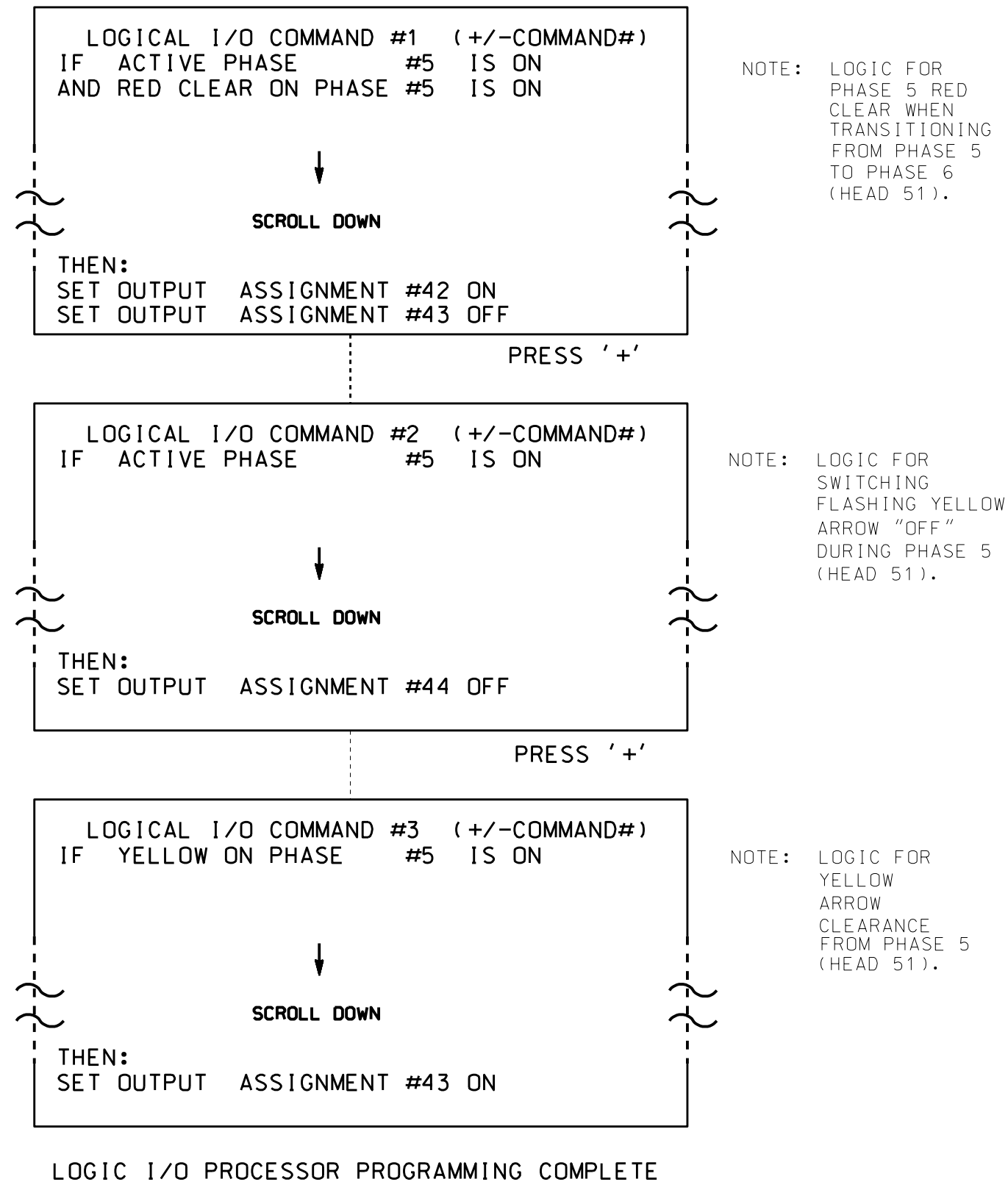


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



**OUTPUT REFERENCE SCHEDULE**

OUTPUT 42	=	Overlap C Red
OUTPUT 43	=	Overlap C Yellow
OUTPUT 44	=	Overlap C Green

**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 07-0803  
DESIGNED: September 2014  
SEALED: 3/27/2015  
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of: 	SR 1009 (South Main Street) at SR 1961 (Market Center Drive)	SEAL 
	Division 7 Guilford County High Point PLAN DATE: November 2014 REVIEWED BY: <i>JTR</i> PREPARED BY: S. Armstrong REVIEWED BY:	REVISIONS INIT. DATE _____ _____ _____

SIC. INVENTORY NO. 07-0803

31-1485-2014 07.rpt  
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