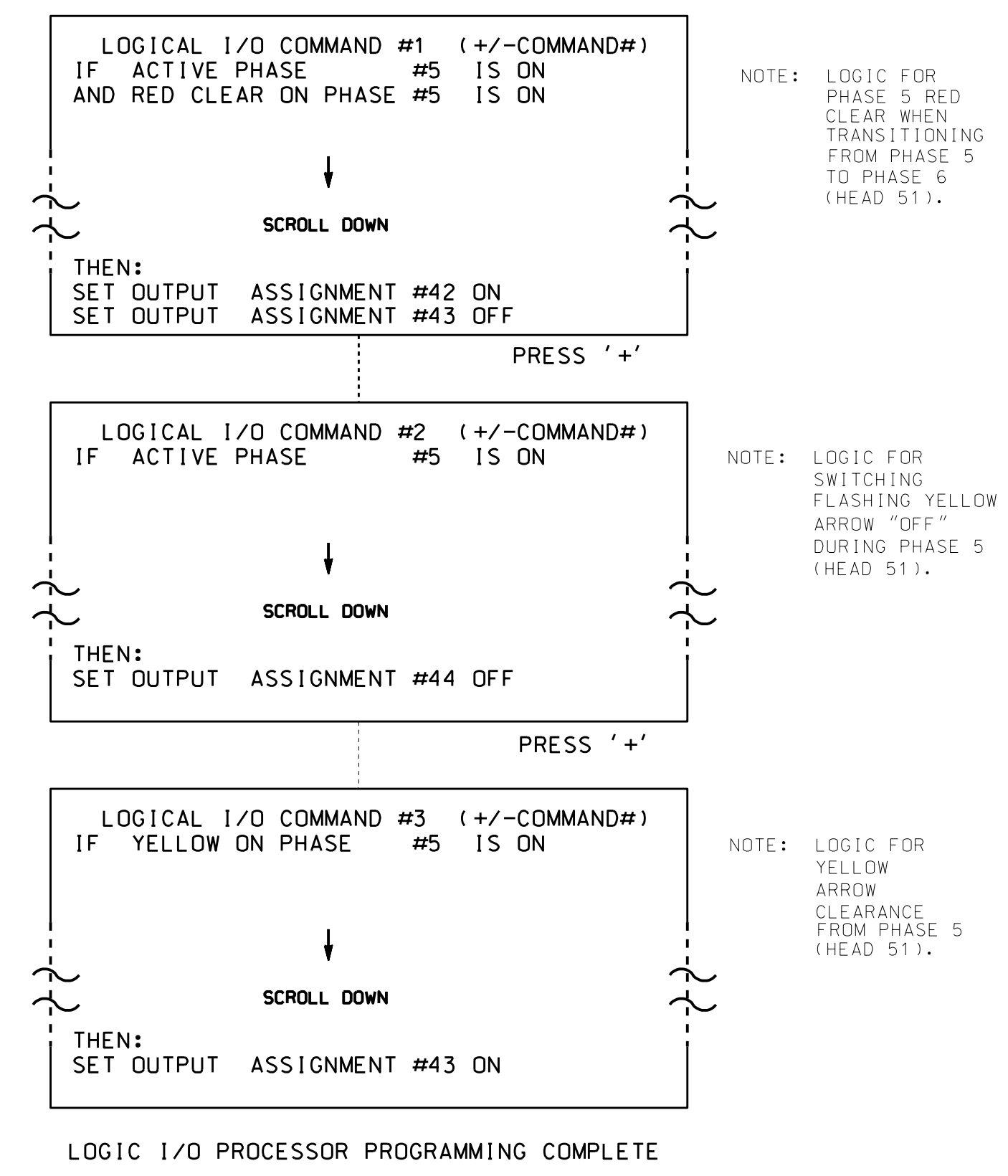


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

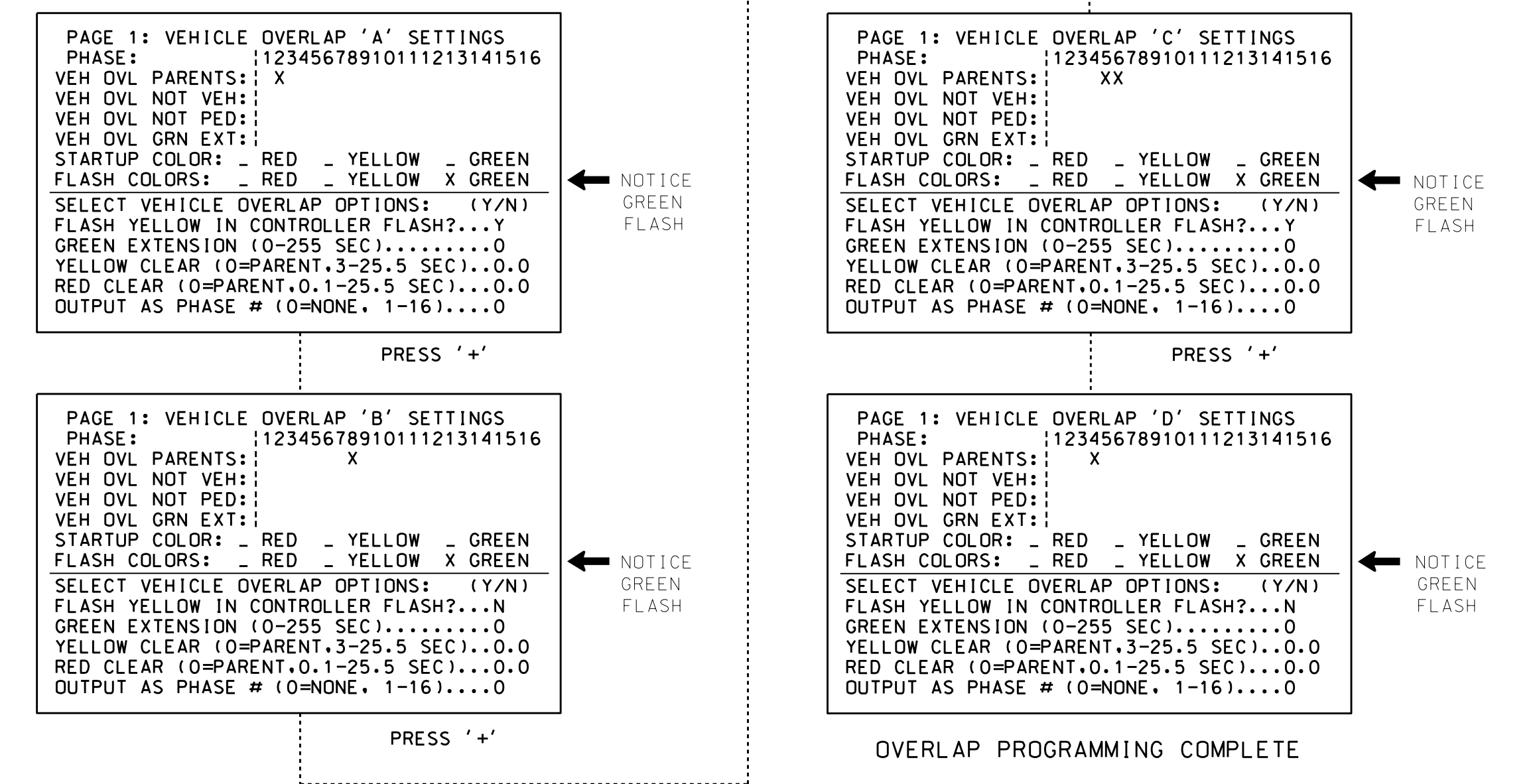
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE	
OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

OVERLAP PROGRAMMING DETAIL (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0783
DESIGNED: January 2015
SEALED: 4/21/2015
REVISED: N/A

Electrical Detail - Sheet 2 of 2

	ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1113 (E. Kivett Dr.) at Centennial Street	SEAL
	Prepared In the Offices of: C. Strickland 750 N. Greenfield Pkwy, Garner, NC 27529	Division 7 Guilford County High Point PLAN DATE: January 2015 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:
REVISIONS INIT. DATE		SIG. INVENTORY NO. 07-0783

09-APR-2015 07:45
 C:\IT\551115\SIG\12801ED09E8434.dgn
 C:\IT\551115\SIG\12801ED09E8434.dgn
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