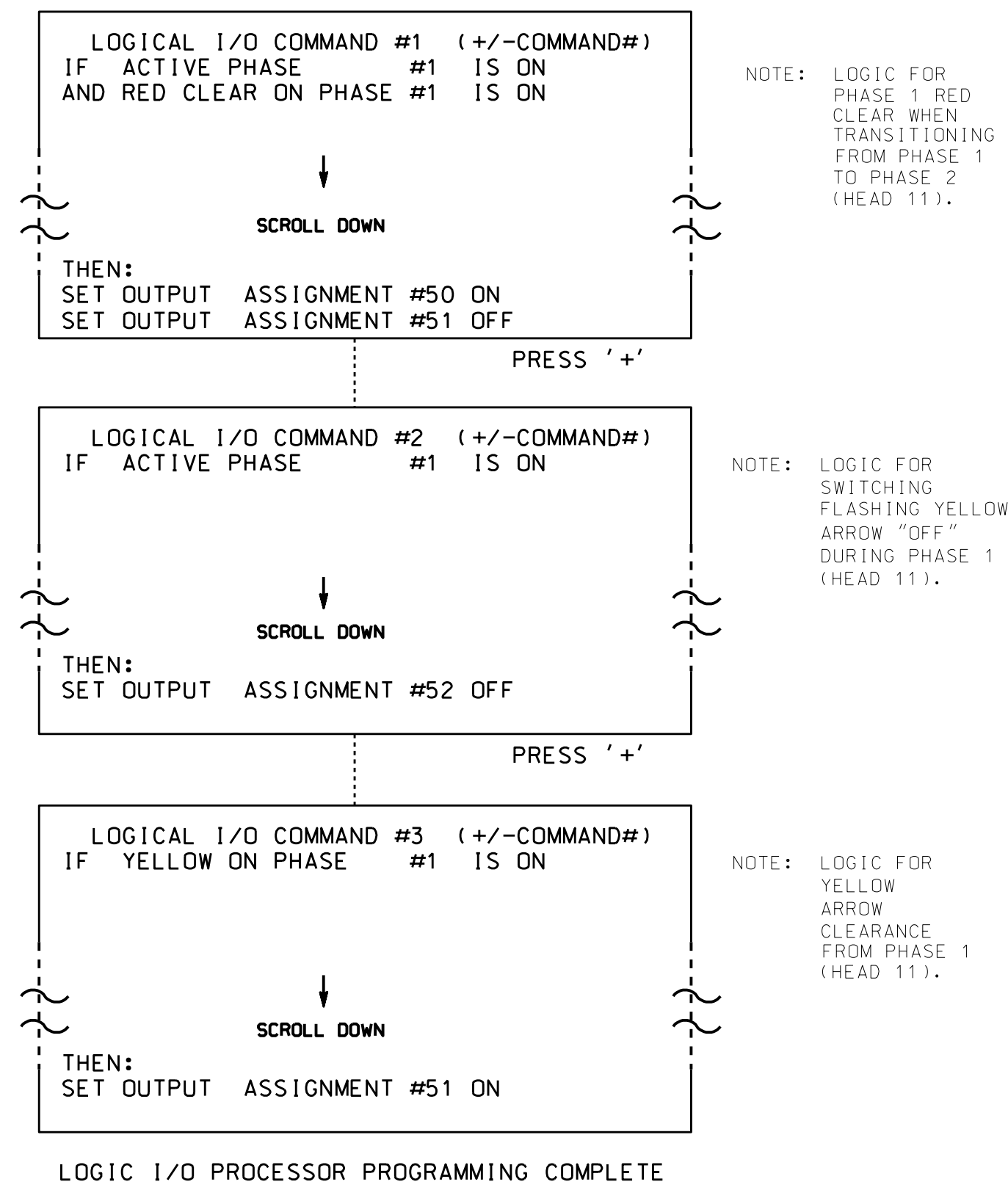


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```


PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0780
DESIGNED: August 2014
SEALED: 4-27-15
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:	SR 1009 (N. Main Street) and SR 1970/1113 (Kivett Dr.)	SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 008453 JOHN T. ROWE, JR.
	Division 7 Guilford County High Point	DocuSigned by: John T. Rowe, Jr. 4/29/2015
Prepared In the Offices of:	PLAN DATE: January 2015 REVIEWED BY: <i>JTR</i>	PREPARED BY: James Peterson REVIEWED BY:
REVISIONS	INIT. DATE	SIG. INVENTORY NO. 07-0780

09-APR-2015 13:18
 C:\IT\55181\T5\Sig\sig\work\output\sig\Map\ Peterson.dgn
 J. Peterson