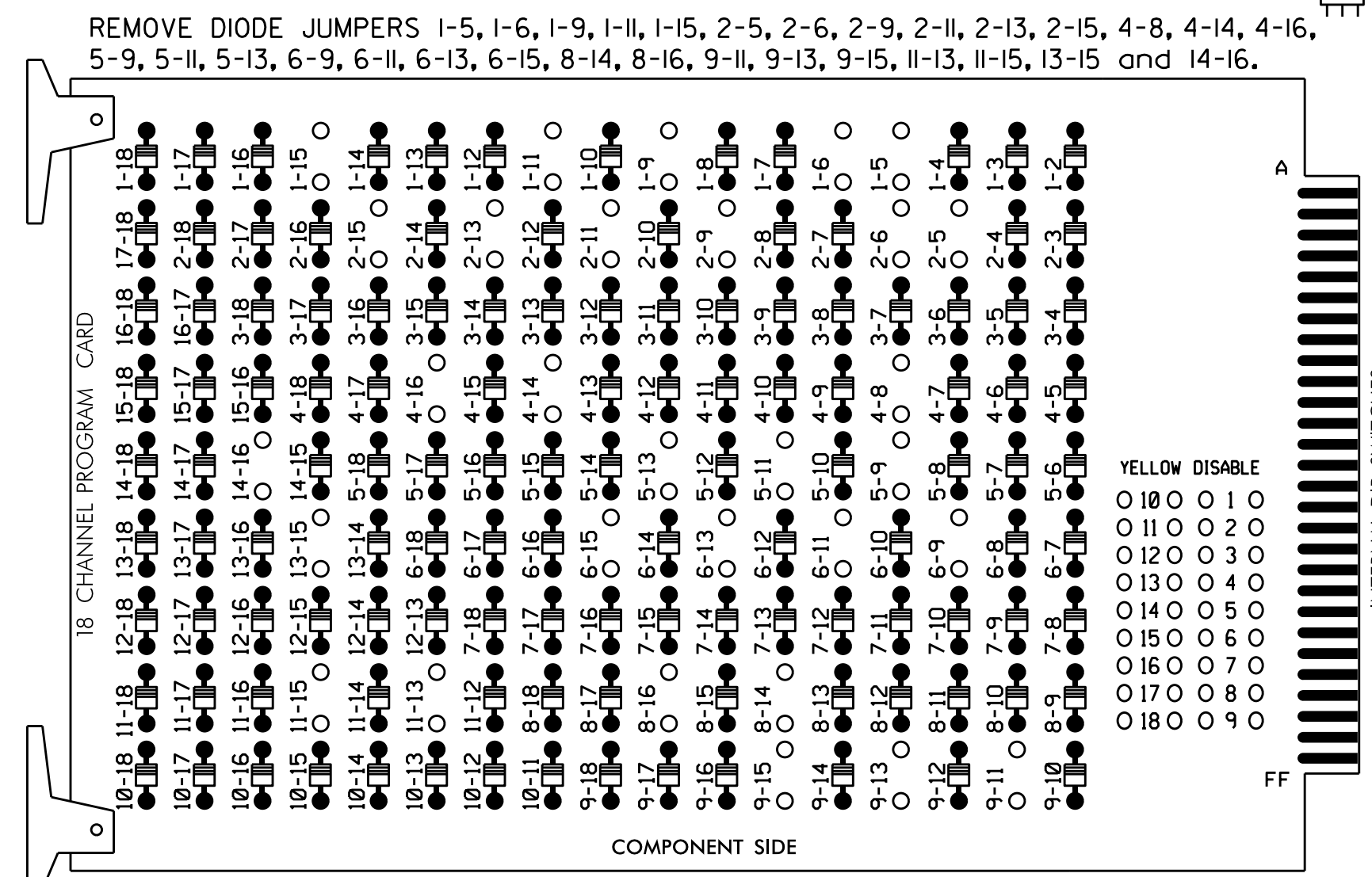


EDI MODEL 2018ECLip-NC CONFLICT MONITOR

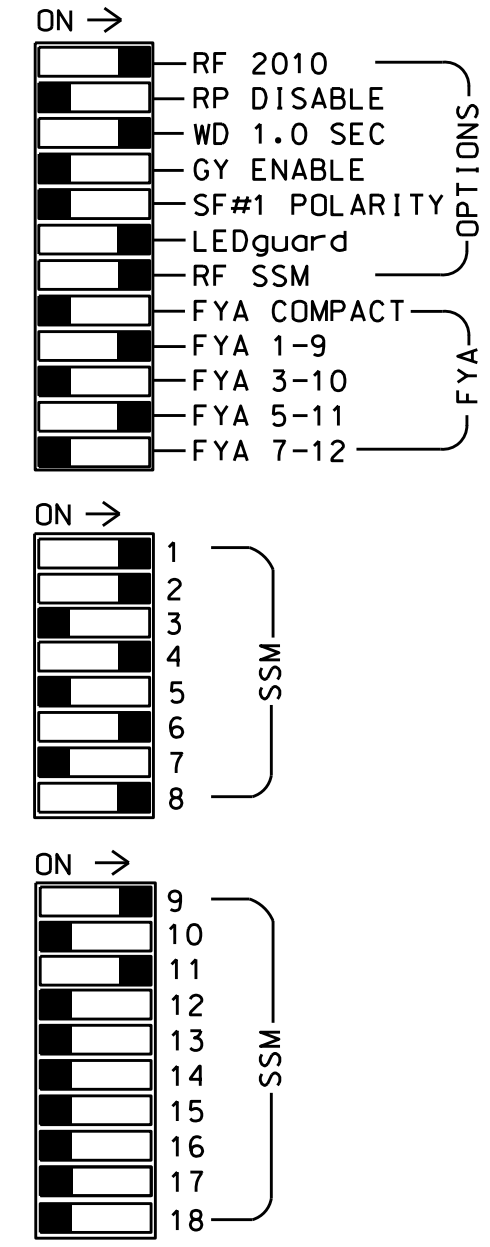
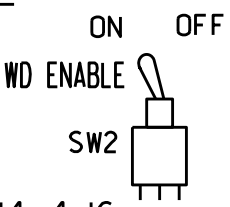
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070L  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,S12.  
 AUX S1,AUX S4.  
 PHASES USED.....1,2,4,5,6,8,2 PED,4 PED,6 PED,8 PED  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

| LOAD SWITCH NO.       | S1  | S2  | S3    | S4       | S5  | S6    | S7       | S8  | S9    | S10 | S11       | S12      | AUX S1 | AUX S2 | AUX S3 | AUX S4 | AUX S5 | AUX S6 |
|-----------------------|-----|-----|-------|----------|-----|-------|----------|-----|-------|-----|-----------|----------|--------|--------|--------|--------|--------|--------|
| CMU CHANNEL NO.       | 1   | 2   | 13    | 3        | 4   | 14    | 5        | 6   | 15    | 7   | 8         | 16       | 9      | 10     | 17     | 11     | 12     | 18     |
| PHASE                 | 1   | 2   | 2 PED | 3        | 4   | 4 PED | 5        | 6   | 6 PED | 7   | 8         | 8 PED    | OLA    | OLB    | SPARE  | OLC    | OLD    | SPARE  |
| SIGNAL HEAD NO.       | 11  | 83  | 21,22 | P21, P22 | NU  | 41,42 | P41, P42 | 51  | 61,62 | NU  | 81,82, 83 | P81, P82 | 11     | NU     | NU     | 51     | NU     | NU     |
| RED                   | *   | 128 |       |          | 101 |       |          | 134 |       |     | 107       |          |        |        |        |        |        |        |
| YELLOW                |     | 129 |       |          | 102 |       | *        | 135 |       |     | 108       |          |        |        |        |        |        |        |
| GREEN                 |     | 130 |       |          | 103 |       |          | 136 |       |     | 109       |          |        |        |        |        |        |        |
| RED ARROW             |     |     |       |          |     |       |          |     |       |     |           |          | A121   |        |        |        | A114   |        |
| YELLOW ARROW          |     | 126 |       |          |     |       |          |     |       |     |           |          | A122   |        |        |        | A115   |        |
| FLASHING YELLOW ARROW |     |     |       |          |     |       |          |     |       |     |           |          | A123   |        |        |        | A116   |        |
| GREEN ARROW           | 127 | 127 |       |          |     |       |          | 133 |       |     |           |          |        |        |        |        |        |        |
| Hand                  |     |     |       | 113      |     | 104   |          | 119 |       |     | 110       |          |        |        |        |        |        |        |
| Person                |     |     |       | 115      |     | 106   |          | 121 |       |     | 112       |          |        |        |        |        |        |        |

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 \* See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

| FILE | 1        | 2        | 3        | 4   | 5   | 6     | 7   | 8            | 9            | 10           | 11           | 12          | 13          | 14          |
|------|----------|----------|----------|-----|-----|-------|-----|--------------|--------------|--------------|--------------|-------------|-------------|-------------|
| U    | ∅ 1      | ∅ 1      | ∅ 2      | ∅ 4 | ∅ 5 | ∅ 6   | ∅ 8 | SYS. DET. S1 | SYS. DET. S2 | SYS. DET. S3 | SYS. DET. S4 | ∅ 2 PED     | ∅ 6 PED     | FS          |
| L    | 1A       | 1B       | 2A,2B    | 4A  | 5A  | 6A,6B | 8A  | SYS. DET. S1 | SYS. DET. S2 | SYS. DET. S3 | SYS. DET. S4 | DC ISOLATOR | DC ISOLATOR | DC ISOLATOR |
| U    | NOT USED | NOT USED | NOT USED | 4B  | 5A  | 6A,6B | 8B  | SYS. DET. S1 | SYS. DET. S2 | SYS. DET. S3 | SYS. DET. S4 | DC ISOLATOR | DC ISOLATOR | DC ISOLATOR |
| L    | NOT USED | NOT USED | NOT USED | 4B  | 5A  | 6A,6B | 8B  | SYS. DET. S1 | SYS. DET. S2 | SYS. DET. S3 | SYS. DET. S4 | DC ISOLATOR | DC ISOLATOR | DC ISOLATOR |

EX. : 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

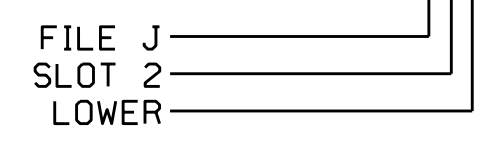
INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.         | LOOP TERMINAL | INPUT FILE POS. | PIN NO. | INPUT ASSIGNMENT NO. | DETECTOR NO. | NEMA PHASE | CALL | EXTEND | FULL TIME DELAY | STRETCH TIME | DELAY TIME |
|------------------|---------------|-----------------|---------|----------------------|--------------|------------|------|--------|-----------------|--------------|------------|
| 1A <sup>1</sup>  | TB2-1,2       | I1U             | 56      | 18                   | 1            | 1          | Y    | Y      |                 |              | 15         |
| 1B               | TB2-5,6       | J4U             | 48      | 10                   | 26           | 6          | Y    | Y      |                 |              | 15         |
| 2A,2B            | TB2-9,10      | I3U             | 63      | 25                   | 32           | 2          | Y    | Y      |                 |              |            |
| 4A               | TB4-9,10      | I6U             | 41      | 3                    | 4            | 4          | Y    | Y      |                 |              | 3          |
| 4B               | TB4-11,12     | I6L             | 45      | 7                    | 14           | 4          | Y    | Y      |                 |              | 10         |
| * S1             | TB6-9,10      | I9U             | 60      | 22                   | 11           | SYS        |      |        |                 |              |            |
| * S2             | TB6-11,12     | I9L             | 62      | 24                   | 13           | SYS        |      |        |                 |              |            |
| 5A <sup>2</sup>  | TB3-1,2       | J1U             | 55      | 17                   | 5            | 5          | Y    | Y      |                 |              | 15         |
|                  | -             | I4U             | 47      | 9                    | 22           | 2          | Y    | Y      |                 |              |            |
| 6A,6B            | TB3-5,6       | J2U             | 40      | 2                    | 6            | 6          | Y    | Y      |                 |              |            |
| 8A               | TB5-9,10      | J6U             | 42      | 4                    | 8            | 8          | Y    | Y      |                 |              | 3          |
| 8B               | TB5-11,12     | J6L             | 46      | 8                    | 18           | 8          | Y    | Y      |                 |              |            |
| * S3             | TB7-9,10      | J9U             | 59      | 21                   | 15           | SYS        |      |        |                 |              |            |
| * S4             | TB7-11,12     | J9L             | 61      | 23                   | 17           | SYS        |      |        |                 |              |            |
| PED PUSH BUTTONS |               |                 |         |                      |              |            |      |        |                 |              |            |
| P21,P22          | TB8-4,6       | I12U            | 67      | 29                   | PED 2        | 2 PED      |      |        |                 |              |            |
| P41,P42          | TB8-5,6       | I12L            | 69      | 31                   | PED 4        | 4 PED      |      |        |                 |              |            |
| P61,P62          | TB8-7,9       | I13U            | 68      | 30                   | PED 6        | 6 PED      |      |        |                 |              |            |
| P81,P82          | TB8-8,9       | I13L            | 70      | 32                   | PED 8        | 8 PED      |      |        |                 |              |            |

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
  - Add jumper from J1-W to I4-W, on rear of input file.
- \* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

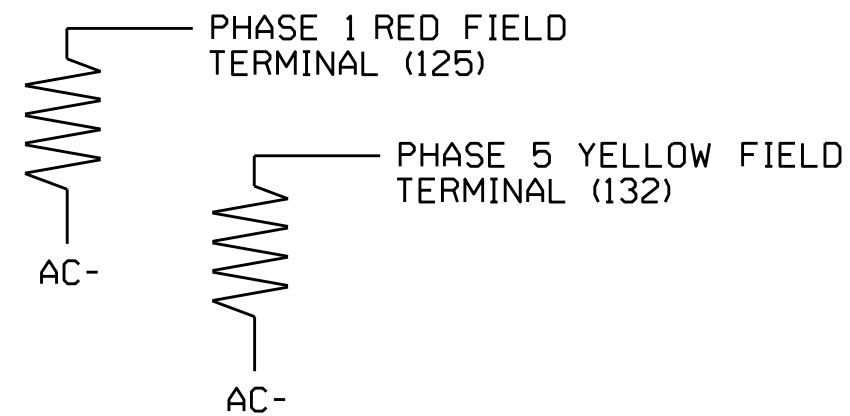
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

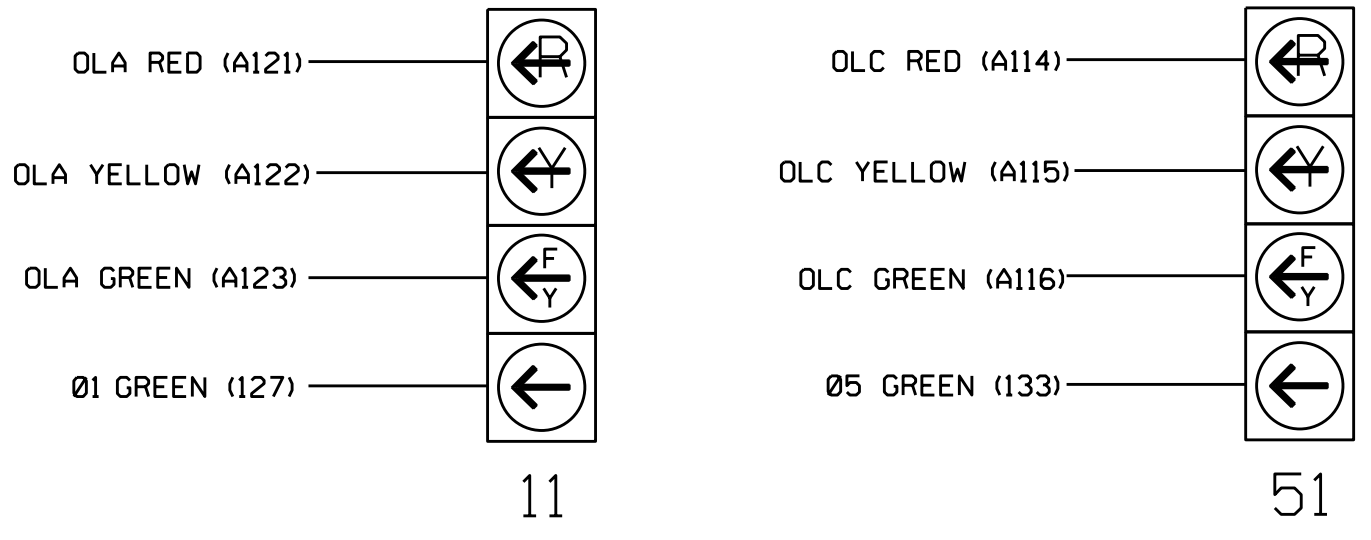
(install resistors as shown below)

| VALUE (ohms) | WATTAGE   |
|--------------|-----------|
| 1.5K - 1.9K  | 25W (min) |
| 2.0K - 3.0K  | 10W (min) |



4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0768  
 DESIGNED: July 2014  
 SEALED: 4-21-15  
 REVISED: N/A

ELECTRICAL DETAIL SHEET 1 OF 2

|  |  |  |  |
|--|--|--|--|
| ELECTRICAL AND PROGRAMMING DETAILS FOR:<br>Prepared In the Offices of:<br>750 N. Greenfield Pkwy, Garner, NC 27529 | SR 1009 (North Main Street) at Westwood Avenue |  | SEAL<br>JOHN T. ROWE, JR.<br>ENGINEER<br>4/22/2015 |
|  | Division 7 Guilford County High Point          | PLAN DATE: October 2014 REVIEWED BY: JTR |  |
| REVISIONS  |  | INIT. DATE                               | DocuSigned by: John T. Rowe, Jr. 4/22/2015         |
| SIG. INVENTORY NO. 07-0768   |  | DATE                                     |  |