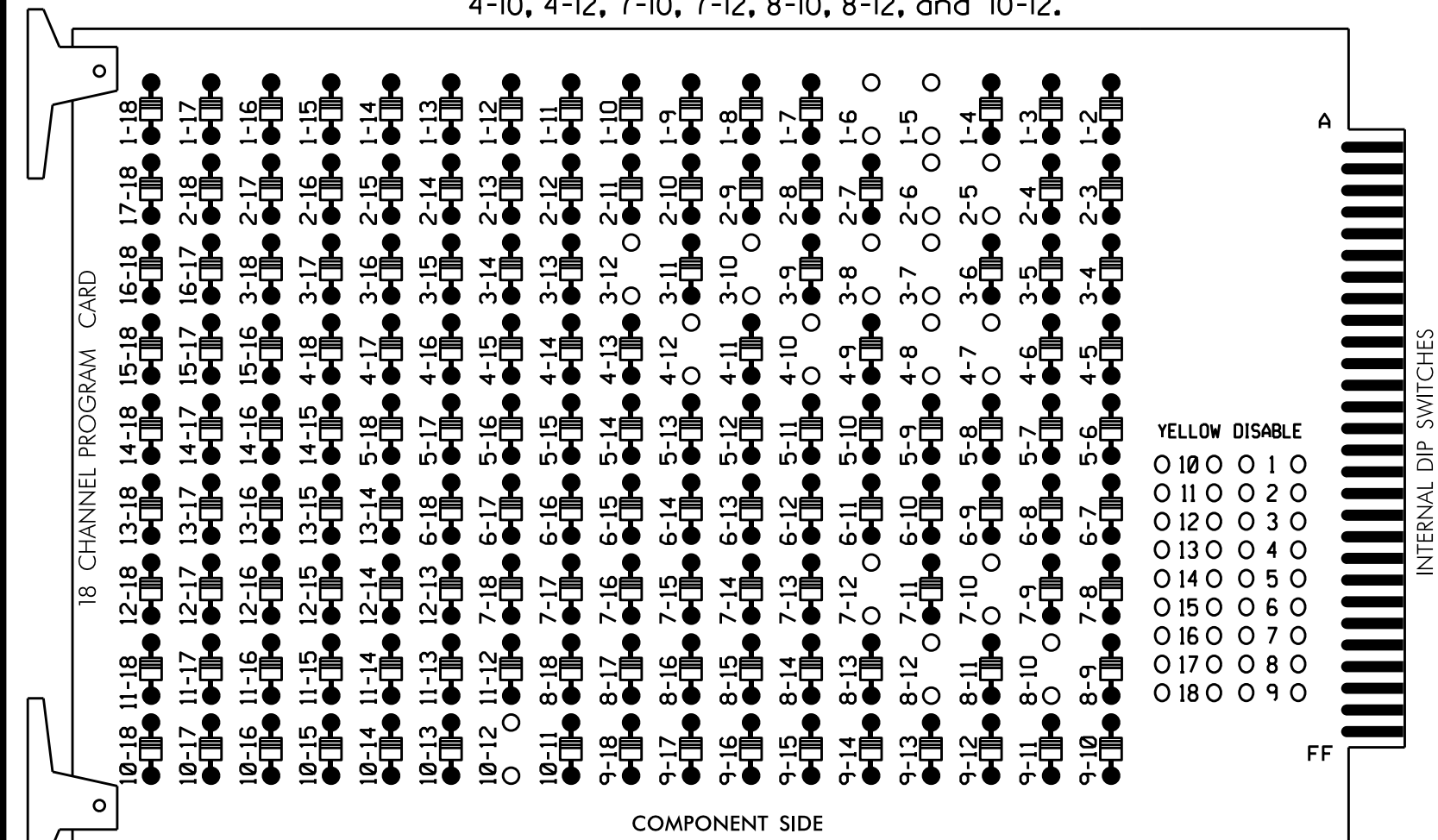


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

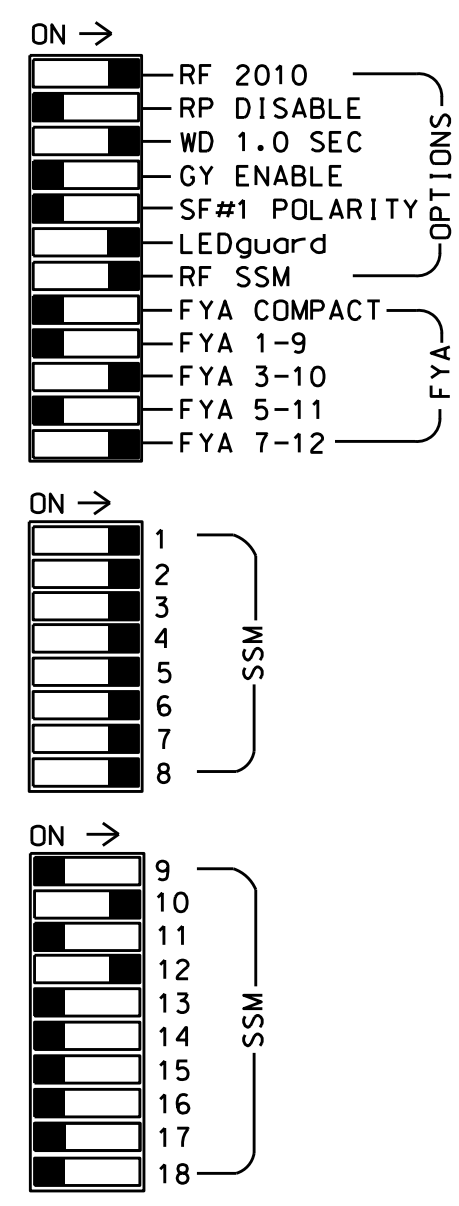
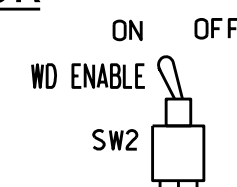
REMOVE DIODE JUMPERS 1-5, 1-6, 2-5, 2-6, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 7-10, 7-12, 8-10, 8-12, and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 2 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3*,S4,S5,S7,S8,S9*,S10,S11,AUX S2,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....NOT USED
 OVERLAP "B".....3+4
 OVERLAP "C".....NOT USED
 OVERLAP "D".....7+8

* S3 AND S9 USED FOR SCHOOL FLASHER

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6					
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18					
PHASE	1	2	2 PED SCHOOL FLASHER	3	4	4 PED	5	6	6 PED SCHOOL FLASHER	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE					
SIGNAL HEAD NO.	11	21,22,23	NU	101,103	23	31	41,42	NU	42	51	61,62	NU	102,104	62	71	81,82	NU	31	NU	71	NU		
RED		128		*	101				134				*	107									
YELLOW		129			102				135					108									
GREEN		130			103				136					109									
RED ARROW	125								131							A124					A101		
YELLOW ARROW	126			117				132	132					123							A125		A102
FLASHING YELLOW ARROW																					A126		A103
GREEN ARROW	127			118	118			133	133					124	124								
PED YELLOW				**	114									**	120								

NU = Not Used

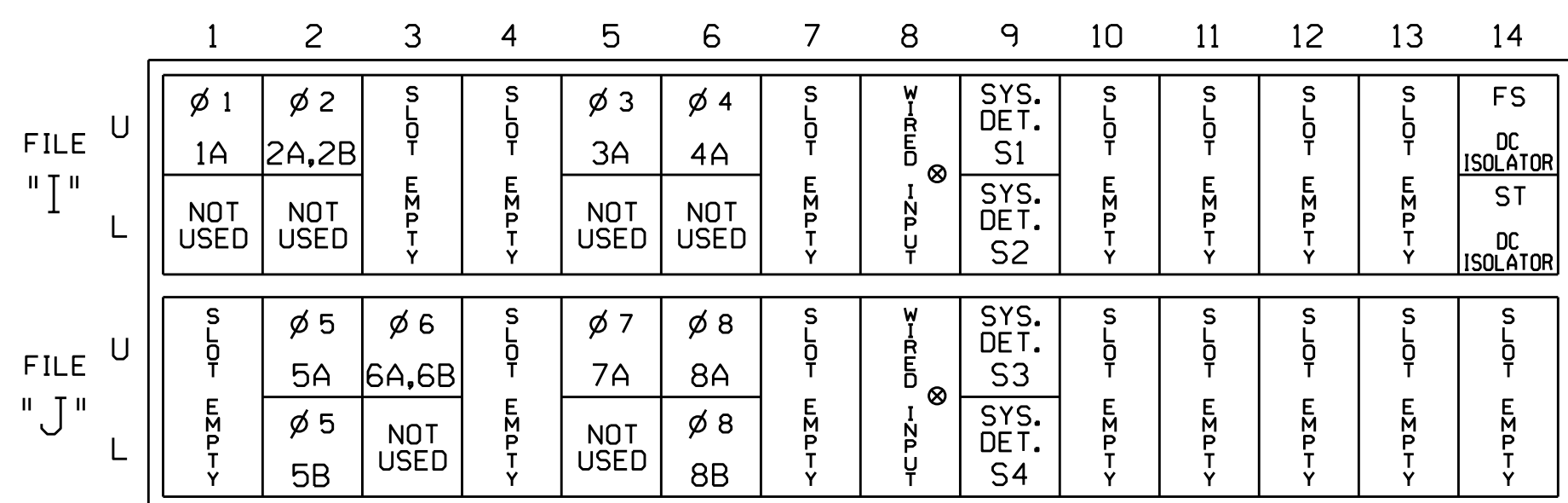
* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

** S3-Y and S9-Y are used for the School Flasher. See sheet 3 for wiring and programming details.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

⊗ Wired Input - Do not populate slot with detector card

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

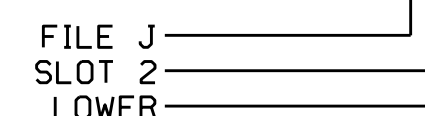
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
3A ¹	TB4-5,6	I5U	58	20	3	3	Y	Y			15
		J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
5A	TB3-5,6	J2U	40	2	6	5	Y	Y			3
5B	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A,6B	TB3-9,10	J3U	64	26	36	6	Y	Y			
7A ²	TB5-5,6	J5U	57	19	7	7	Y	Y			15
		I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
* S3	TB7-9,10	J9U	59	21	15	SYS					
* S4	TB7-11,12	J9L	61	23	17	SYS					

¹Add jumper from I5-W to J8-W, on rear of input file.

²Add jumper from J5-W to I8-W, on rear of input file.

* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

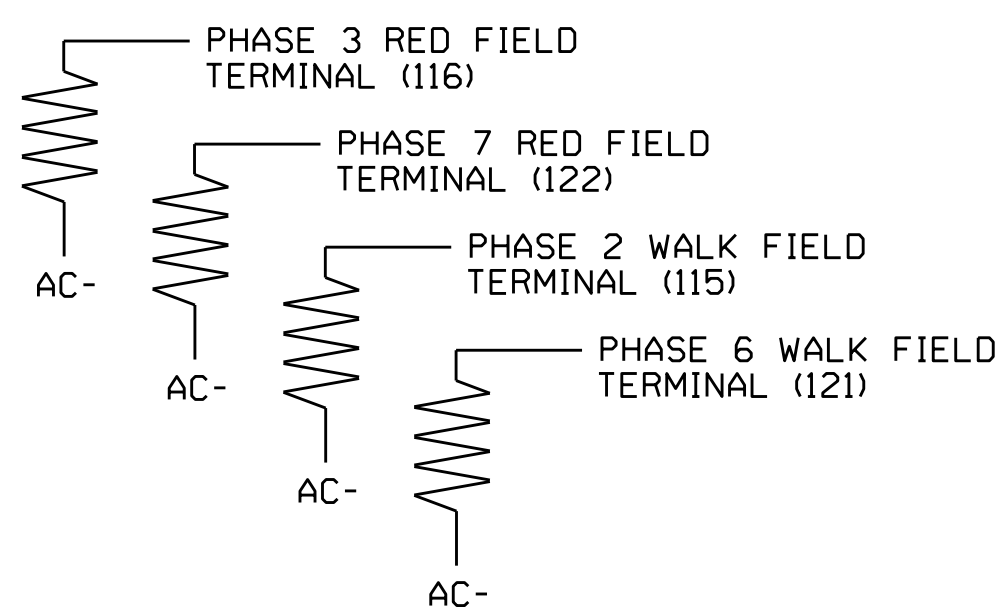
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

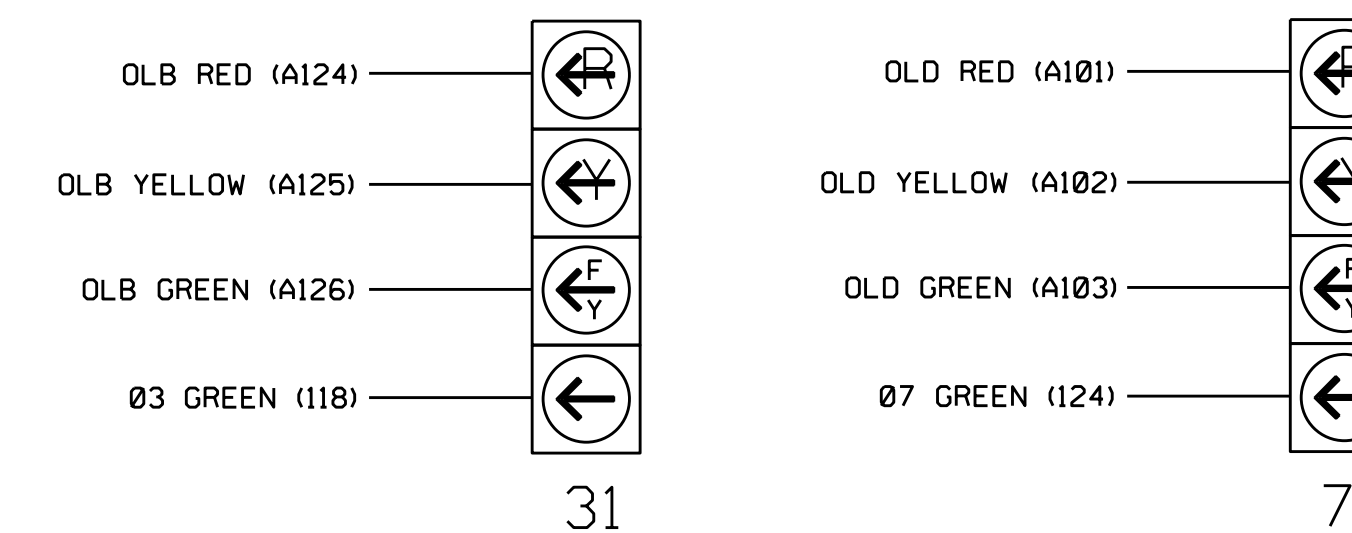
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 31 and 71 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0756
 DESIGNED: August 2014
 SEALED: 3/18/2015
 REVISED: N/A

Electrical Detail - Sheet 1 of 3

Electrical and Programming Details for: NC 68 (Westchester Drive) at SR 1768 (W. Lexington Ave.)

Prepared in the Offices of: **Transporatio Mobility and Safety Solutions** (Seal of North Carolina State Transportation Department)

Division 7 Guilford County High Point

PLAN DATE: November 2014 REVIEWED BY: *STR*

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS: _____ INIT. DATE

DocuSigned by: **John T. Rowe, Jr.** 3/19/2015

SIG. INVENTORY NO. 07-0756