PROJECT REFERENCE NO.	SHEET NO.
B-3159	Sig. 6.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3. IF ENABLED, DISABLE ACT LOGIC COMMANDS 4, 5 AND 6.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
  LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE       #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

PRESS '+'

```
  LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE       #1  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

PRESS '+'

```
  LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE    #1  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #51 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
 PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE:          ¦1234567891011121314 1516
VEH OVL PARENTS: ¦XX
VEH OVL NOT VEH: ¦
VEH OVL NOT PED: ¦
VEH OVL GRN EXT: ¦
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:   _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0735T2
DESIGNED: May 2015
SEALED: 07/01/2015
REVISED: N/A

Electrical Detail - Temp 2 - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 8 (Winston Road)
at
I-85 Bus./US 29 SB-US 64-70 WB Ramp

Division 9 | Davidson County | Lexington

PLAN DATE: June 2015 | REVIEWED BY:

PREPARED BY: C. Strickland | REVIEWED BY:

REVISIONS	INIT.	DATE

Prepared in the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy.Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 016286 ENGINEER MILTON I. DEAN

Docu|Signed by
Milton I. Dean
CB897289D1A2497...
7/10/2015
DATE

SIG. INVENTORY NO. 09-0735T2

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