

NOTE: This Overlap Detail will NOT be used during Temp 2 Operation.

**OVERLAP PROGRAMMING DETAIL - PAGE 1**

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2 and 3.
- From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
↓
SCROLL DOWN
↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
PRESS '+'
  
```

NOTE: Logic for Phase 5 RED Clear when transitioning from Phase 5 to Phase 6 (Head 51).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
↓
SCROLL DOWN
↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
PRESS '+'
  
```

NOTE: Logic for Switching Overlap C Green "OFF" during Phase 5 (Head 51).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
↓
SCROLL DOWN
↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
  
```

NOTE: Logic for Yellow Arrow Clearance from Phase 5 (Head 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OUTPUT REFERENCE SCHEDULE**

OUTPUT 42 = Overlap C Red  
 OUTPUT 43 = Overlap C Yellow  
 OUTPUT 44 = Overlap C Green

NOTE: This Overlap Detail WILL be used during Temp 2 Operation.

**OVERLAP PROGRAMMING DETAIL FOR PROTECTED LEFT TURN (51) - PAGE 2**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PRESS '+' TWICE

NOTICE PAGE 2 →


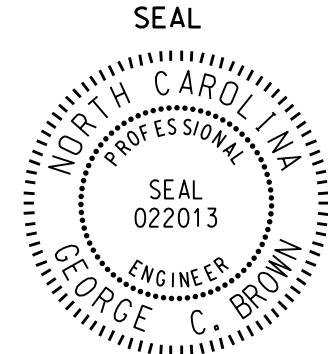
```

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

OVERLAP PROGRAMMING COMPLETE

Electrical Detail - Sheet 2 of 4 (Temporary 2)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0085 T2  
 DESIGNED: January 2015  
 SEALED: 1/29/15  
 REVISED: N/A

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	NC 268 (Elkin Highway) at SR 1966 (Airport Road)/SR 2333 (River Liberty Grove Church Rd)		SEAL  GEORGE C. BROWN ENGINEER
	PLAN DATE: January 2015 PREPARED BY: B. SIMMONS	REVIEWED BY: REVIEWED BY:	
REVISIONS INIT. DATE			SIG. INVENTORY NO. 11-0085 T2