

26

39

21

* Disable delay on this loop during Alternate Phase operation (See Sheet 3 of 3).

****** System detector only. Remove the vehicle phase assigned to this detector in the default programming.

J3U 64

J3L | 77 |

TB7-9,10 J9U 59

6B

6C

*** *** S1

TB3-9,10

TB3-11,12

INPUT FILE POSITION LEGEND: J2L

FILE J	
SLUI 2-	
LOWER-	

36

46

15

6

6

SYS

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'C' SE PHASE: 12345678910111 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL CEN EXT:
STARTUP COLOR: _ RED _ YELLOW FLASH COLORS: _ RED _ YELLOW
SELECT VEHICLE OVERLAP OPTIONS: FLASH YELLOW IN CONTROLLER FLAS GREEN EXTENSION (0-255 SEC) YELLOW CLEAR (0=PARENT,3-25.5 S RED CLEAR (0=PARENT,0.1-25.5 SE OUTPUT AS PHASE # (0=NONE, 1-16

OVERLAP PROGRAMMING COMPLETE

06:37 TS Sig -2015 &SU*I

CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
Y	Y	-	-	15
Y	Y	-	-	-
Y	Y	-	-	-
Y	Y	_	-	_
-	-	_	-	-







- PROCESSOR).









 $\sqrt{1}$ THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1013 DESIGNED: February 2008 SEALED: 5/6/2008 REVISED: 7/22/2015