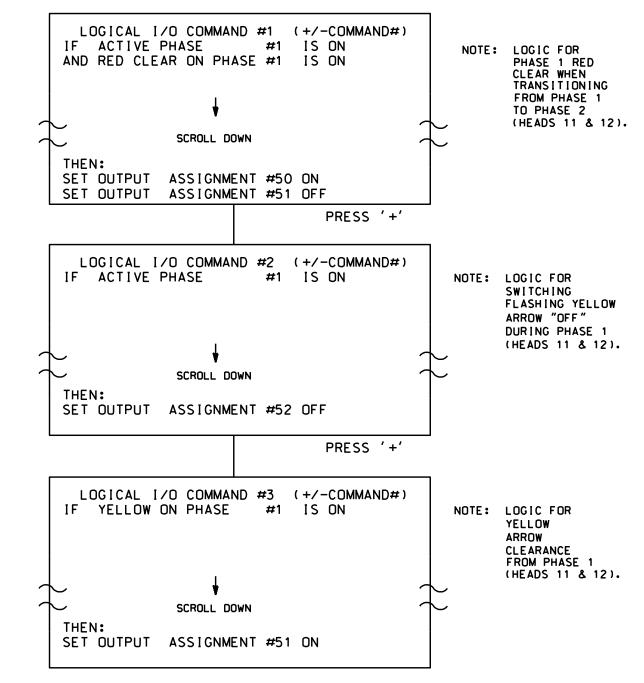
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

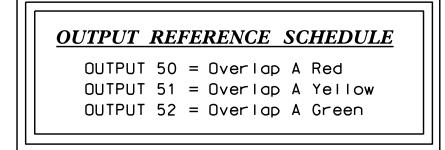
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1 THROUGH 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE



INPUT FILE POSITION LAYOUT

(front view)

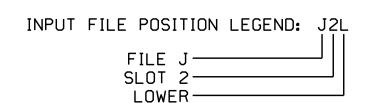
	1	2	3	4	5	6	7	8	9	10	11	12	13	14
	Ø 1	ø 2	SLOT	S	S	S	S	S	SYS. DET.	S L	S	S	S	FS
FILE U	1A	2A	O T	Ō	ŌT	þ T	Ď	Ď	S1	Ō	<u> </u>	ÖŢ	Þ	DC ISOLATOR
"I" ,	NOT	ø 2	EMPT	ш∑п	EΜρ	E M P r	E M P	E M P	NOT	E M P	E M P	E M P	E M P	ST
-	USED	2B	T Y	, T Y	T Y	T Y	T Y	Ť Y	USED	T Y	T Y	T Y	T Y	DC ISOLATOR
	S	S	S	S	S	S	S	S	S	S	s	S	S	s
FILE U	L O T	L O T	L O T	L O T	L O T									
"J" .	E M	E M P	ЕМРТ	ШΣΩ	EΣΩ	E M P +	E M	EΔp	E M P	E M	E M P	EΣΩ	EΜρ	E H
L	P T Y	P T Y	P T Y	P T Y	P T Y	P T Y								
EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH S										SENSI	<u></u> J E			

INPUT FILE CONNECTION & PROGRAMMING CHART

ST = STOP TIME

LOOP N	O. LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
* 1A	TB2-1,2	I1U	56	18	1	1	Υ	Υ	-	-	15
2A	TB2-5,6	I2U	39	1	2	2	Υ	Υ	1	-	-
2B	TB2-7,8	I2L	43	5	12	2	Υ	Υ	1	-	-
* * S1	TB6-9,10	I9U	60	22	11	SYS	1	-	1	-	-

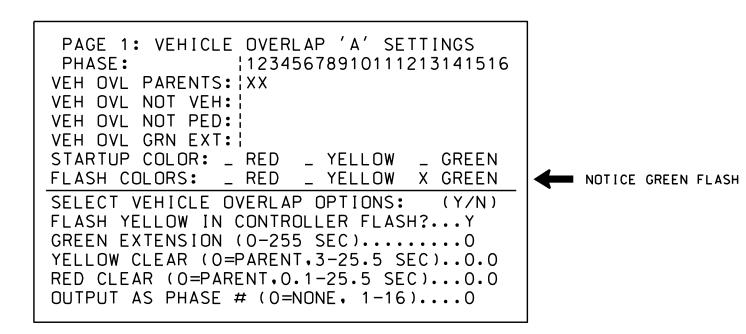
- * Disable delay on this loop during Alternate Phase operation (See Sheet 3 of 3).
- ** System detector only. Remove the vehicle phase assigned to this detector in the default programming.



OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE



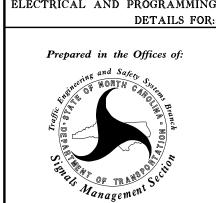
THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 03-1014

DESIGNED: February 2008

SEALED: 5/6/2008

REVISED: 7/22/2015



US 421 (Carolina Beach Road) Myrtle Grove South U-Turn

Division 3 New Hanover County Myrtle Grove PLAN DATE: February 2008 REVIEWED BY: PREPARED BY: R. Hinshaw REVIEWED BY:

This document is only certified as to the revisions. **→Ď**SIT. DATE 97R 7/23/2015

 $\overline{f V}$ No changes to this electrical plan. (WSA)

SIG. INVENTORY NO. 03-1014

REVISION V SEAL

008453

John T. Rowe, Jr. 7/23/2015

SEAL

Not a certified document as to the

Original Document but Only as to

the Revisions - This document originally issued and sealed by

F. Royal Hinshaw. #032117.

on 5/6/08.