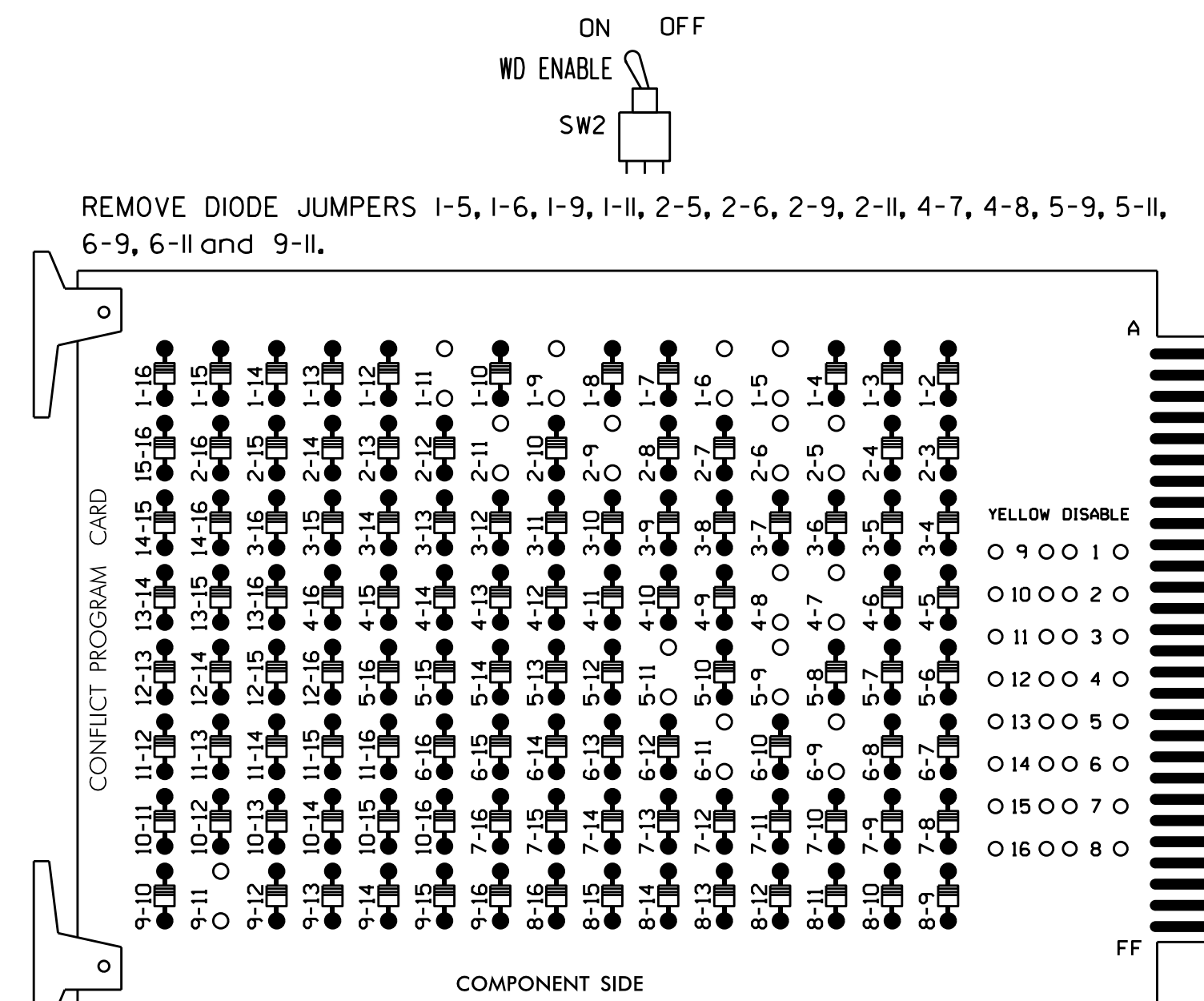


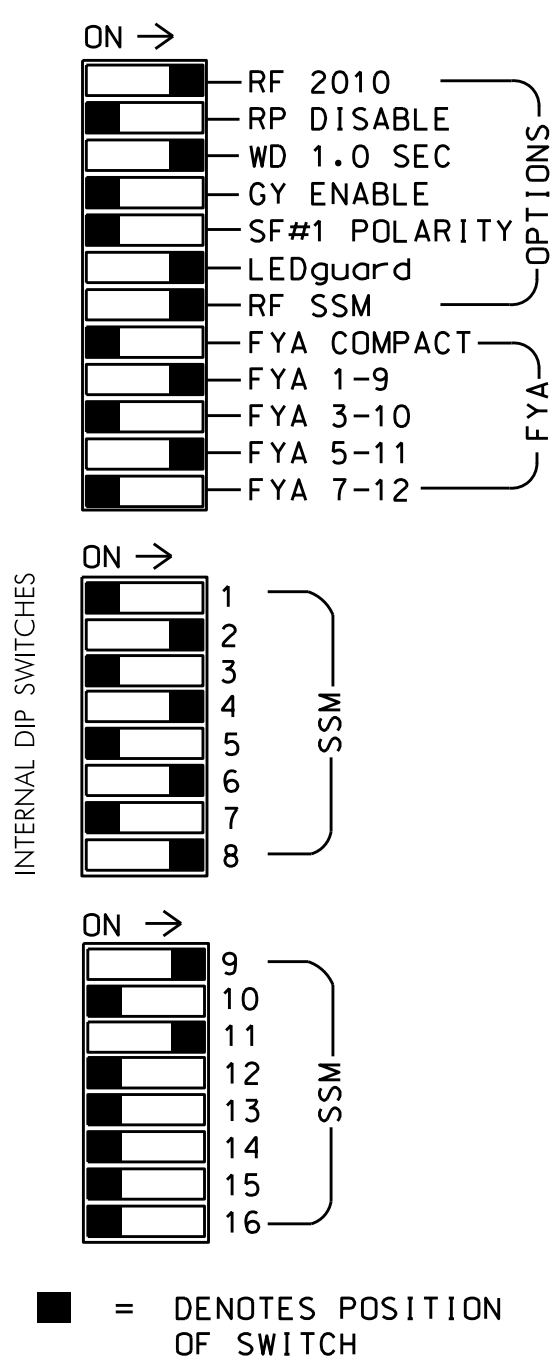
**EDI MODEL 2010ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3, 5,7,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11*	21,22	NU	NU	41,42	NU	51*	61,62	NU	41	81,82	NU	11*	NU	NU	51*	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW															A121			A114
YELLOW ARROW															A122			A115
FLASHING YELLOW ARROW															A123			A116
GREEN ARROW	127							133			124							

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S6,S7,S8,S9,S12
 PHASES USED.....1,2,4,5,6,*7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED
 * USED ONLY DURING PREEMPT

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14	FS
U	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	DC ISOLATOR
L	NOT USED	2A	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	ST
U	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	∅ 18	DC ISOLATOR
L	NOT USED	6A	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

⊗ Wired Input - Do not populate slot with detector card

* See Opticom Field Wire Detail below.

FS = FLASH SENSE
 ST = STOP TIME

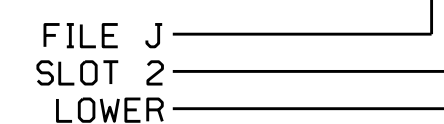
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

¹Add jumper from I1-W to J4-W, on rear of input file.

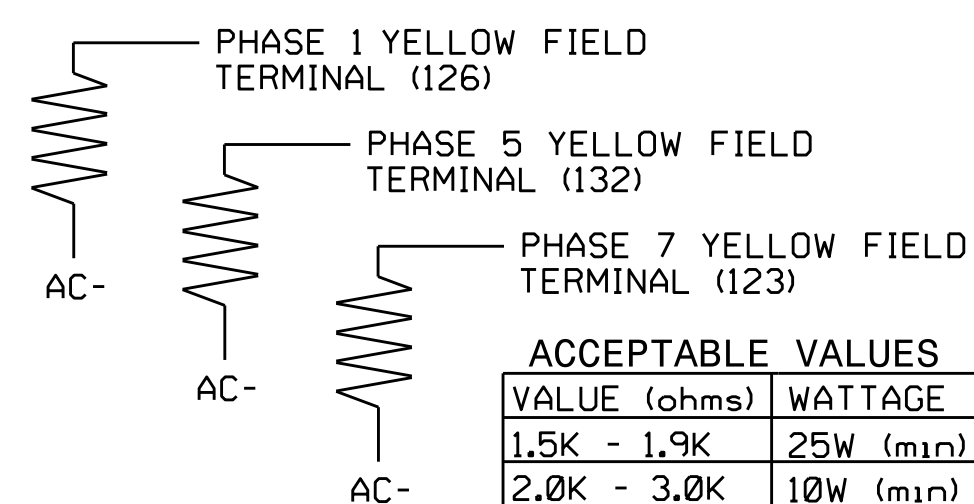
²Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



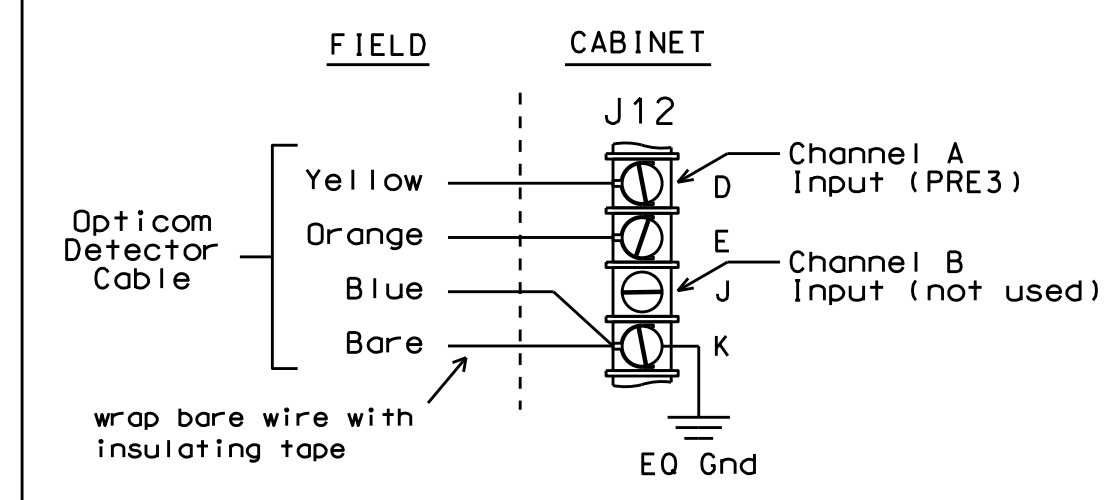
LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



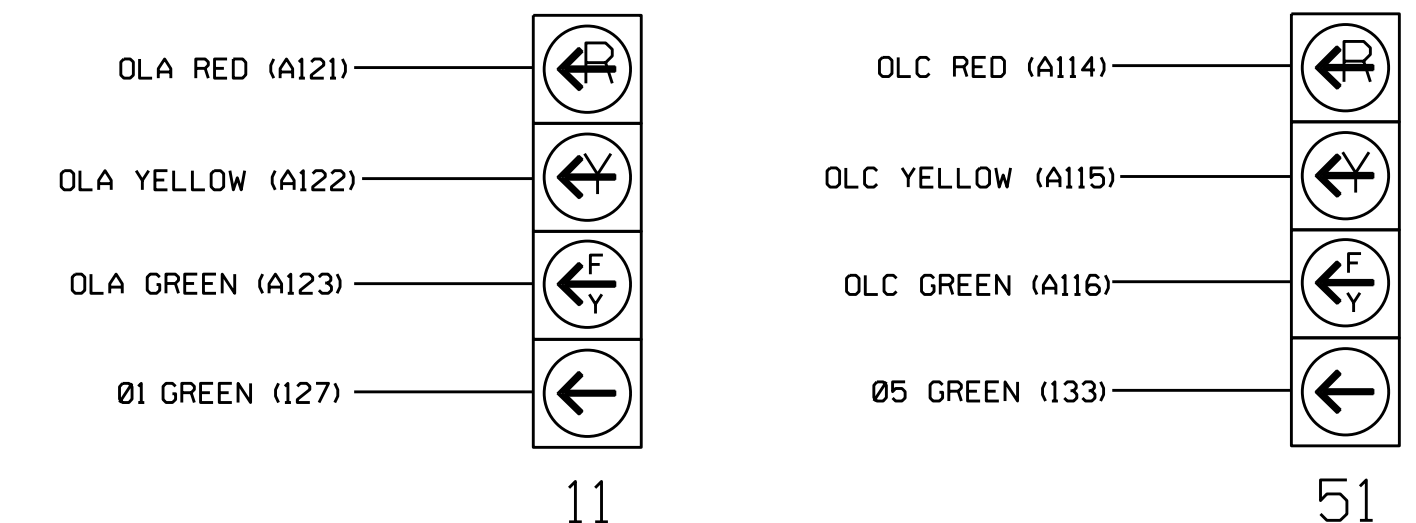
TYPICAL OPTICOM FIELD WIRE DETAIL

(input file, rear view)



4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

PREEMPT ONLY PHASE OMIT NOTE

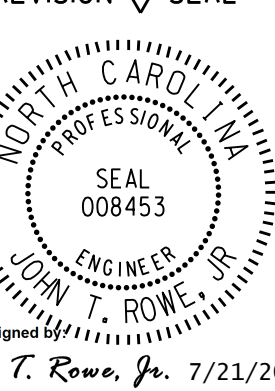
(program controller as shown below)

From Main Menu press '2' (Phase Control). Then '1' (Phase Control Functions). Program Phase 7 for 'Omit Phase' and Phases 1, 2, 4, 5, 6 and 8 for 'Startup Calls'. This is to prevent Phase 7 from being served when not in Preempt.

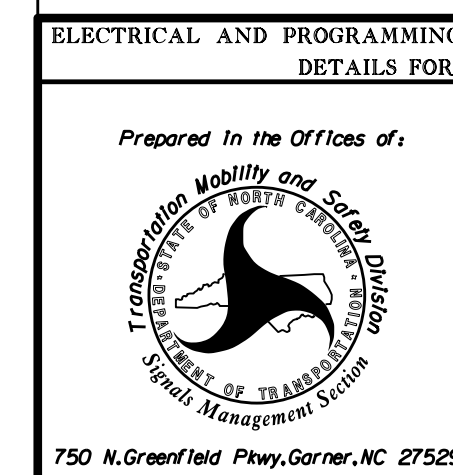


THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0827
 DESIGNED: July 2010
 SEALED: 7/7/2010
 REVISED: 7/20/2015

REVISION SEAL



ELECTRICAL DETAIL SHEET 1 OF 2



US 421 (Carolina Beach Road) at SR 1576 (River Road)/ SR 1531 (S. Seabreeze Road)
 PLAN DATE: July 2010
 PREPARED BY: C. Strickland
 REVISIONS: Revised preempt dwell yellow clear time. (WSA)
 DATE: 7/23/2015

Not a certified document. This document originally issued and sealed by George C. Brown, PE, #022013 on 07/23/2010. This document shall not be considered a certified document.
 SIGNATURE: _____ DATE: _____
 SIG. INVENTORY NO. 03-0827