PROJECT REFERENCE NO.	SHEET NO.
R-2915A	Sig. 4.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
  LOGICAL I/O COMMAND #1   (+/-COMMAND#)
IF   ACTIVE PHASE        #5   IS ON
AND RED CLEAR ON PHASE #5   IS ON

            SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #30 ON
SET OUTPUT   ASSIGNMENT #31 OFF
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
  LOGICAL I/O COMMAND #2   (+/-COMMAND#)
IF   ACTIVE PHASE        #5   IS ON

            SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #32 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

PRESS '+'

```
  LOGICAL I/O COMMAND #3   (+/-COMMAND#)
IF   YELLOW ON PHASE     #5   IS ON

            SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #31 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 30 = Overlap C Red
OUTPUT 31 = Overlap C Yellow
OUTPUT 32 = Overlap C Green
OUTPUT 34 = Phase 5 Green

Note: All outputs shown above have been remapped.
See sheets 3 and 4 of this electrical detail.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```
 PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE:          |12345678910111213141516
VEH OVL PARENTS: |    XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```
 PAGE 1: VEHICLE OVERLAP 'E' SETTINGS
 PHASE:          |12345678910111213141516
VEH OVL PARENTS: |    X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....7
```

← NOTICE OUTPUT AS PHASE

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 11-1174 T3
DESIGNED: May 2015
SEALED: 5/22/2015
REVISED: N/A

Electrical Detail - Temporary Signal 3 (TCP Phase III) - Sheet 3 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:

US 421
at
US 221

Division 11 Watauga County DS Deep Gap

PLAN DATE: May 2015 REVIEWED BY: JTR

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS	INIT.	DATE

Prepared in the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by:
John T. Rowe, Jr. 5/26/2015
SIGNATURE DATE

SIG. INVENTORY NO. 11-1174 T3

26-MAY-2015 07:01
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