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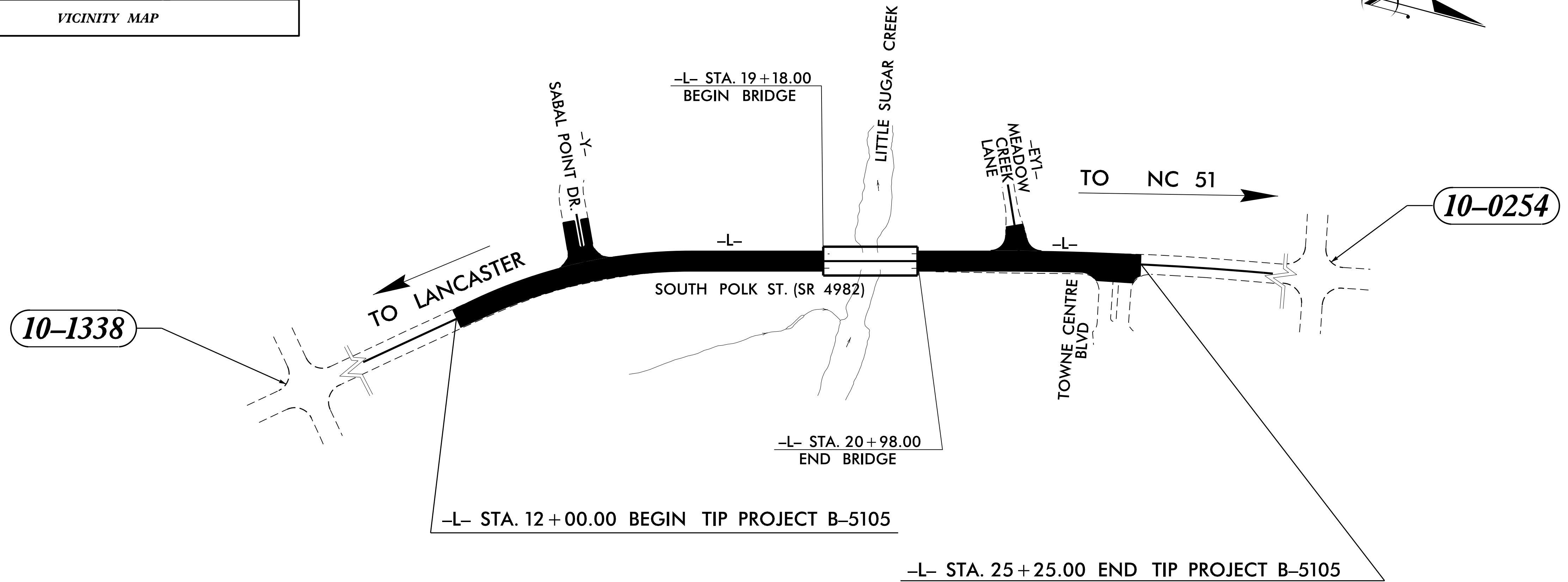
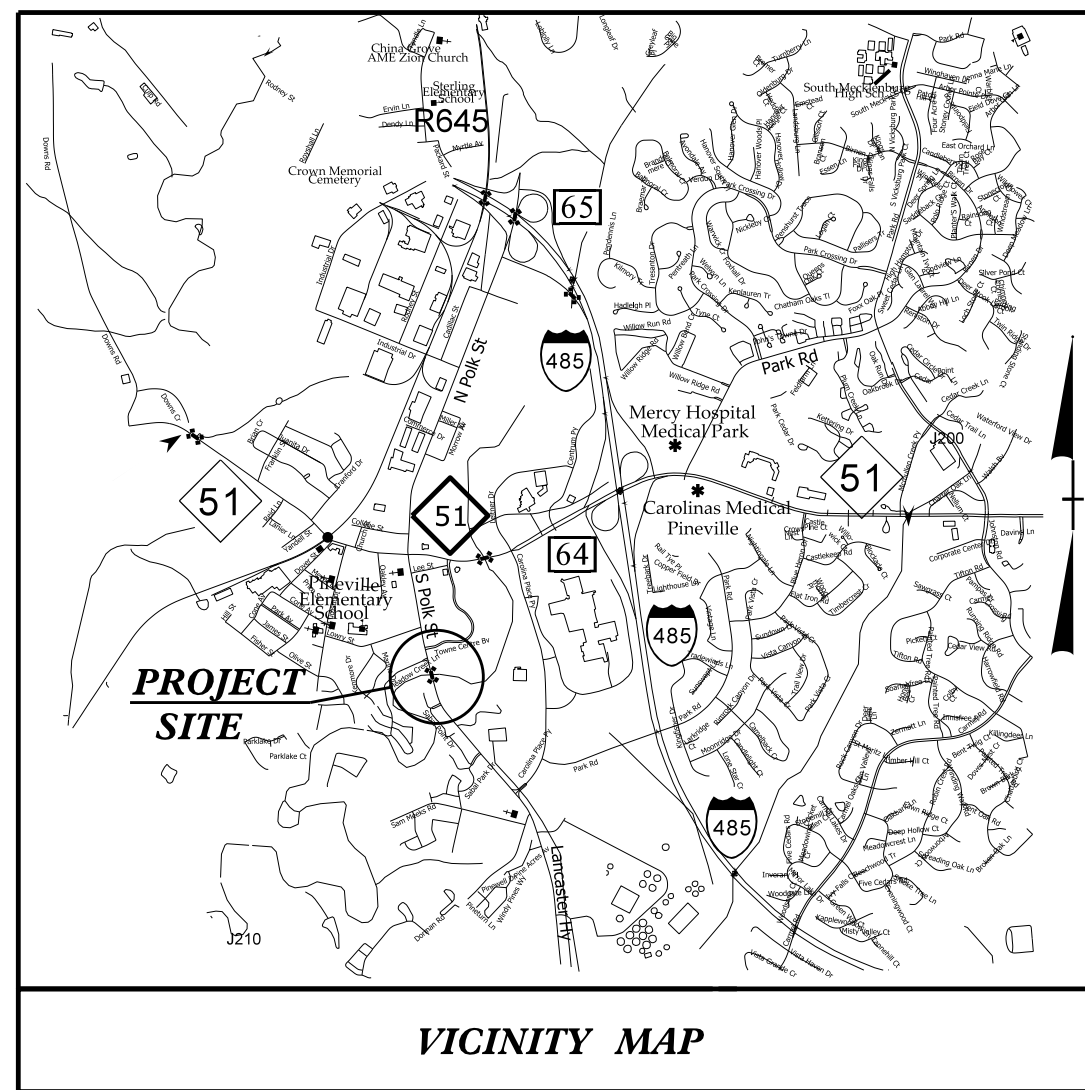
STATE OF NORTH CAROLINA
DIVISION OF HIGHWAYS

MECKLENBURG COUNTY

LOCATION: BRIDGE 49 ON SR 4982 (SOUTH POLK ST.)

TYPE OF WORK: TRAFFIC SIGNALS

Project: B-5105



Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.

Index of Plans		
Sheet #	Reference #	Location/Description
Sig. 1.0	B - 5105	Title Sheet
Sig. 2.0-2.2	10 - 1338 T	SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)-TEMPORARY
Sig. 3.0-3.2	10 - 1338	SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)
Sig. 4.0-4.1	10 - 0254 T	NC 51 (Main Street) / NC 51 Pineville-Matthews Road at SR 4982 (Polk Street) -TEMPORARY
Sig. 5.0-5.1	10 - 0254	NC 51 (Main Street) / NC 51 Pineville-Matthews Road at SR 4982 (Polk Street)

INTELLIGENT TRANSPORTATION AND SIGNALS UNIT

Contacts:

Timothy J. Williams, PE - Western Region Signals Engineer
George C. Brown, PE - Signal Equipment Design Engineer

Prepared In the Office of:
DIVISION OF HIGHWAYS
TRANSPORTATION MOBILITY AND SAFETY
DIVISION

750 N. Greenfield Parkway, Garner, NC 27529

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PHASING DIAGRAM

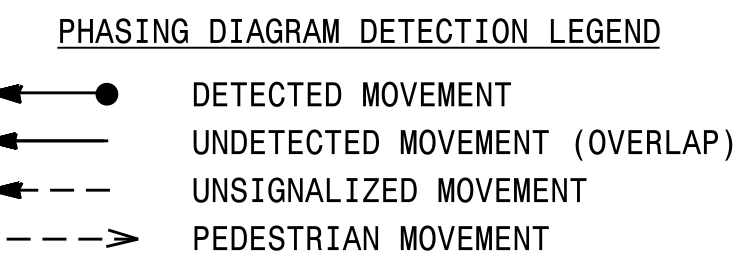
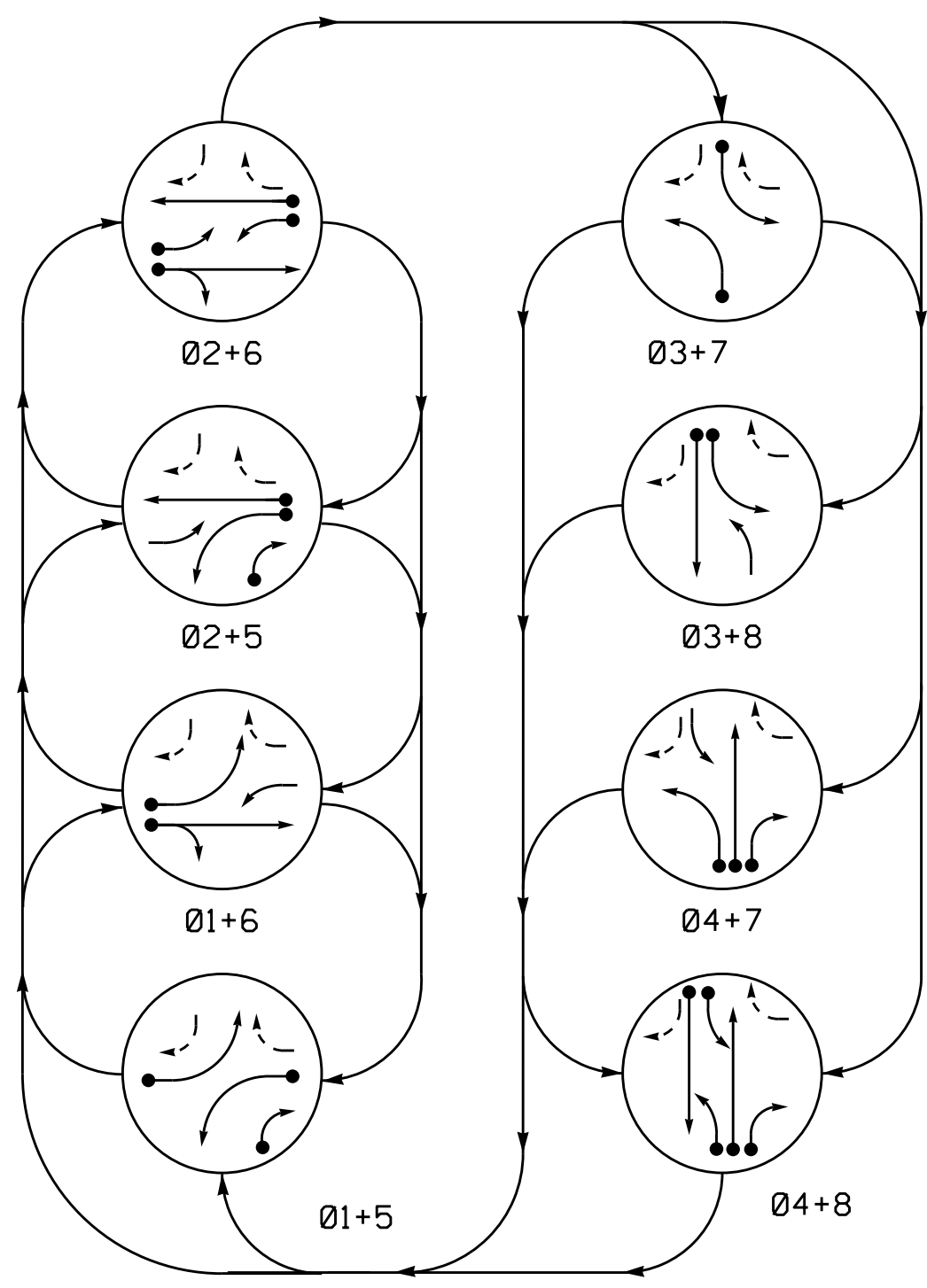
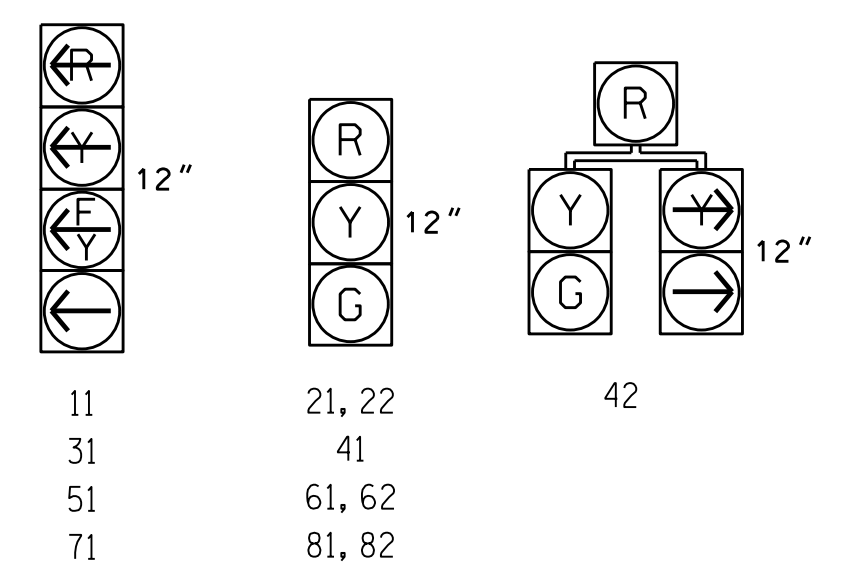


TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21, 22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41	R	R	R	R	R	R	G	G
42	←	←	←	←	←	←	G	G
51	←	←	←	←	←	←	←	←
61, 62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81, 82	R	R	R	R	R	G	R	G

SIGNAL FACE I.D.

All Heads L.E.D.



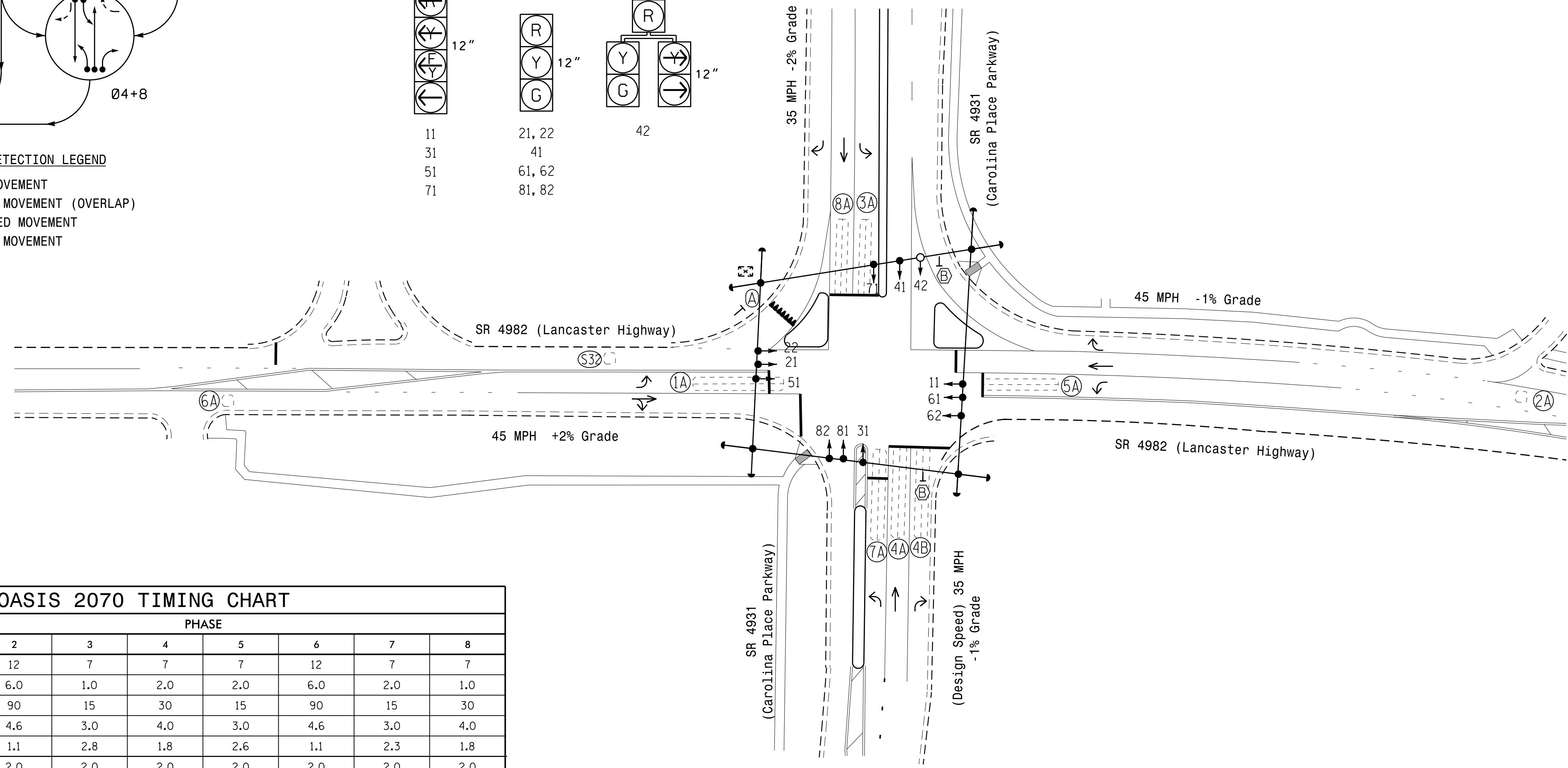
OASIS 2070 LOOP & DETECTOR INSTALLATION

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
					PHASE	CALLING	EXTENSION	FULL TIME DELAY				
1A	6X40	0	2-4-2	-	1	Y	Y	-	15	-	-	-
2A	6X6	300	5	-	2	Y	Y	-	3	-	-	-
3A	6X60	+5	2-4-2	-	3	Y	Y	-	15	-	-	-
4A	6X40	0	2-4-2	-	4	Y	Y	-	-	-	-	-
4B	6X40	0	2-4-2	-	4/5	Y	Y	-	10	-	-	-
5A	6X40	+15	2-4-2	-	5	Y	Y	-	15	-	-	-
6A	6X6	300	5	-	6	Y	Y	-	3	-	-	-
7A	6X60	+15	2-4-2	-	7	Y	Y	-	15	-	-	-
8A	6X40	+5	2-4-2	-	8	Y	Y	-	-	-	-	-
S32	6X6	+180	EXIST	-	-	-	-	-	-	-	Y	-

8 Phase Fully Actuated NC 51 (Pineville) CLS

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Set all detector units to presence mode.
6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
7. Closed loop system data: Controller Asset # 1338.

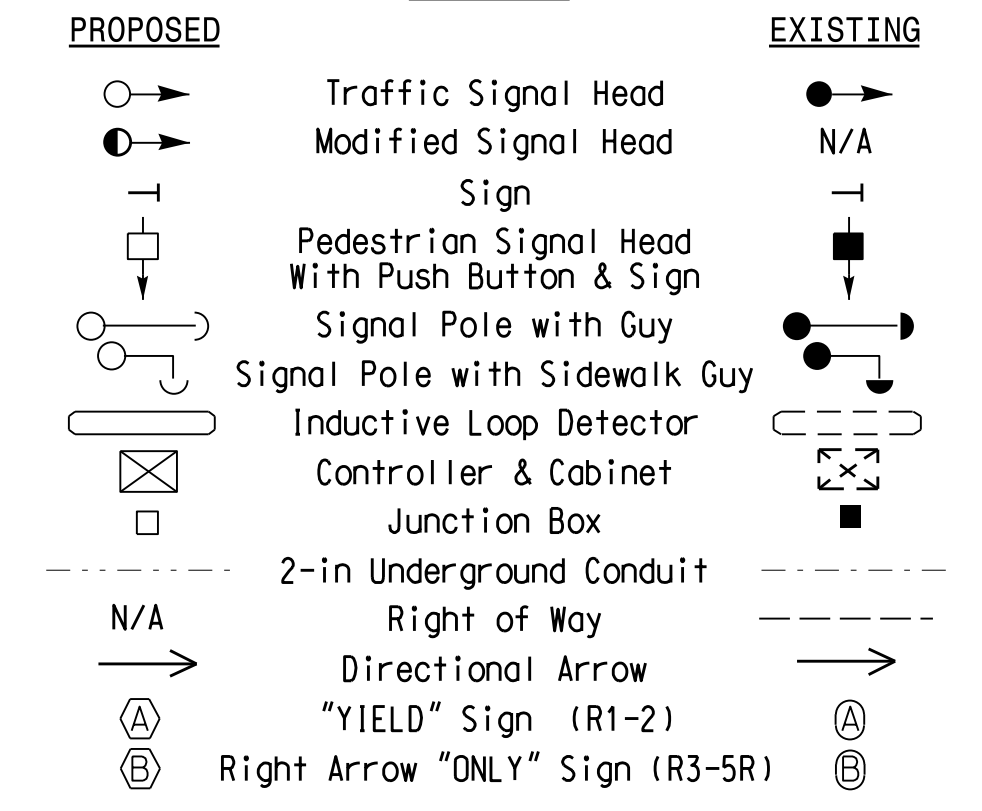


OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	1.0	2.0	2.0	6.0	2.0	1.0
Max Green 1 *	15	90	15	30	15	90	15	30
Yellow Clearance	3.0	4.6	3.0	4.0	3.0	4.6	3.0	4.0
Red Clearance	2.3	1.1	2.8	1.8	2.6	1.1	2.3	1.8
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	2.5	-	-	-	2.5	-	-
Max Variable Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	30	-	-	-	30	-	-
Time To Reduce *	-	15	-	-	-	15	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



Signal Upgrade - Temporary Design

SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)

Division 10 Mecklenburg County Pineville

PLAN DATE: March 2015 REVIEWED BY: [Signature]

PREPARED BY: C. Pierce REVIEWED BY: [Signature]

750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 1" = 40'

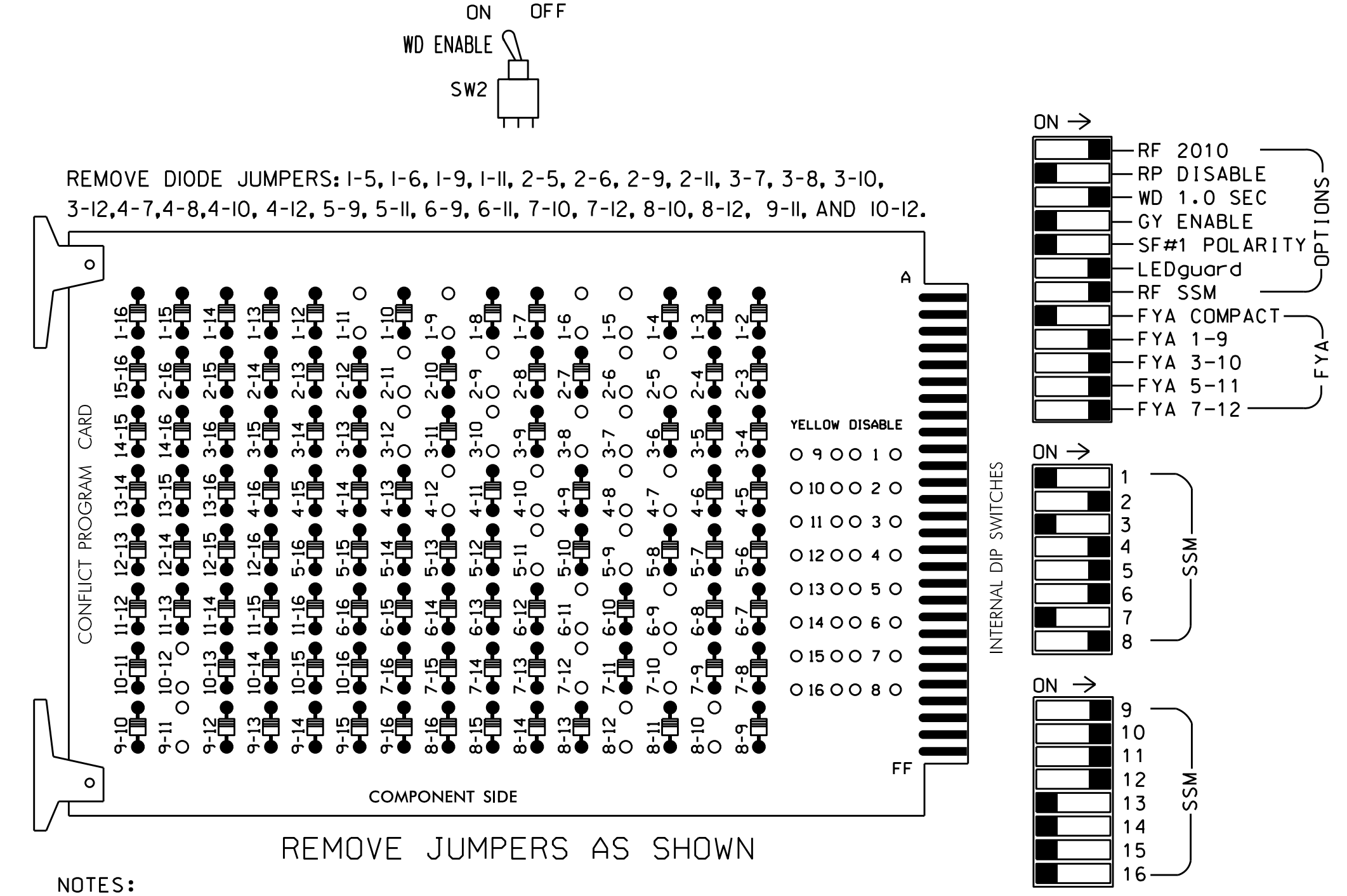
DocuSign by: S. J. Williams 4/16/2015

SIG. INVENTORY NO. 10-1338 T

16-APP-2015-03-24
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EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3, 7,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the NC 51 (Pineville) System.

SIGNAL HEAD HOOK-UP CHART

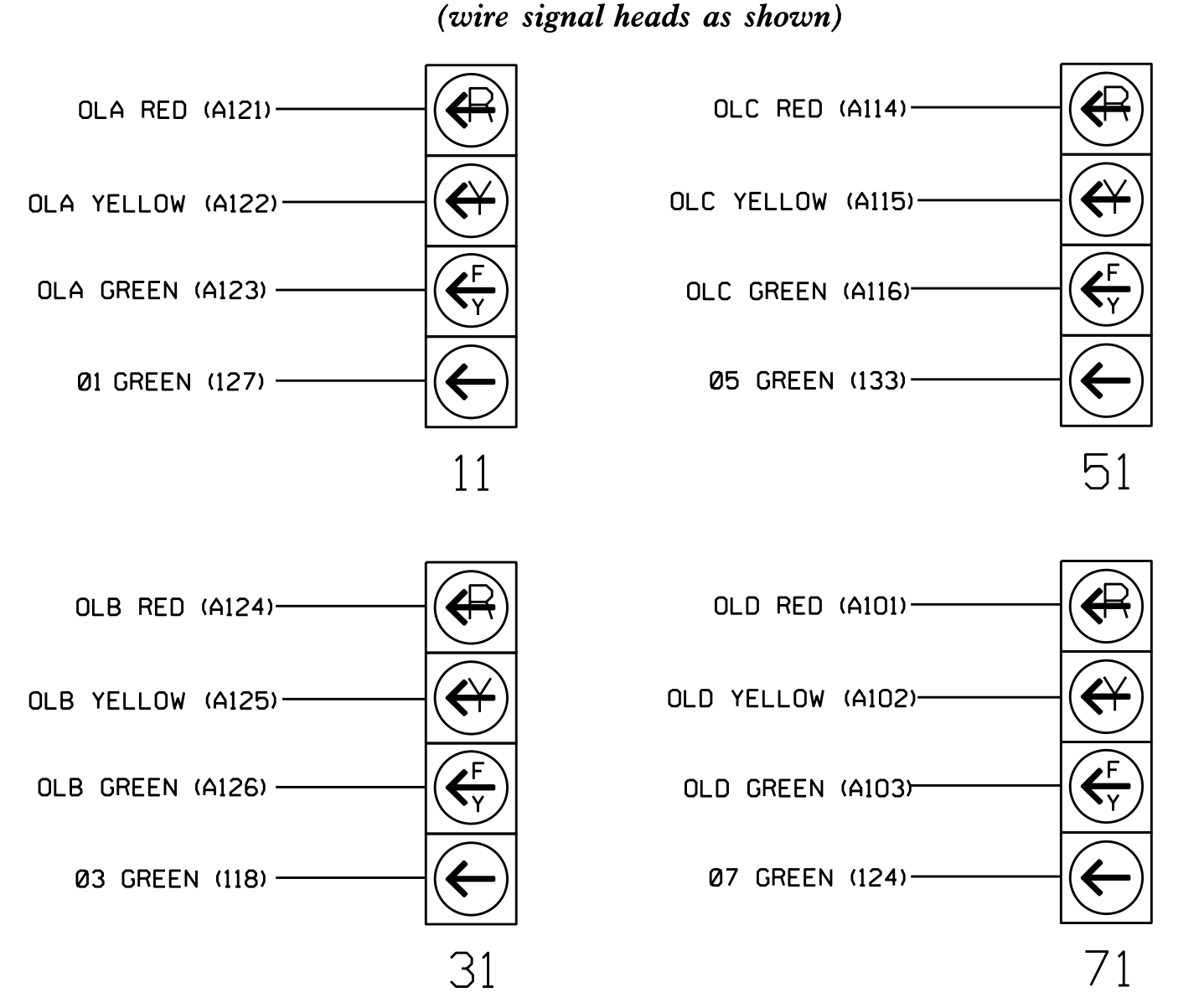
LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11★	21,22	NU	31★	41,42	NU	42	51★	61,62	NU	71★	81,82	NU	11★	31★	NU	51★	71★	NU
RED		128			101		*		134			107							
YELLOW	*	129		*	102				135		*	108							
GREEN		130			103				136			109							
RED ARROW														A121	A124		A114	A101	
YELLOW ARROW							132							A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127				118		133	133		124									

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

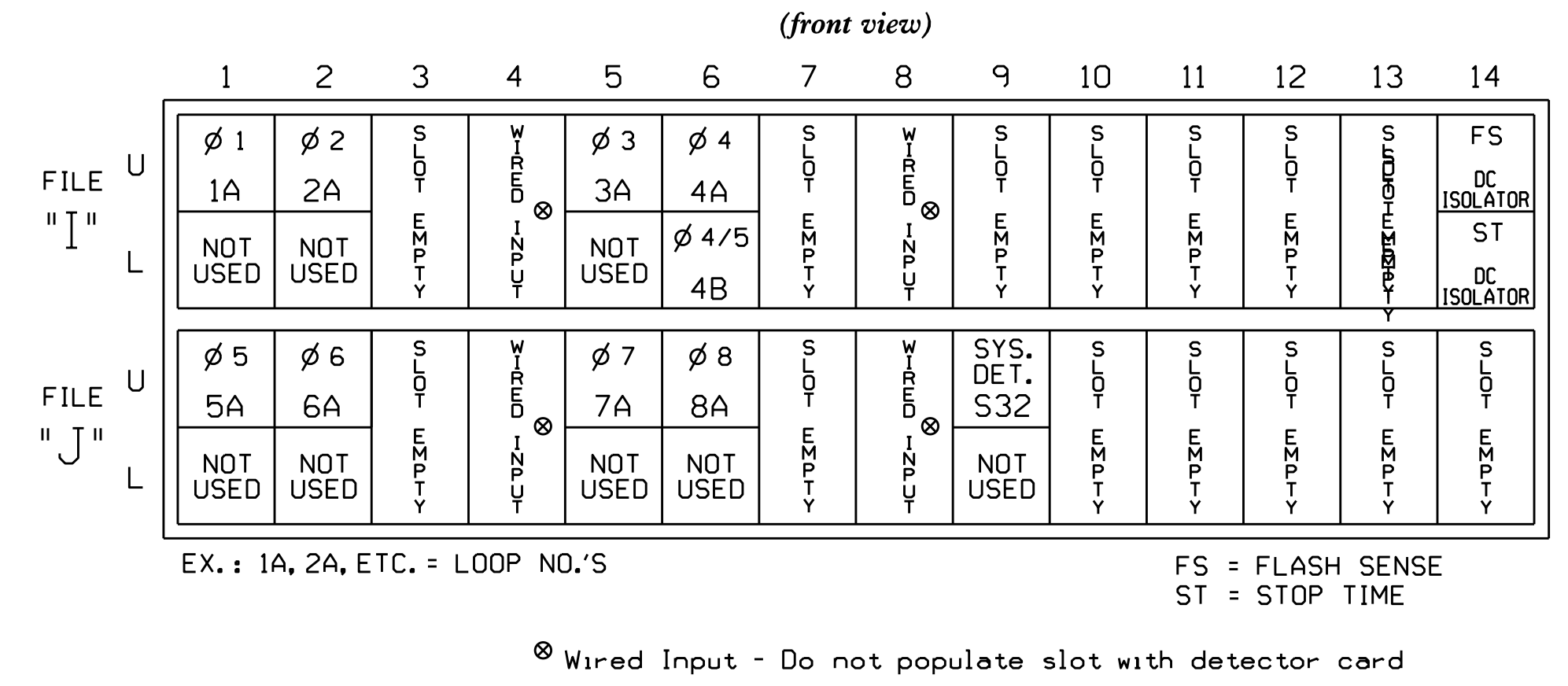
CONTROLLER.....2070L
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,
 S12,S13
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

4 SECTION FYA PPLT SIGNAL WIRING DETAIL



NOTE
 1. The sequence display for these signals require special logic programming. See sheet 2 of 2 for programming instructions.

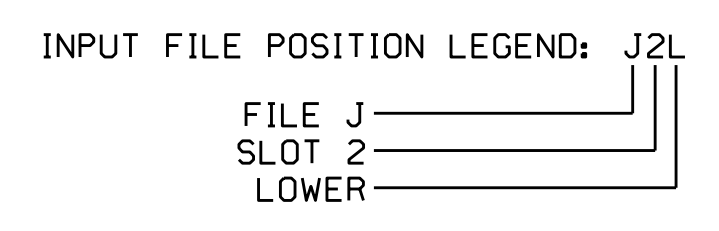
INPUT FILE POSITION LAYOUT



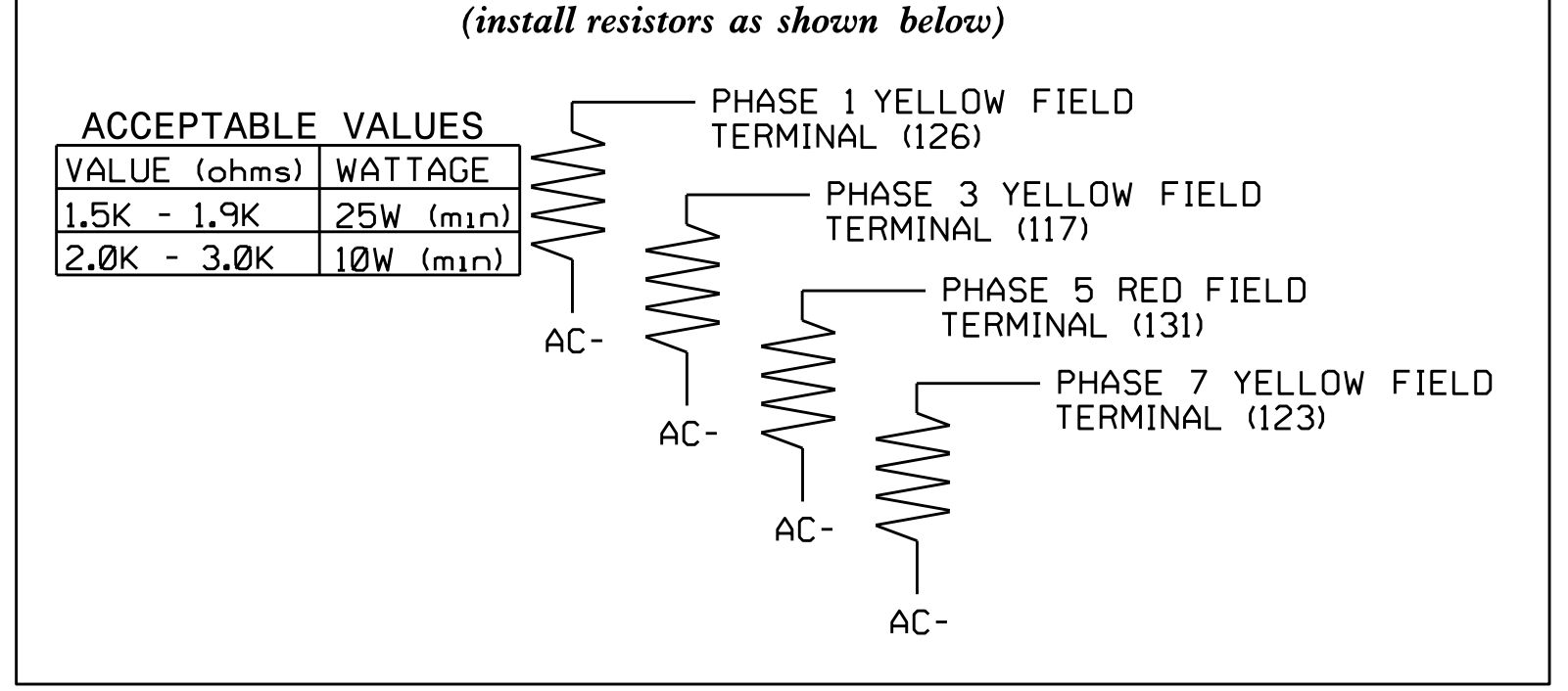
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
	TB4-5,6	I5U	58	20	3	3	Y	Y			15
3A ²	-	J8U	50	12	28	8	Y	Y			
	TB4-9,10	I6U	41	3	4	4	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4/5	Y	Y			10
5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A ⁴	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
*S32	TB7-9,10	J9U	59	21	15	SYS					

- Add jumper from I1-W to J4-W, on rear of input file.
 - Add jumper from I5-W to J8-W, on rear of input file.
 - Add jumper from J1-W to I4-W, on rear of input file.
 - Add jumper from J5-W to I8-W, on rear of input file.
- * System detector only. Remove the vehicle phase assigned to this detector in the default programming.



LOAD RESISTOR INSTALLATION DETAIL



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1338T
 DESIGNED: March 2015
 SEALED: 4/16/2015
 REVISED: N/A

Electrical Detail - Temporary - Sheet 1 of 2

Electrical and Programming Details For: SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)

Division 10 Mecklenburg County Pineville

PLAN DATE: April 2015 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Prepared In the Offices of: Transportation Mobility and Safety Solutions, Inc. (North Carolina Professional Engineer Seal: GEORGE C. BROWN, SEAL 022013)

750 N. Greenfield Pkwy, Garner, NC 27529

Signed by: George C. Brown 4/22/2015

SIG. INVENTORY NO. 10-1338T

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #49 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #48 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #41 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #40 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

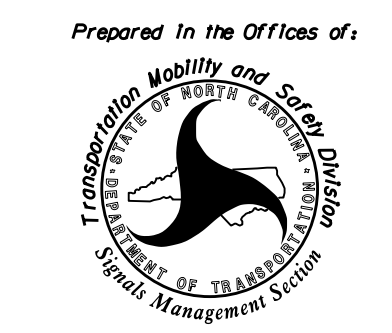
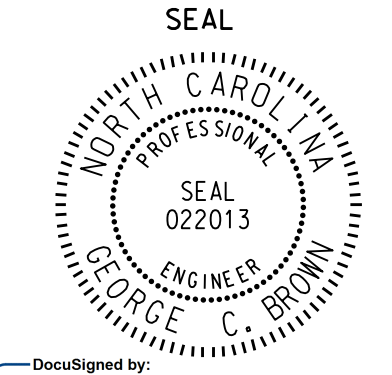
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 39 =	Overlap D Red
OUTPUT 40 =	Overlap D Yellow
OUTPUT 41 =	Overlap D Green
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 47 =	Overlap B Red
OUTPUT 48 =	Overlap B Yellow
OUTPUT 49 =	Overlap B Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

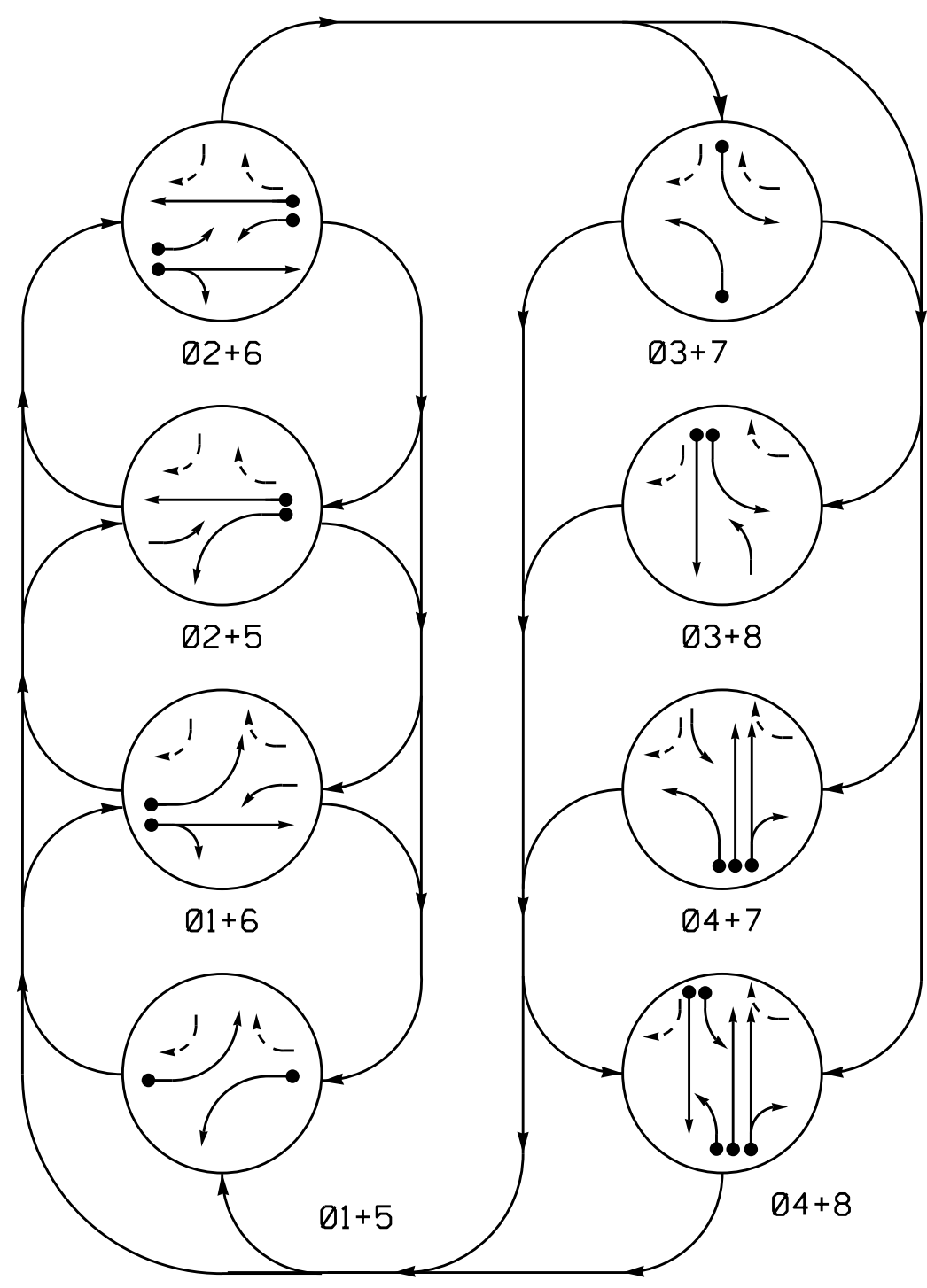
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1338T
DESIGNED: March 2015
SEALED: 4/16/2015
REVISED: N/A

Electrical Design - Temporary - Sheet 2 of 2

	<p>Prepared In the Offices of:</p> <p>SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)</p>	<p>SEAL</p> 						
<p>Division 10 Mecklenburg County Pineville</p>	<p>PLAN DATE: March 2015 REVIEWED BY: T. Joyce</p> <p>PREPARED BY: C. Strickland REVIEWED BY:</p>	<p>DocuSigned by: George C. Brown 4/22/2015</p>						
<p>750 N. Grantfield Pkwy, Garner, NC 27529</p>	<table border="1" style="width: 100%;"> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </table>	REVISIONS	INIT.	DATE				<p>SIG. INVENTORY NO. 10-1338T</p>
REVISIONS	INIT.	DATE						

02-10-2015 09:55
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 08317.cwk\and

PHASING DIAGRAM



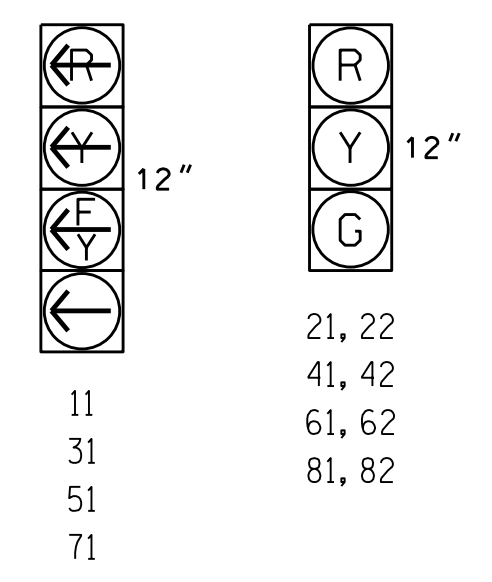
PHASING DIAGRAM DETECTION LEGEND

- ←●→ DETECTED MOVEMENT
- ←○→ UNDETECTED MOVEMENT (OVERLAP)
- ←- - - UNSIGNALIZED MOVEMENT
- ←- - - PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21, 22	R	R	G	G	R	R	R	R
31	←	←	←	←	←	←	←	←
41, 42	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61, 62	R	G	R	G	R	R	R	R
71	←	←	←	←	←	←	←	←
81, 82	R	R	R	R	R	G	R	G

SIGNAL FACE I.D.

All Heads L.E.D.



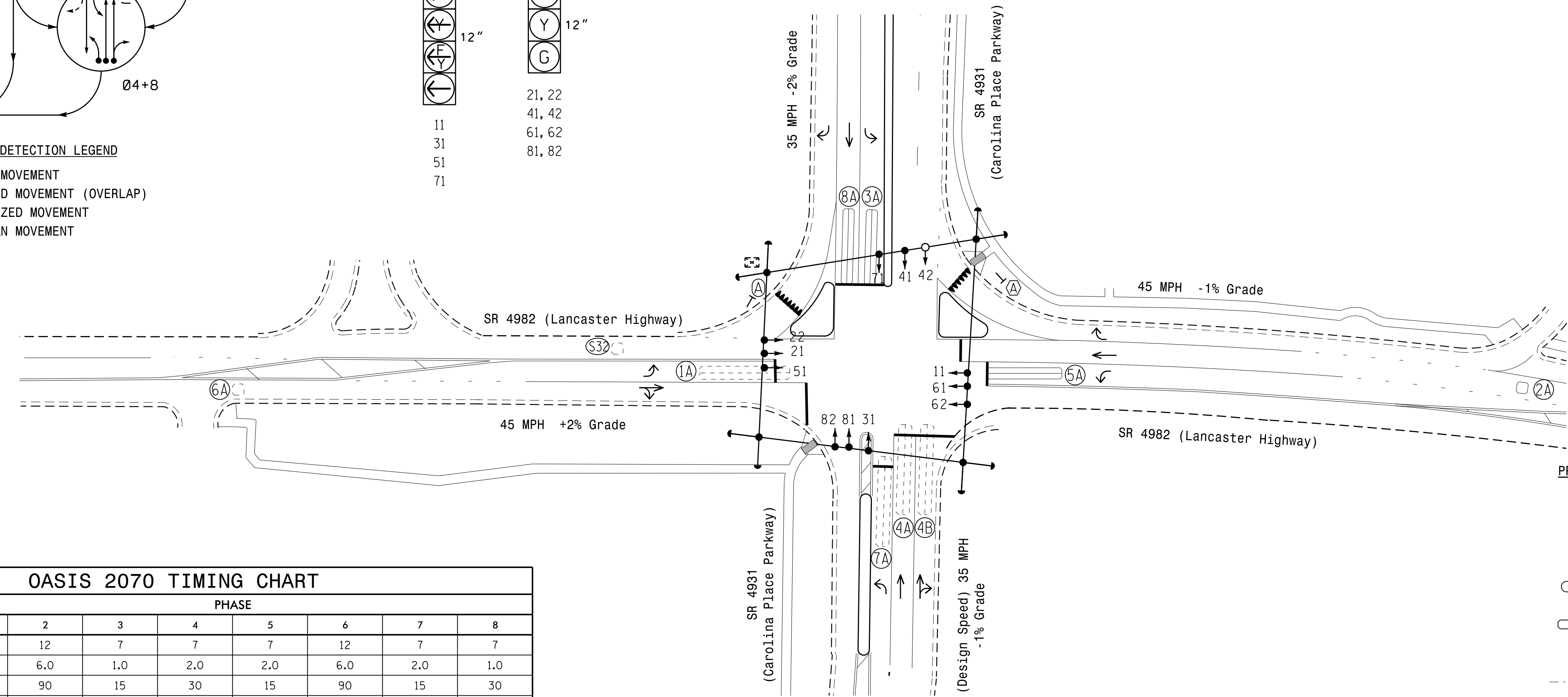
OASIS 2070 LOOP & DETECTOR INSTALLATION

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING							
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
1A	6X40	0	2-4-2	-	1	Y	Y	-	-	15	-	-
2A	6X6	300	5	Y	2	Y	Y	-	-	3	-	-
3A	6X60	+5	2-4-2	Y	3	Y	Y	-	-	15	-	-
4A	6X40	0	2-4-2	-	4	Y	Y	-	-	-	-	-
4B	6X40	0	2-4-2	-	4	Y	Y	-	-	10	-	-
5A	6X40	+15	2-4-2	Y	5	Y	Y	-	-	15	-	-
6A	6X6	300	5	-	6	Y	Y	-	-	3	-	-
7A	6X60	+15	2-4-2	-	7	Y	Y	-	-	15	-	-
8A	6X40	+5	2-4-2	Y	8	Y	Y	-	-	-	-	-
S32	6X6	+180	EXIST	-	-	-	-	-	-	-	Y	-

8 Phase Fully Actuated NC 51 (Pineville) CLS

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Set all detector units to presence mode.
6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
7. Closed loop system data: Controller Asset # 1338.
8. Remove signs "B" from Temporary Design.



OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1*	7	12	7	7	7	12	7	7
Extension 1*	2.0	6.0	1.0	2.0	2.0	6.0	2.0	1.0
Max Green 1*	15	90	15	30	15	90	15	30
Yellow Clearance	3.0	4.6	3.0	4.0	3.0	4.6	3.0	4.0
Red Clearance	2.3	1.1	2.8	1.8	2.6	1.1	2.3	1.8
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1*	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation*	-	2.5	-	-	-	2.5	-	-
Max Variable Initial*	-	34	-	-	-	34	-	-
Time Before Reduction*	-	30	-	-	-	30	-	-
Time To Reduce*	-	15	-	-	-	15	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND

- | | | | |
|--|---|--|-----------------------------------|
| | PROPOSED Traffic Signal Head | | EXISTING Traffic Signal Head |
| | PROPOSED Modified Signal Head | | EXISTING N/A |
| | PROPOSED Sign | | EXISTING N/A |
| | PROPOSED Pedestrian Signal Head With Push Button & Sign | | EXISTING N/A |
| | PROPOSED Signal Pole with Guy | | EXISTING N/A |
| | PROPOSED Signal Pole with Sidewalk Guy | | EXISTING N/A |
| | PROPOSED Inductive Loop Detector | | EXISTING Inductive Loop Detector |
| | PROPOSED Controller & Cabinet | | EXISTING Controller & Cabinet |
| | PROPOSED Junction Box | | EXISTING Junction Box |
| | PROPOSED 2-in Underground Conduit | | EXISTING 2-in Underground Conduit |
| | PROPOSED Right of Way | | EXISTING Right of Way |
| | PROPOSED Directional Arrow | | EXISTING Directional Arrow |
| | PROPOSED "YIELD" Sign (R1-2) | | EXISTING "YIELD" Sign (R1-2) |

Signal Upgrade - Final Design

750 N. Greenfield Pkwy, Garner, NC 27529

SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)

Division 10 Mecklenburg County Pineville

PLAN DATE: March 2015 REVIEWED BY: C. Pierce

PREPARED BY: C. Pierce REVIEWED BY: S. O. Williams

REVISIONS: _____ INIT: _____ DATE: _____

SEAL

SEAL 24393

ENGINEER

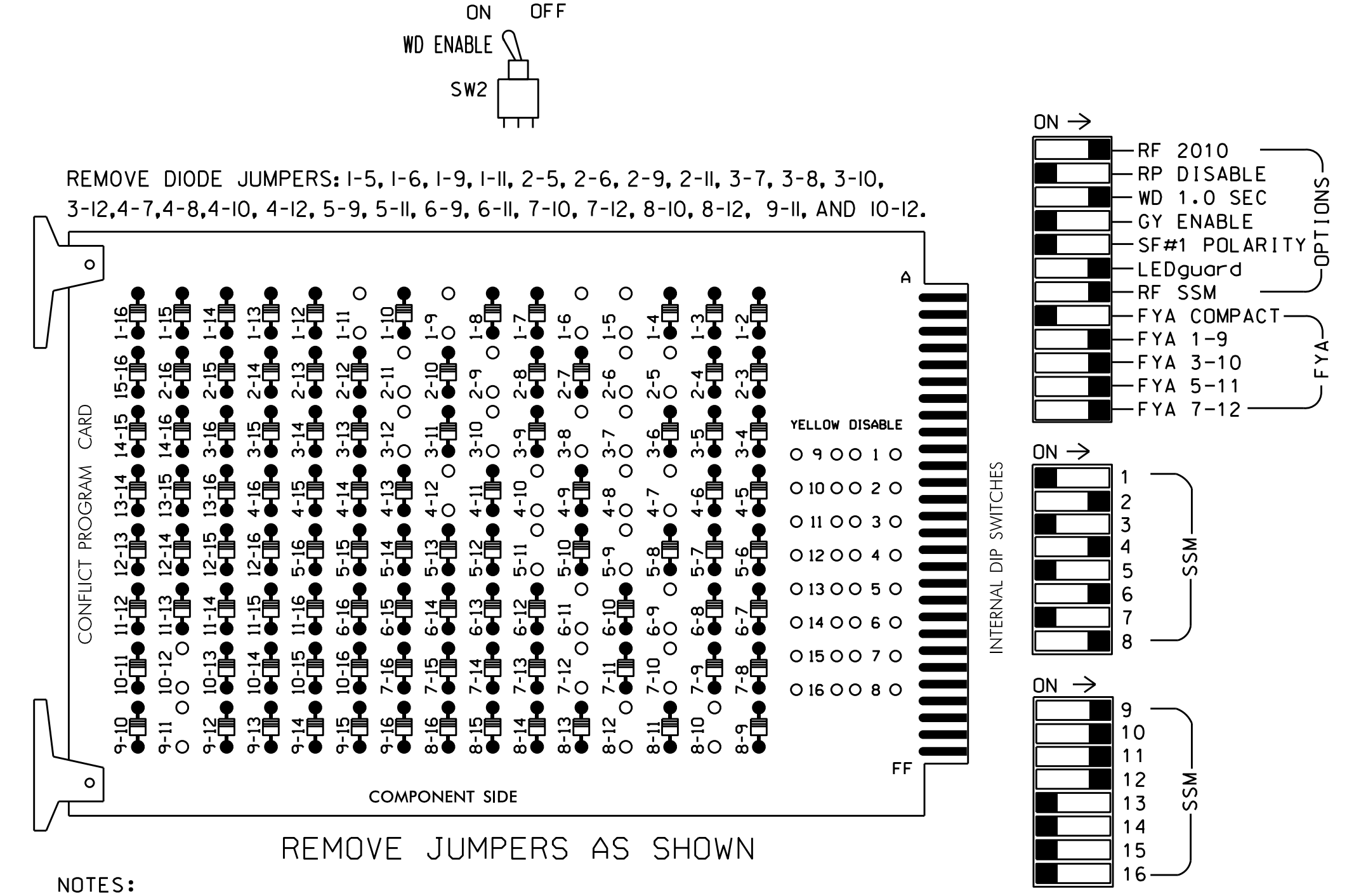
THOMAS J. WILLIAMS

4/16/2015

SIG. INVENTORY NO. 10-1338

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS: 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11, AND 10-12.

REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 1,3,5,7,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the NC 51 (Pineville) System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2P	3	4	4P	5	6	6P	7	8	8P	9	10	11	12	13	14
SIGNAL HEAD NO.	11★	21,22	NU	31★	41,42	NU	51★	61,62	NU	71★	81,82	NU	11★	31★	NU	51★	71★	NU
RED		128		101			134			107								
YELLOW	*	129		* 102			* 135			* 108								
GREEN		130		103			136			109								
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127			118			133			124								

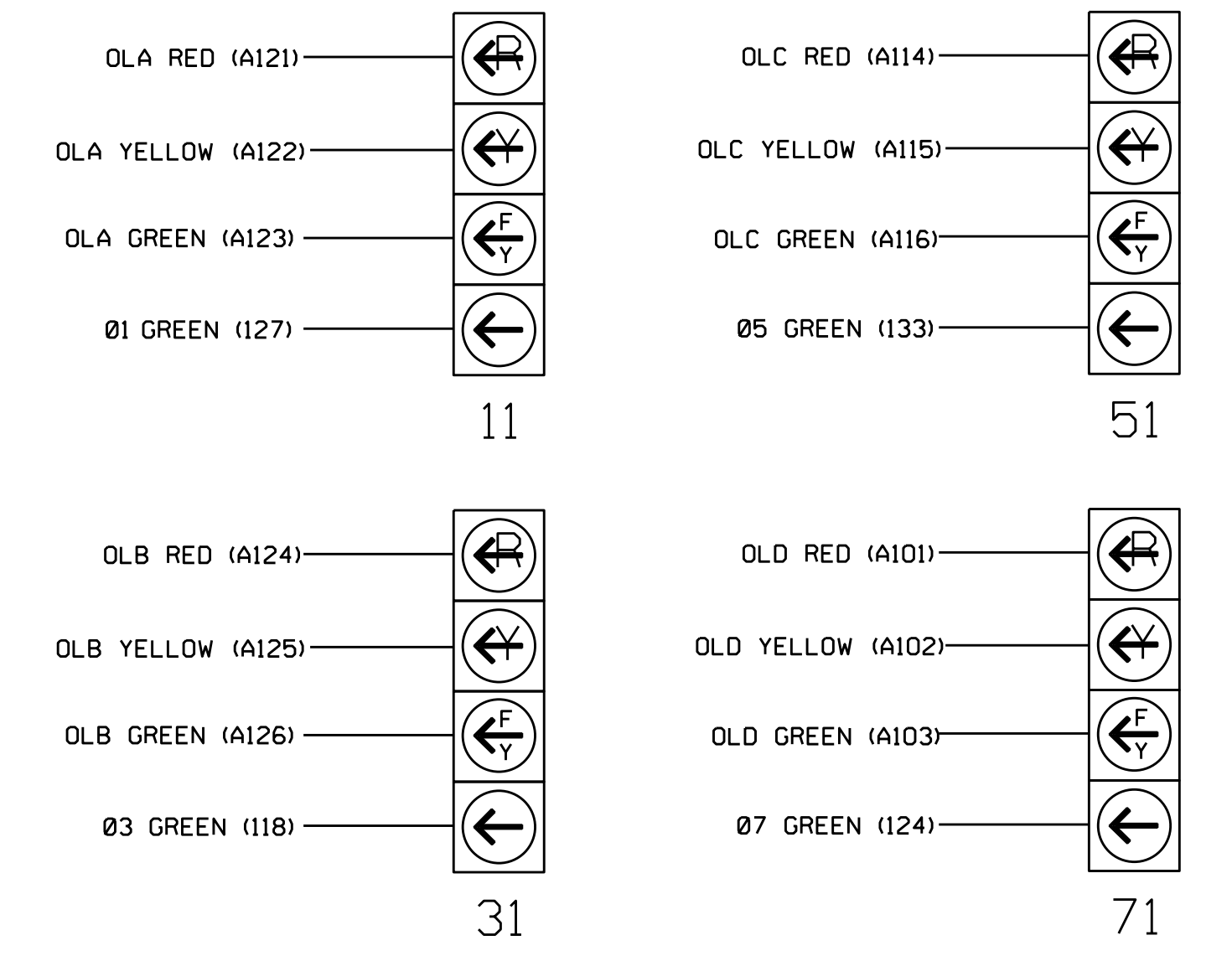
NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,
 S12,S13
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)

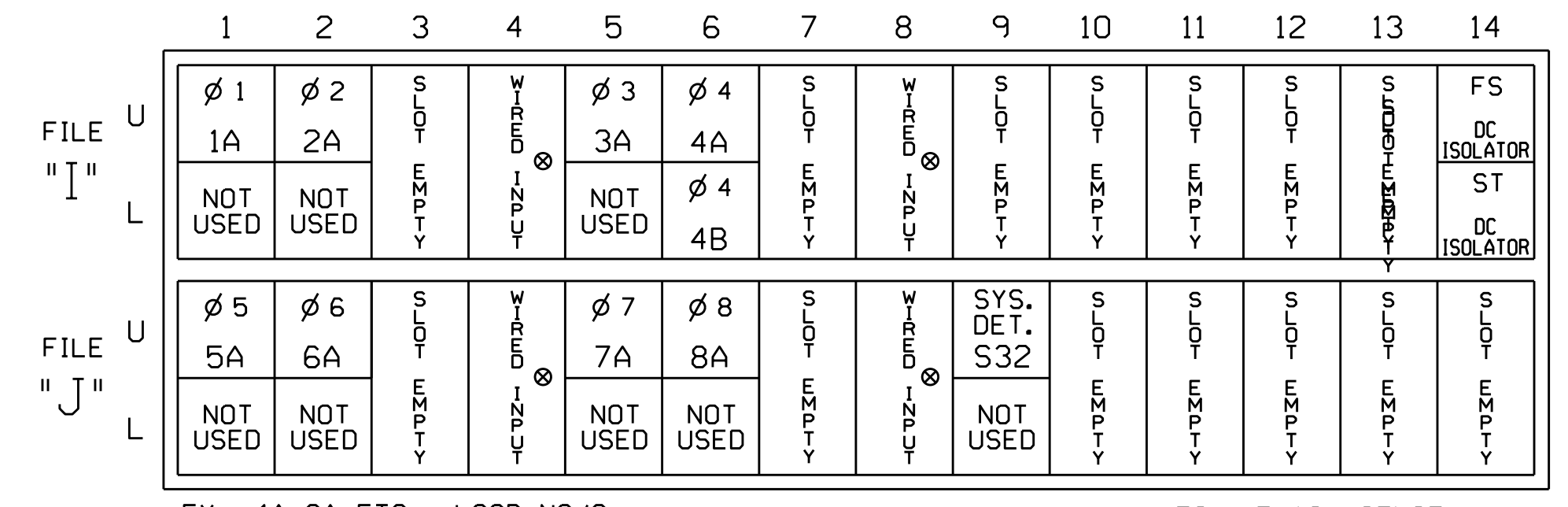


NOTE

- The sequence display for these signals require special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

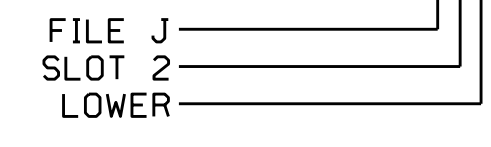
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
	TB4-5,6	I5U	58	20	3	3	Y	Y			15
3A ²	-	J8U	50	12	28	8	Y	Y			
	TB4-9,10	I6U	41	3	4	4	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A ⁴	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
*S32	TB7-9,10	J9U	59	21	15	SYS					

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

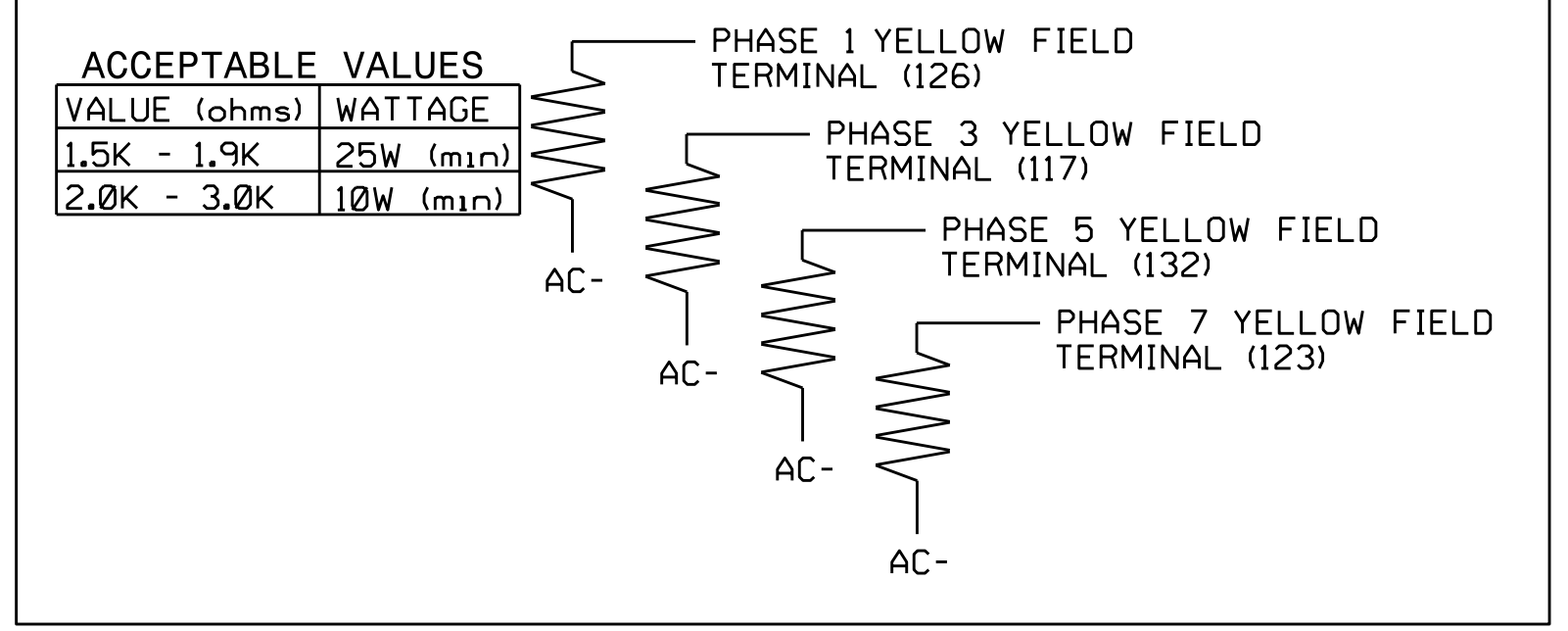
* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

Electrical Detail - Sheet 1 of 2

Electrical and Programming Details For: SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)

Prepared in the Offices of: **Transylvania Mobility and Safety Solutions**

Division 10 Mecklenburg County Pineville

PLAN DATE: April 2015 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

SEAL: GEORGE C. BROWN, PROFESSIONAL ENGINEER, No. 022013

DocuSigned by: George C. Brown 4/22/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 10-1338

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #49 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #48 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #41 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON

↓
SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #40 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH: XX
VEH OVL NOT PED: XX
VEH OVL GRN EXT: XX
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

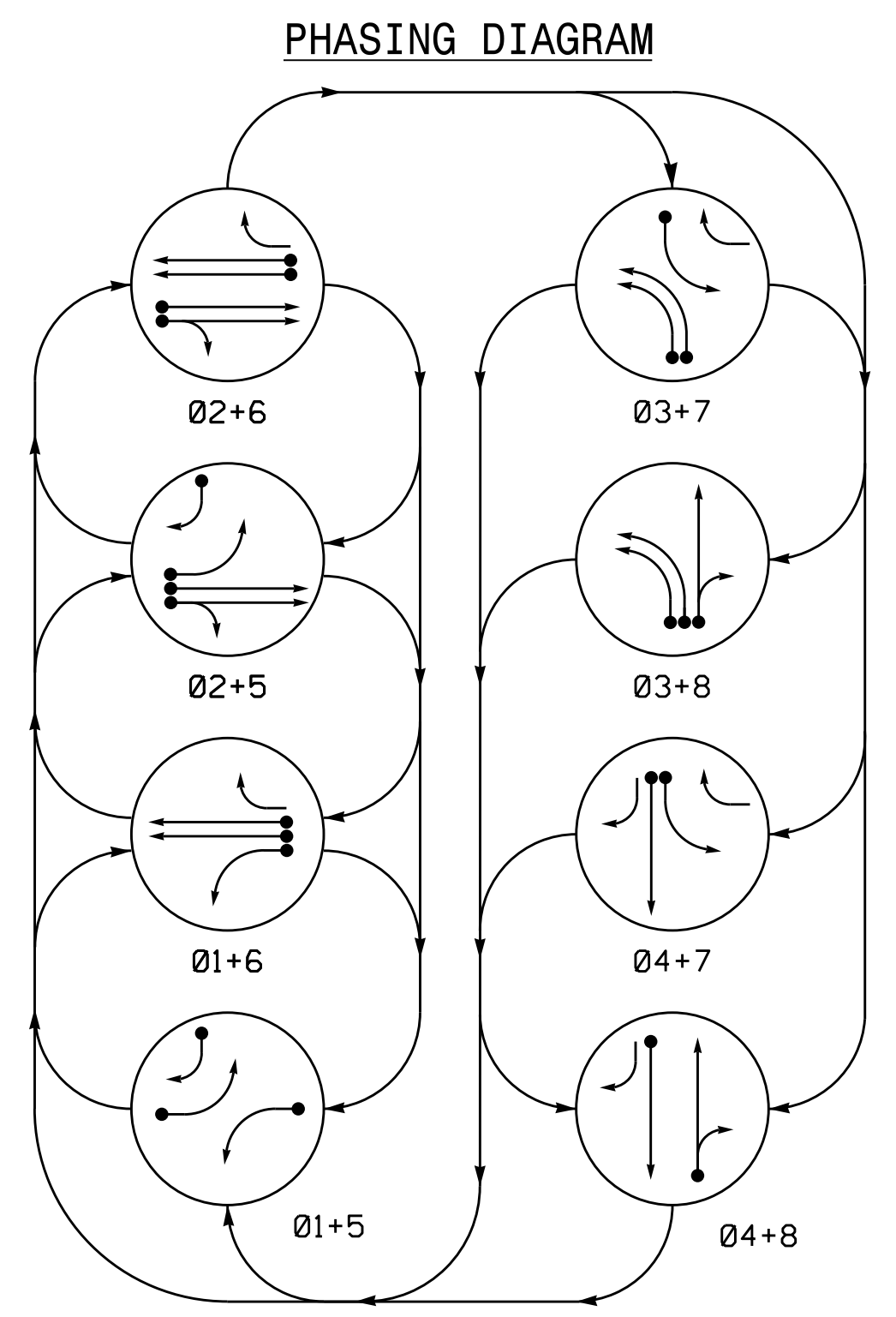
OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 39 =	Overlap D Red
OUTPUT 40 =	Overlap D Yellow
OUTPUT 41 =	Overlap D Green
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 47 =	Overlap B Red
OUTPUT 48 =	Overlap B Yellow
OUTPUT 49 =	Overlap B Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1338
DESIGNED: March 2015
SEALED: 4/16/2015
REVISED: N/A

Electrical Design - Sheet 2 of 2

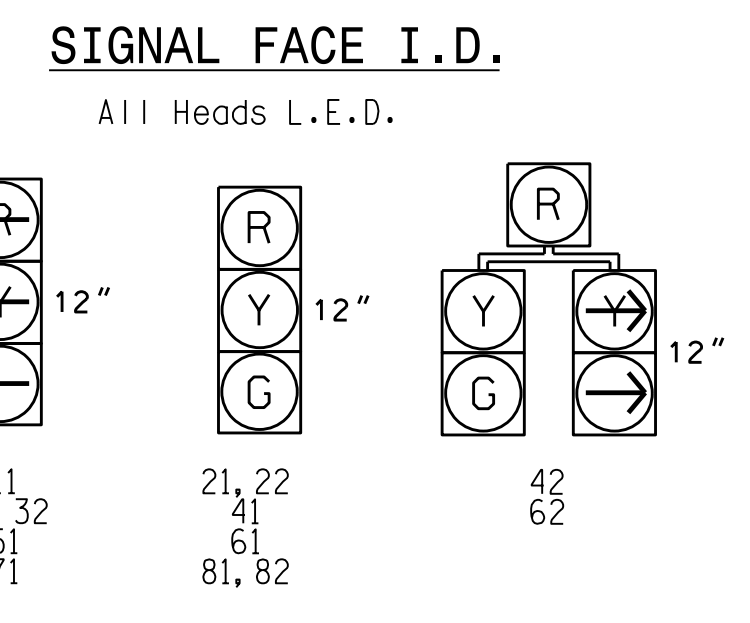
	DETAILS FOR: SR 4982 (Lancaster Highway) at SR 4931 (Carolina Place Parkway)		SEAL PROFESSIONAL ENGINEER GEORGE C. BROWN SEAL 022013
	Division 10 Mecklenburg County Pineville	PLAN DATE: March 2015 REVIEWED BY: T. Joyce	
REVISIONS		INIT. DATE	DocuSigned by: George C. Brown 4/22/2015 F12901ED08E8434 DATE
750 N. Grantfield Pkwy, Garner, NC 27529		SIG. INVENTORY NO. 10-1338	DATE

C:\Users\jg515\Documents\10-1338_sme.le.xxx.dgn
 2015-04-16 09:57
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PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- - - UNSIGNALIZED MOVEMENT
- - - PEDESTRIAN MOVEMENT



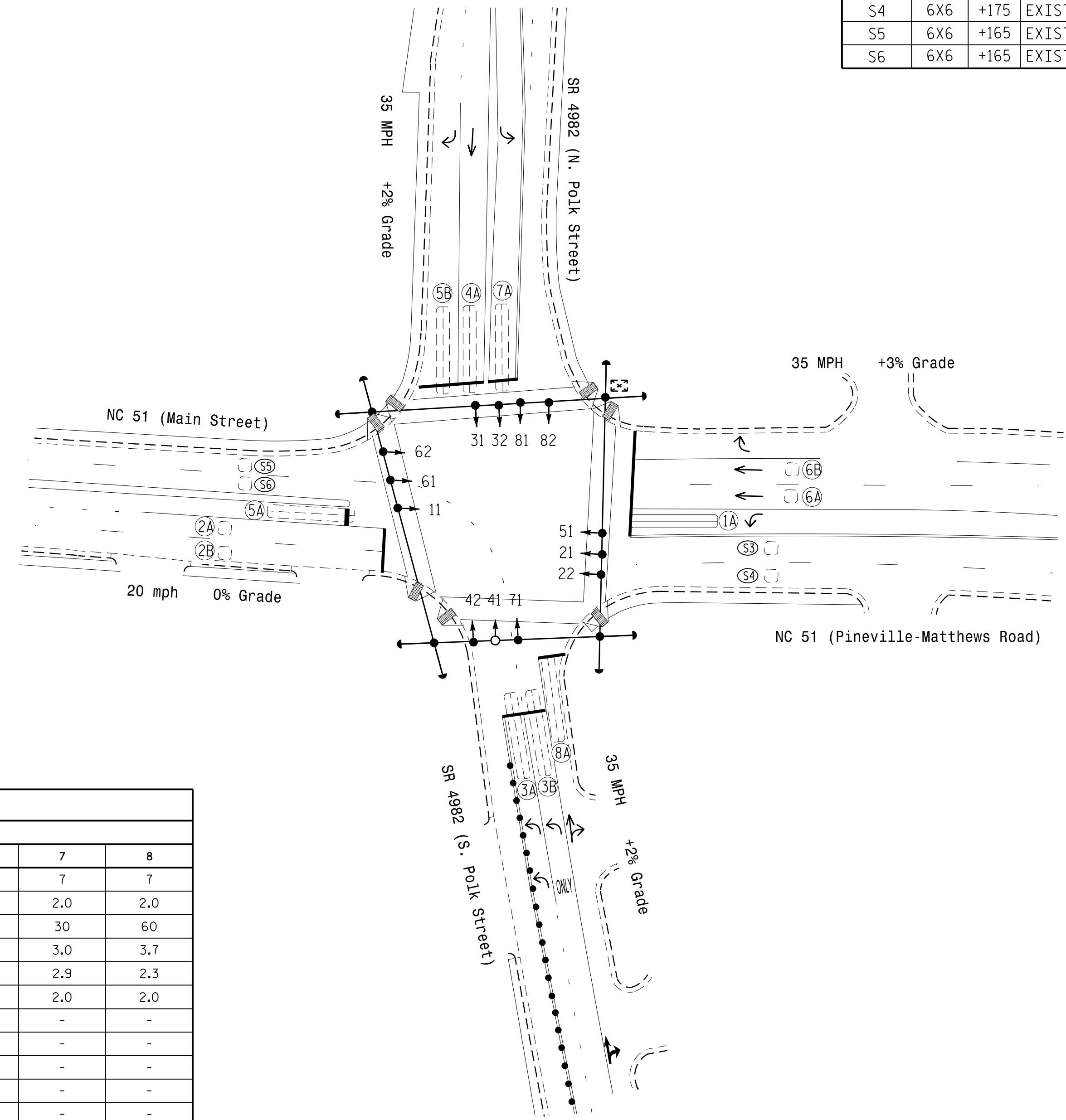
SIGNAL FACE	PHASE							
	Ø1+5	Ø2+6	Ø2+5	Ø3+7	Ø3+8	Ø4+7	Ø4+8	PEDEST
11	—	—	—	—	—	—	—	—
21, 22	R	R	G	G	R	R	R	Y
31, 32	—	—	—	—	—	—	—	—
41	R	R	R	R	R	R	G	G
42	R	R	R	R	R	R	G	G
51	—	—	—	—	—	—	—	—
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	—	—	—	—	—	—	—	—
81,82	R	R	R	R	R	G	R	G

OASIS 2070 LOOP & DETECTOR INSTALLATION												
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING						SYSTEM LOOP	NEW CARD
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME		
1A	6X40	0	2-4-2	Y	1	Y	Y	-	-	3	-	-
2A	6X6	70	EXIST	-	2	Y	Y	-	-	-	-	-
2B	6X6	70	EXIST	-	2	Y	Y	-	-	-	-	-
3A	6X40	+10	2-4-2	-	3	Y	Y	-	-	3	-	-
3B	6X40	+10	2-4-2	-	3	Y	Y	-	-	-	-	-
4A	6X40	+5	2-4-2	-	4	Y	Y	-	-	-	-	-
5A	6X40	+5	2-4-2	-	5	Y	Y	-	-	3	-	-
5B	6X40	+5	2-4-2	-	5	Y	Y	-	-	15	-	-
6A	6X6	70	EXIST	-	6	Y	Y	-	-	-	-	-
6B	6X6	70	EXIST	-	6	Y	Y	-	-	-	-	-
7A	6X40	+5	2-4-2	-	7	Y	Y	-	-	3	-	-
8A	6X40	+5	2-4-2	-	8	Y	Y	-	-	10	-	-
S3	6X6	+175	EXIST	-	-	-	-	-	-	-	Y	-
S4	6X6	+175	EXIST	-	-	-	-	-	-	-	Y	-
S5	6X6	+165	EXIST	-	-	-	-	-	-	-	Y	-
S6	6X6	+165	EXIST	-	-	-	-	-	-	-	Y	-

8 Phase Fully Actuated W/Emergency Preemption NC 51 (Pineville) CLS

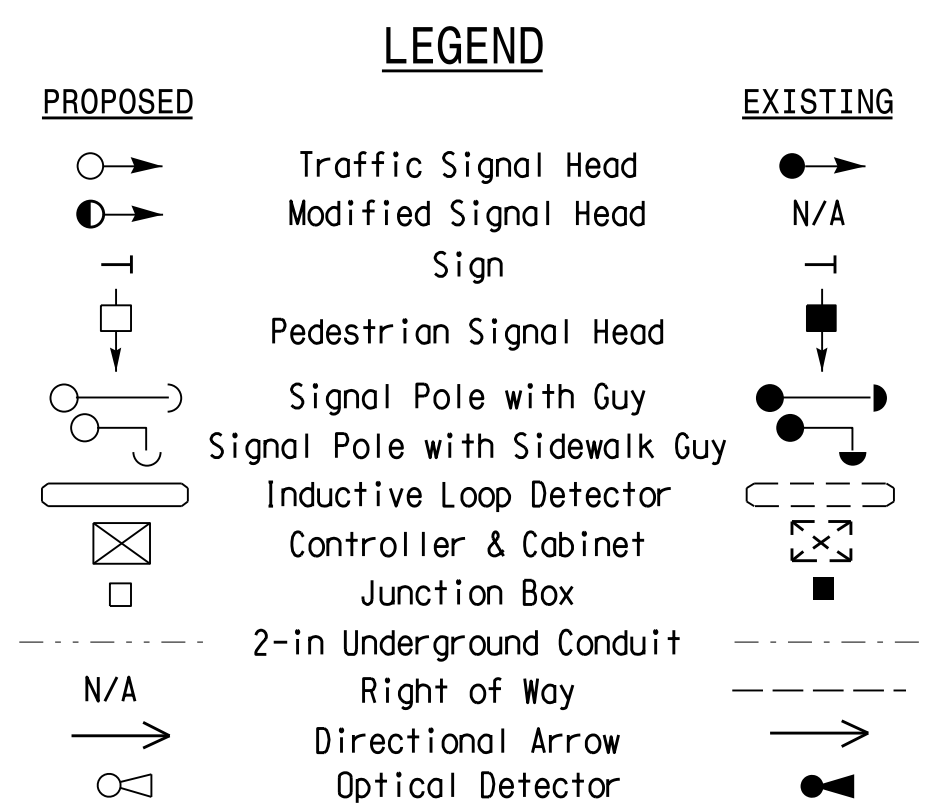
NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Intersection Number # 254.



FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	10	7	7	7	10	7	7
Extension 1 *	2.0	3.0	2.0	2.0	2.0	3.0	2.0	2.0
Max Green 1 *	25	90	25	60	15	90	30	60
Yellow Clearance	3.0	3.0	3.0	3.7	3.0	3.7	3.0	3.7
Red Clearance	2.8	3.2	3.6	2.0	3.2	2.0	2.9	2.3
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	-	-	-	-	-	-	-
Max Variable Initial *	-	-	-	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-	-	-	-
Time To Reduce *	-	-	-	-	-	-	-	-
Minimum Gap	-	-	-	-	-	-	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	-	-	-	-	-
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Final Design

NC 51 (Main Street) /
NC 51 (Pineville-Matthews Road)
at
SR 4982 (Polk Street)
Pineville, Mecklenburg County

PLAN DATE: March 2015 REVIEWED BY: C. Pierce

750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 1" = 40'

SEAL

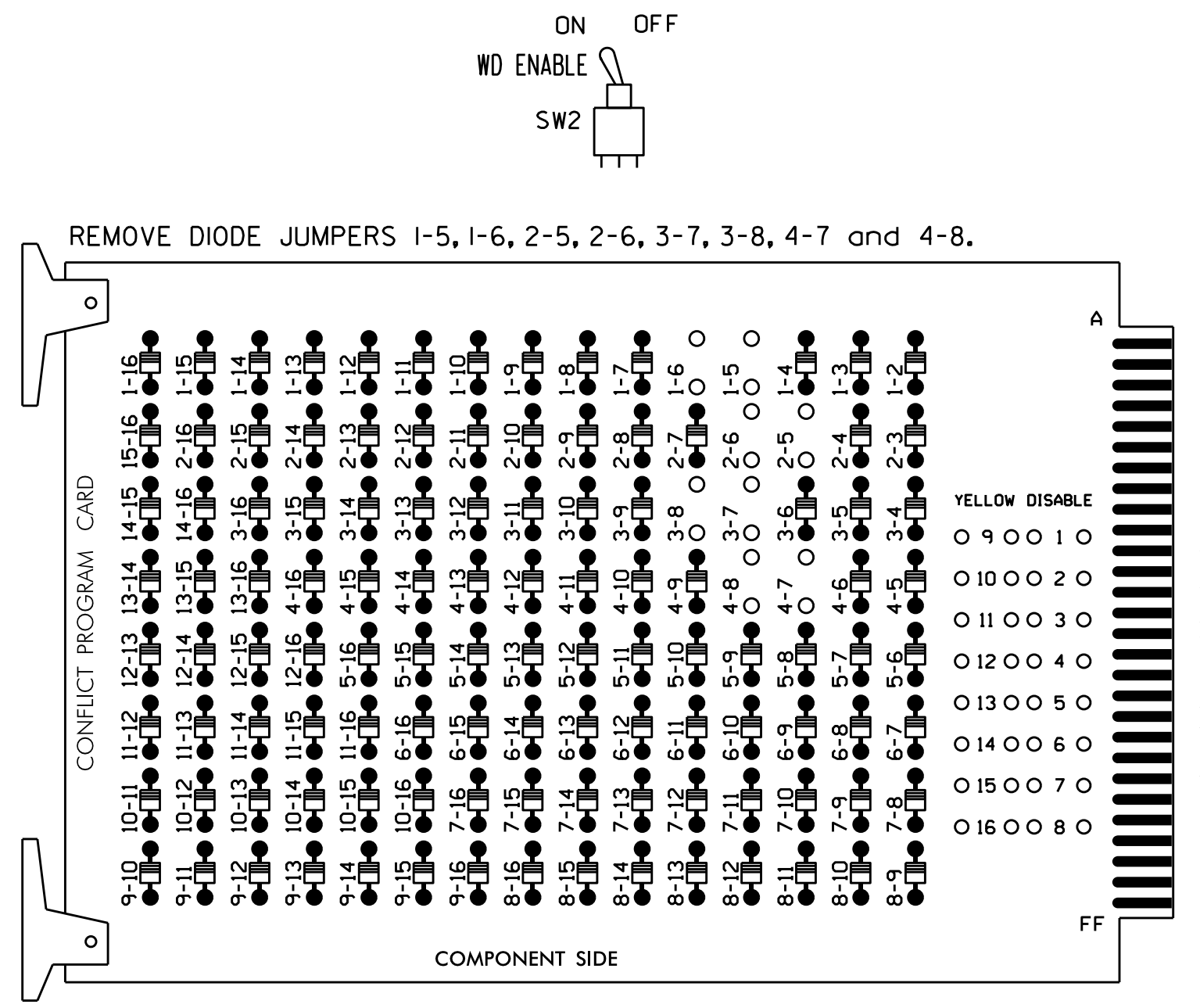
4/16/2015

SIG. INVENTORY NO. 10-0254

16-Apr-2015 09:27
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 C:\Users\cspence

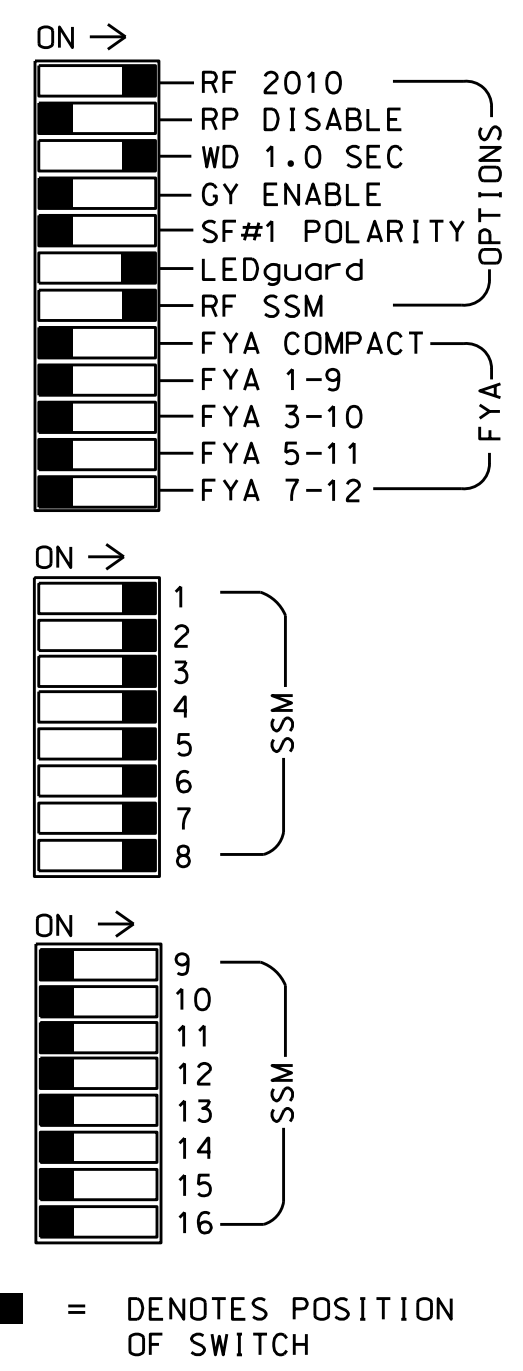
EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 9,10, 11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.
- The cabinet and controller are part of the NC 51 (Pineville) Closed Loop System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31,32	41,42	NU	42	51	61,62	NU	62	71	81,82	NU	NU	NU	NU	NU
RED		128			101				134									
YELLOW		129			102				135									
GREEN		130			103				136									
RED ARROW	125			116				131				122						
YELLOW ARROW	126			117				132	132			123	123					
GREEN ARROW	127			118				133	133			124	124					

NU = Not Used

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET332
 SOFTWAREECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 (12-STD, 6-AUX)
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAPS.....NONE

PHASE SEQUENCE PROGRAMMING DETAIL

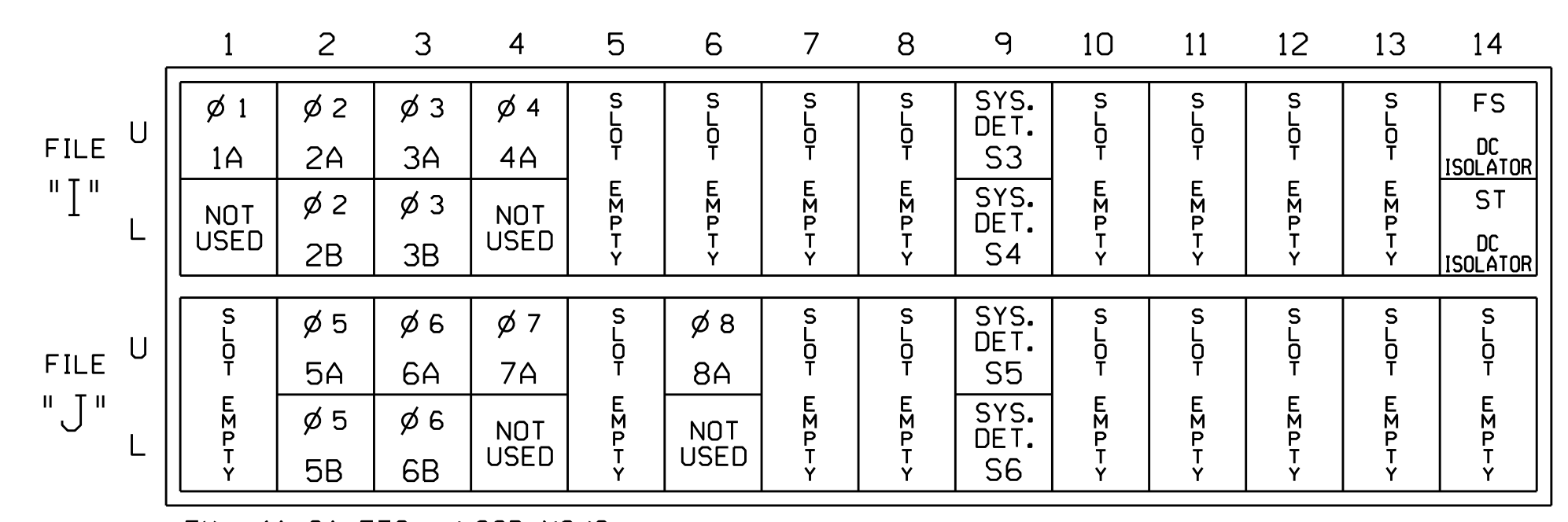
(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE: PAGE 1 NEXT: PAGES)	RNG:LEAD	BARRIER 1	X-LAG:LEAD	BARRIER 2	X-LAG:LEAD	BARRIER 3	X-LAG
1	1	2	0	3	4	0	0
2	5	6	0	7	8	0	0
3	0	0	0	0	0	0	0
4	0	0	0	0	0	0	0

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

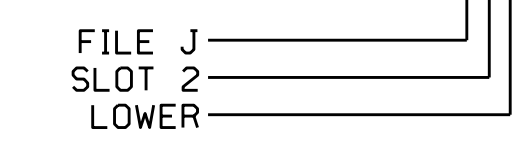
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB2-9,10	I3U	63	25	32	3	Y	Y			3
3B	TB2-11,12	I3L	76	38	42	3	Y	Y			
4A	TB4-1,2	I4U	47	9	22	4	Y	Y			
5A	TB3-5,6	J2U	40	2	6	5	Y	Y			3
5B	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
7A	TB5-1,2	J4U	48	10	26	7	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
* S3	TB6-9,10	I9U	60	22	11	SYS					
* S4	TB6-11,12	I9L	62	24	13	SYS					
* S5	TB7-9,10	J9U	59	21	15	SYS					
* S6	TB7-11,12	J9L	61	23	17	SYS					

* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-0254
 DESIGNED: March 2015
 SEALED: 4/16/2015
 REVISED: N/A

Electrical Detail

Electrical and Programming Details For: NC 51 (Main Street)/ NC 51 (Pineville-Matthews Road) at SR 4982 (Polk Street)

Division 10 Mecklenburg County Pineville

PLAN DATE: April 2015 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

750 N. Greenfield Pkwy, Garner, NC 27529

Seal: George C. Brown, Professional Engineer, No. 022013, State of North Carolina

DocuSigned by: George C. Brown 4/22/2015

SIG. INVENTORY NO. 10-0254

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 2015-04-16 11:15
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