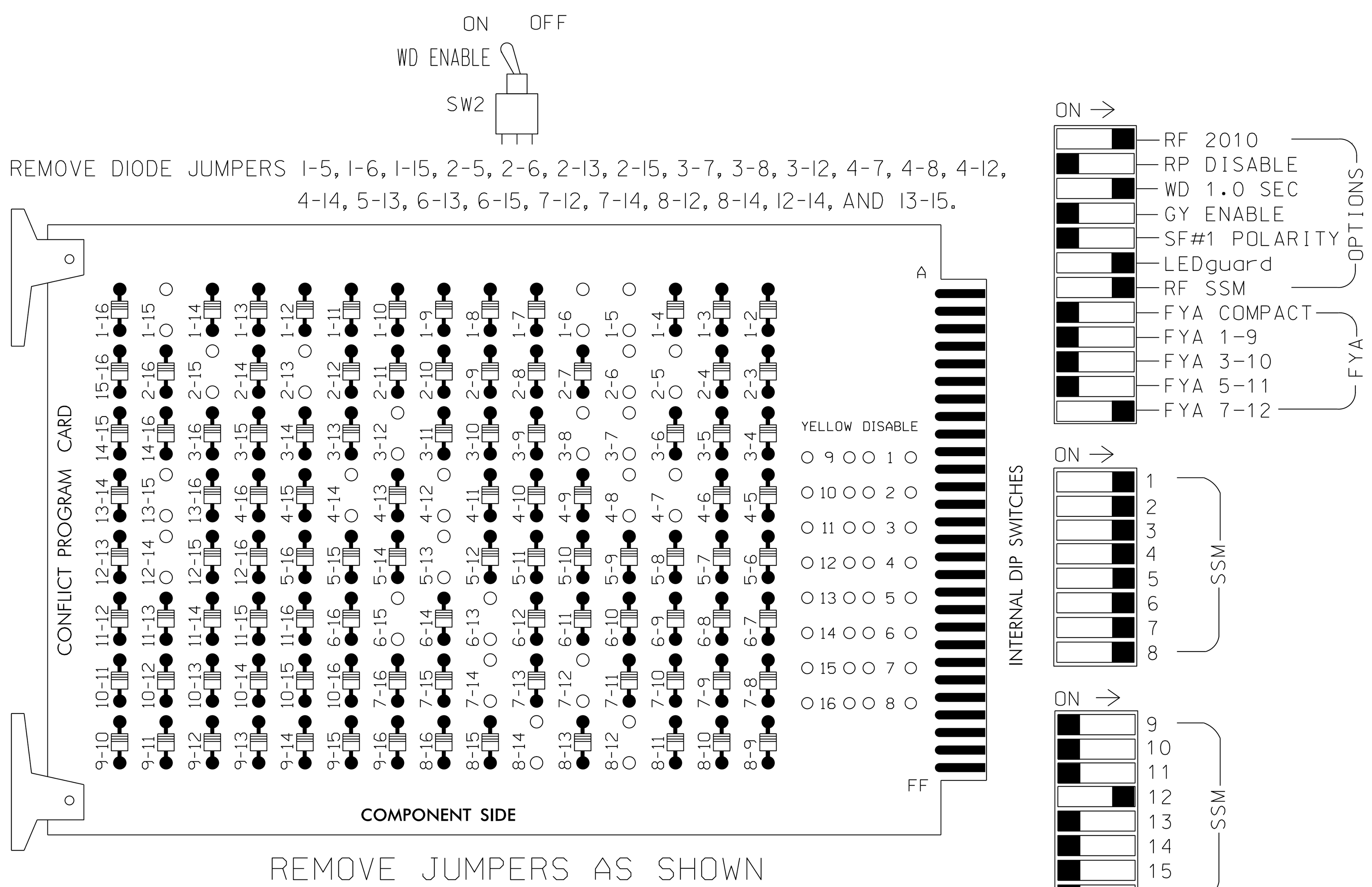
PROJECT REFERENCE NO.	SHEET NO.
W-5514	Sig.9.1

EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-15, 2-5, 2-6, 2-13, 2-15, 3-7, 3-8, 3-12, 4-7, 4-8, 4-12, 4-14, 5-13, 6-13, 6-15, 7-12, 7-14, 8-12, 8-14, 12-14, AND 13-15.



REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 9,10,11, 13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
3. Program phase 8 for Dual Entry.
4. Enable Simultaneous Gap-Out for all phases.
5. Program phases 2 and 6 for Variable Initial and Gap Reduction.
6. Program phases 2 and 6 for Start Up In Green.
7. Program phases 2, 4 and 6 for 'STARTUP PED CALL'.
8. Program phases 2 and 6 for Yellow Flash.
9. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER..............2070L
CABINET.................332 /W/ AUX
SOFTWARE................ECONOLITE OASIS
CABINET MOUNT...........BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED......S1,S2,S2P,S3,S4,S4P,S5,S6,S6P,S7,S8,S13
PHASES USED.............1,2,2PED,3,4,4PED,5,6,6PED,7,8.
OVERLAP "A".............NOT USED
OVERLAP "B".............NOT USED
OVERLAP "C".............NOT USED
OVERLAP "D".............7+8

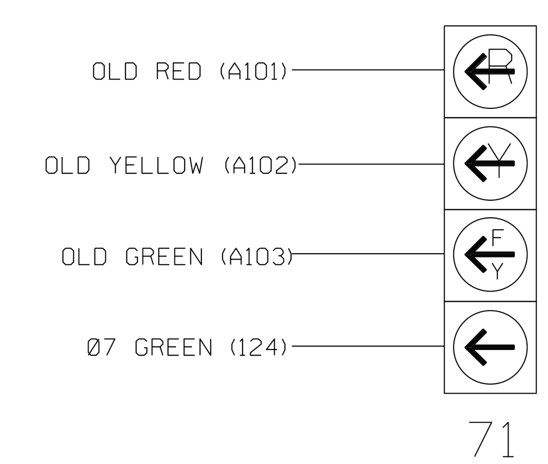
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3		S4	S4P	S5	S6	S6P	S7		S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3		4	4 PED	5	6	6 PED	7		8	8 PED	OLA	OLB	SPARE	OLC	** OLD	SPARE
SIGNAL HEAD NO.	11	21,22 23	P21, P22	22	31,32	41,42	P41, P42	51	61,62 63	P61, P62	71★	63	81,82	NU	NU	NU	NU	NU	71★	NU
RED		128				101			134			*	107							
YELLOW		129				102			135				108							
GREEN		130				103			136				109							
RED ARROW	125				116			131											A101	
YELLOW ARROW	126			117	117			132				123							A102	
FLASHING YELLOW ARROW																			A103	
GREEN ARROW	127			118	118			133			124	124								
(Don't Walk)			113				104			119										
(Walk)			115				106			121										

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail below.
** Flash Note: Rewire Overlap "D" to flash on Flasher unit #2, Circuit #2.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	SLOT EMPTY	Ø2/SYS 2A/S2A	Ø 3 3A	SLOT EMPTY	SLOT EMPTY	Ø 4 4A	SLOT EMPTY	WIRED INPUT ⊗	Ø 1 1A	SLOT EMPTY	SLOT EMPTY	Ø2PED DC ISOLATOR	Ø6PED DC ISOLATOR	FS DC ISOLATOR
	L		Ø2/SYS 2B/S2B	Ø 3 3B			Ø 4 4B			NOT USED			Ø 4PED DC ISOLATOR	NOT USED	ST DC ISOLATOR
FILE "J"	U	SLOT EMPTY	Ø6/SYS 6A/S6A	Ø6/SYS 6C/S6C	SLOT EMPTY	Ø 7 7A	Ø 8 8A	SLOT EMPTY	SLOT EMPTY	Ø 5 5A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L		Ø6/SYS 6B/S6B	NOT USED		NOT USED	NOT USED			NOT USED					

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S2A	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S2B	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
3A	TB2-9,10	I3U	63	25	32	3	Y	Y			3
3B	TB2-11,12	I3L	76	38	42	3	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
1A	TB6-9,10	I9U	60	22	11	1	Y	Y			3
6A/S6A	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S6B	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
6C/S6C	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
7A[1]	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
5A	TB7-9,10	J9U	59	21	15	5	Y	Y			3
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1] Add jumper from J5-W to I8-W, on rear of input file.

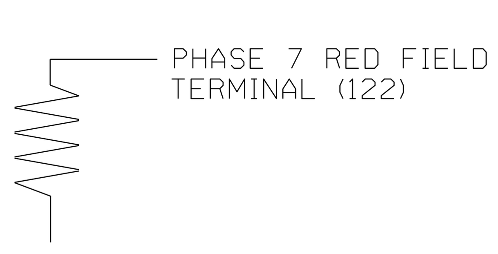
INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



$$$$$SYSTIME$$$$$
$$$$$$$$$$$$$$DGN$$$$$$$$$$$$$$$$
$$$$USERNAME$$$$

4000 WestChase Boulevard, Suite 530
Raleigh, NC 27607
NC License No. C-3705

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0324
DESIGNED: April 2015
SEALED: 4-27-2015
REVISED:

Signal Upgrade ELECTRICAL DETAIL SHEET 1 OF 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

SR 1007 (All American Expy/Owen Dr)
at
SR 1151 (Owen Dr/Walter Reed Rd)

Prepared for the Offices of:
Transportation Mobility and Safety Division
State of North Carolina Department of Transportation
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

Division 06 Cumberland County Fayetteville

PLAN DATE: April 2015 | REVIEWED BY: J.L. Lewis
PREPARED BY: D.J. Darity | VHB PROJECT NO.: 38286.03

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL ENGINEER
SEAL 19713
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2015.04.27
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SIG. INVENTORY NO. 06-0324