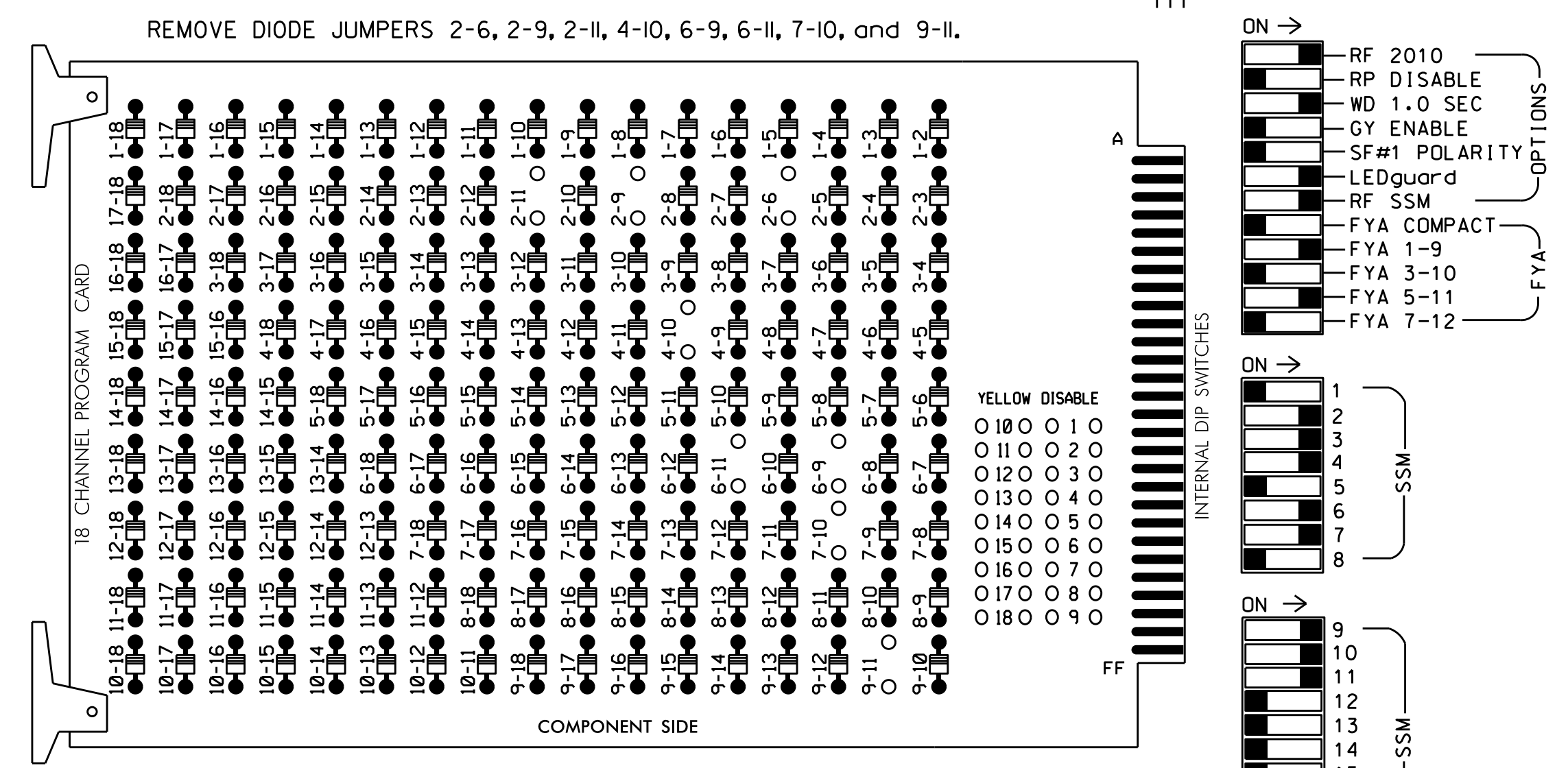


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR
PROGRAMMING DETAIL

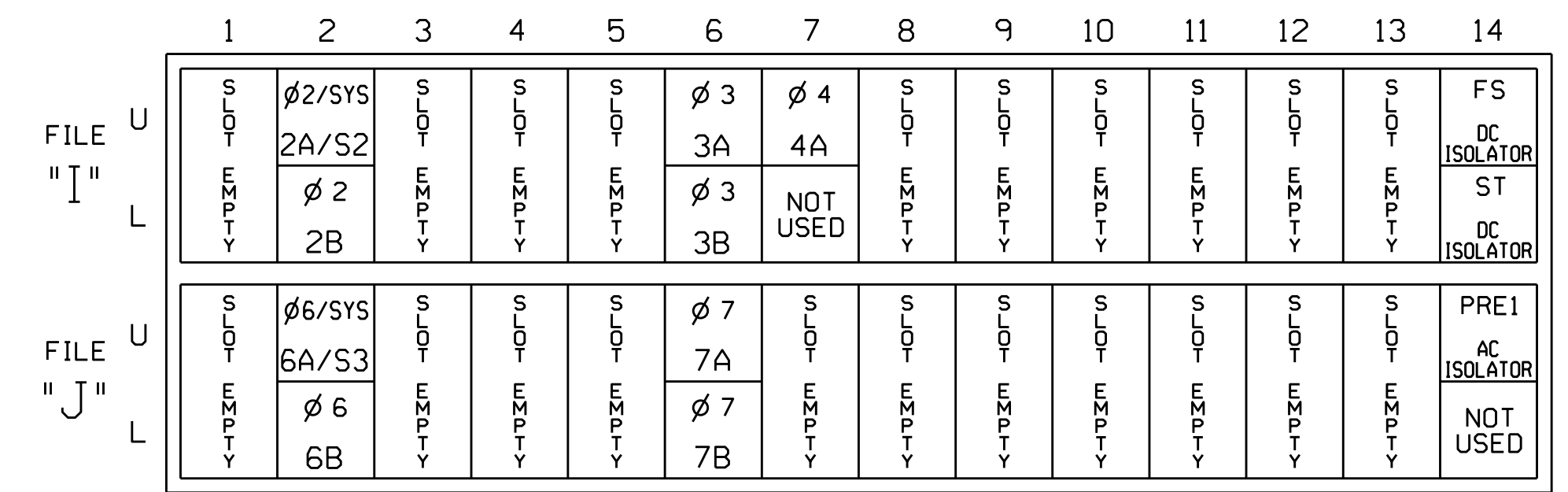
(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

INPUT FILE POSITION LAYOUT
(front view)

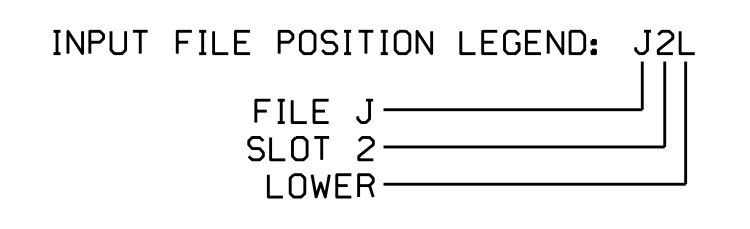


EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME
PRE1 = RR PREEMPT

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S2	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			3
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			15
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
6A/S3	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A	TB5-9,10	J6U	42	4	8	7	Y	Y			3
7B	TB5-11,12	J6L	46	8	18	7	Y	Y	Y		5



SIGNAL HEAD HOOK-UP CHART

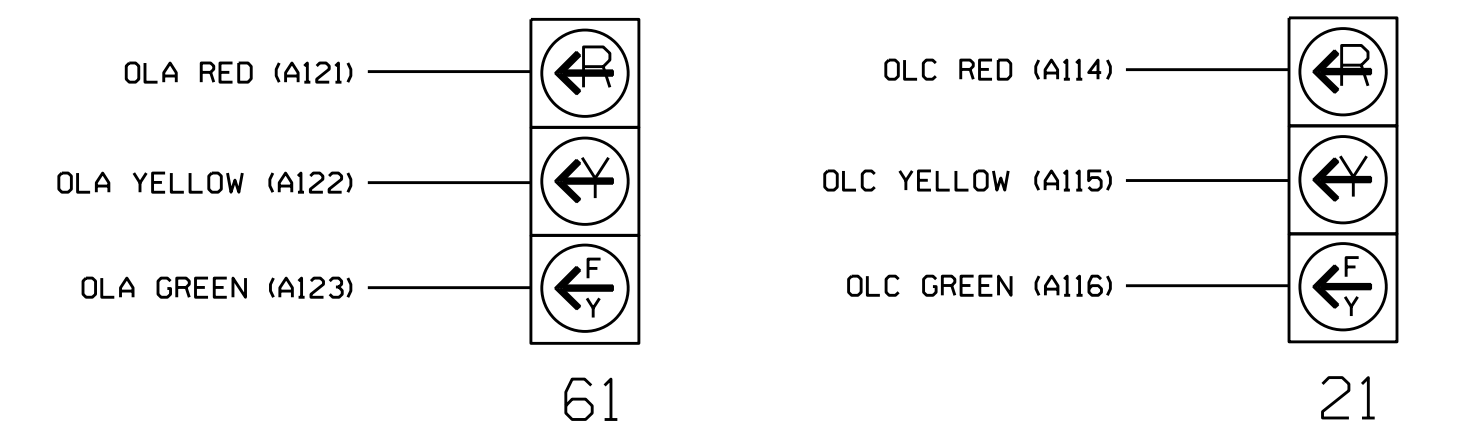
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6						
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18						
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE						
SIGNAL HEAD NO.	NU	22,23	NU	23	31	32,33	41	42	NU	NU	62,63	NU	71	72	NU	NU	61*	91	92	NU	21*	NU	NU	
RED		128		116	116	101	101			134	122	122						A124	A124					
YELLOW		129		117	117	102	102			135	123	123						A125	A125					
GREEN		130		118	118	103	103			136	124	124						A126	A126					
RED ARROW																		A121					A114	
YELLOW ARROW																		A122					A115	
FLASHING YELLOW ARROW																		A123					A116	
GREEN ARROW																							A126	

NU = Not Used
* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail below.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
5. The cabinet and controller are part of the Prospect St. Closed Loop System.

FYA SIGNAL WIRING DETAIL
(wire signal heads as shown)



EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S4,S5,S8,S10,AUX S1,AUX S2,AUX S4
PHASES USED.....2,3,4,6,7,9*
OVERLAP "A".....6
OVERLAP "B".....4+7+9
OVERLAP "C".....2
OVERLAP "D".....NOT USED
* PHASE USED DURING PREEMPT ONLY

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0313
DESIGNED: February 2015
SEALED: 2/19/2015
REVISED: N/A

Electrical Detail - Sheet 1 of 3

	Electrical and Programming Details For:	SR 1619 (Prospect Street) at I-85 Bus/US 29 SB-70 WB Ramp and SR 1621 (Bethel Dr.)	SEAL JOHN T. ROWE, JR. ENGINEER 008453
	Prepared In the Offices of:	Division 8 Randolph County High Point	SEAL JOHN T. ROWE, JR. ENGINEER 008453
PLAN DATE: February 2015	REVIEWED BY: [Signature]	PREPARED BY: S. Armstrong	REVIEWED BY: [Signature]
REVISIONS	INIT.	DATE	Date Signed by: John T. Rowe, Jr. 2/23/2015 DATE
750 N. Greenfield Pkwy, Garner, NC 27529	SIG. INVENTORY NO. 08-0313	DATE	DATE

23-EDP-2015-08-56
 S:\ITS\ASIS\15_Signal\work\hgr\oups\g_Maps\mstr\mstr\0313_sml.elec.xxx.dgn
 S:\ITS\ASIS\15_Signal\work\hgr\oups\g_Maps\mstr\mstr\0313_sml.elec.xxx.dgn
 S:\ITS\ASIS\15_Signal\work\hgr\oups\g_Maps\mstr\mstr\0313_sml.elec.xxx.dgn