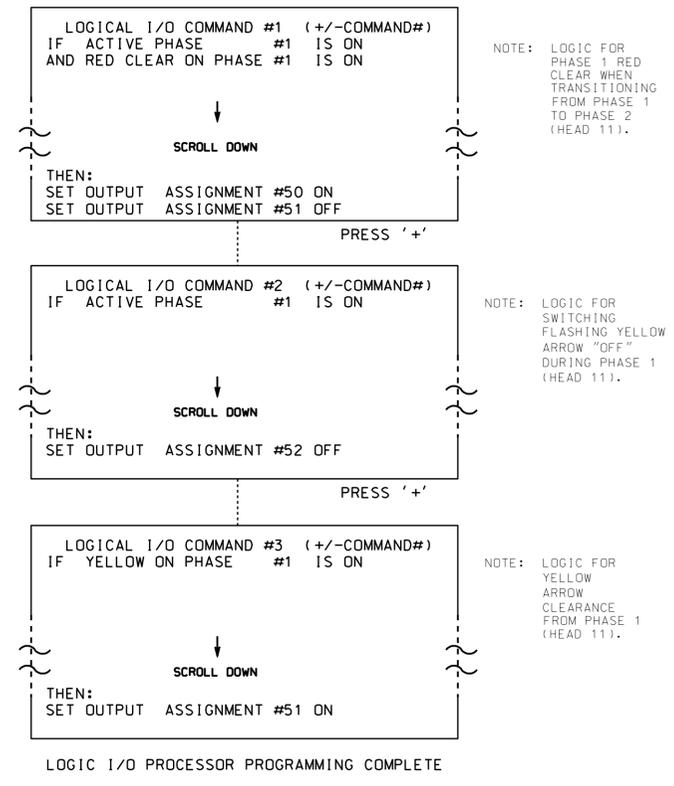## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
    LOGICAL I/O COMMAND #1  (+/-COMMAND#)
    IF  ACTIVE PHASE      #1  IS ON
    AND RED CLEAR ON PHASE #1  IS ON

                    ↓
              SCROLL DOWN

    THEN:
    SET OUTPUT   ASSIGNMENT #50 ON
    SET OUTPUT   ASSIGNMENT #51 OFF
                                PRESS '+'
```
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```
    LOGICAL I/O COMMAND #2  (+/-COMMAND#)
    IF  ACTIVE PHASE      #1  IS ON

                    ↓
              SCROLL DOWN

    THEN:
    SET OUTPUT   ASSIGNMENT #52 OFF
                                PRESS '+'
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```
    LOGICAL I/O COMMAND #3  (+/-COMMAND#)
    IF  YELLOW ON PHASE   #1  IS ON

                    ↓
              SCROLL DOWN

    THEN:
    SET OUTPUT   ASSIGNMENT #51 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).
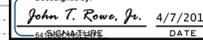
```
    PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
    PHASE:             |1234567891011121314 1516
    VEH OVL PARENTS:|XX
    VEH OVL NOT VEH:|
    VEH OVL NOT PED:|
    VEH OVL GRN EXT:|
    STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
    FLASH COLORS:  _ RED  _ YELLOW  X GREEN   ← NOTICE GREEN FLASH
    SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)........0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
PRESS '+' TWICE

```
    PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
    PHASE:             |1234567891011121314 1516
    VEH OVL PARENTS:|     X
    VEH OVL NOT VEH:|
    VEH OVL NOT PED:|
    VEH OVL GRN EXT:|
    STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
    FLASH COLORS:  _ RED  _ YELLOW  X GREEN   ← NOTICE GREEN FLASH
    SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)........0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 08-0624T
DESIGNED: March 2014
SEALED: 4-01-15
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 2

07-APR-2015 13:25
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jtpeterson