PROJECT REFERENCE NO.	SHEET NO.
W-5107	Sig. 4.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #7  (+/-COMMAND#)
IF  ACTIVE PHASE       #3  IS ON
AND RED CLEAR ON PHASE #3  IS ON

                 SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #8  (+/-COMMAND#)
IF  ACTIVE PHASE       #3  IS ON

                 SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #9  (+/-COMMAND#)
IF  YELLOW ON PHASE    #3  IS ON

                 SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦ X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦  XX
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦     X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦        X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS),
THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PRESS '+'

NOTICE PAGE 2 →

```
PAGE 2: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          ¦123456789101112131415 16
VEH OVL PARENTS:¦  X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1374
DESIGNED: August 2014
SEALED: 10-27-14
REVISED: N/A

ELECTRICAL DETAIL SHEET 2 OF 4

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SR 2308 (Peedin Road Extension)
at
US 70 WB Ramps/
SR 2307 (Gor-an Farm Road)

Division 4 Johnston County DS Pine Level

PLAN DATE: September 2014	REVIEWED BY: JTR		
PREPARED BY: James Peterson	REVIEWED BY:		
REVISIONS		INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by: John T. Rowe, Jr. 10/29/2014
641D90C145EE4F5... DATE

SIG. INVENTORY NO. 04-1374

29-OCT-2014 07:52
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jtpeterson