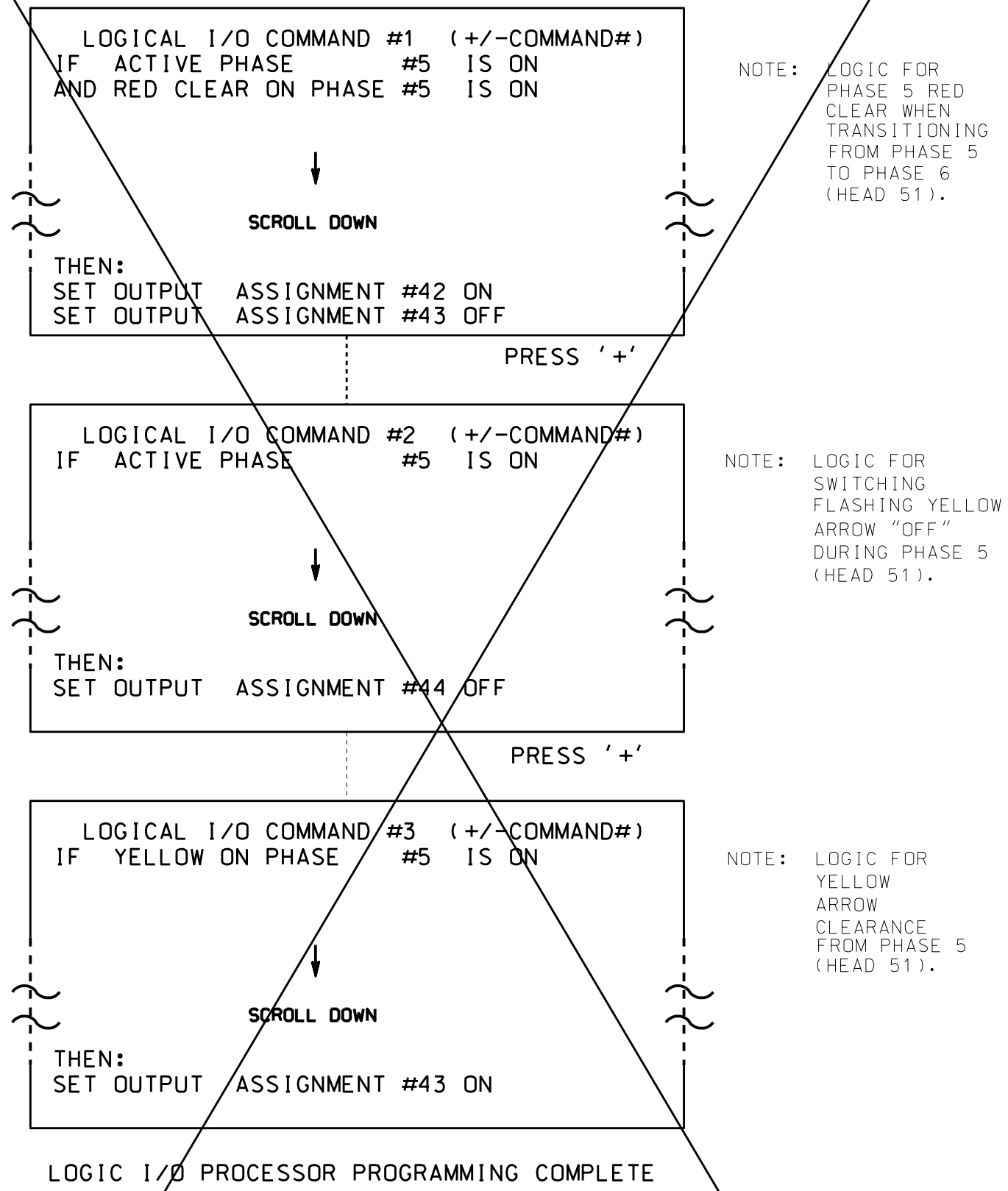


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

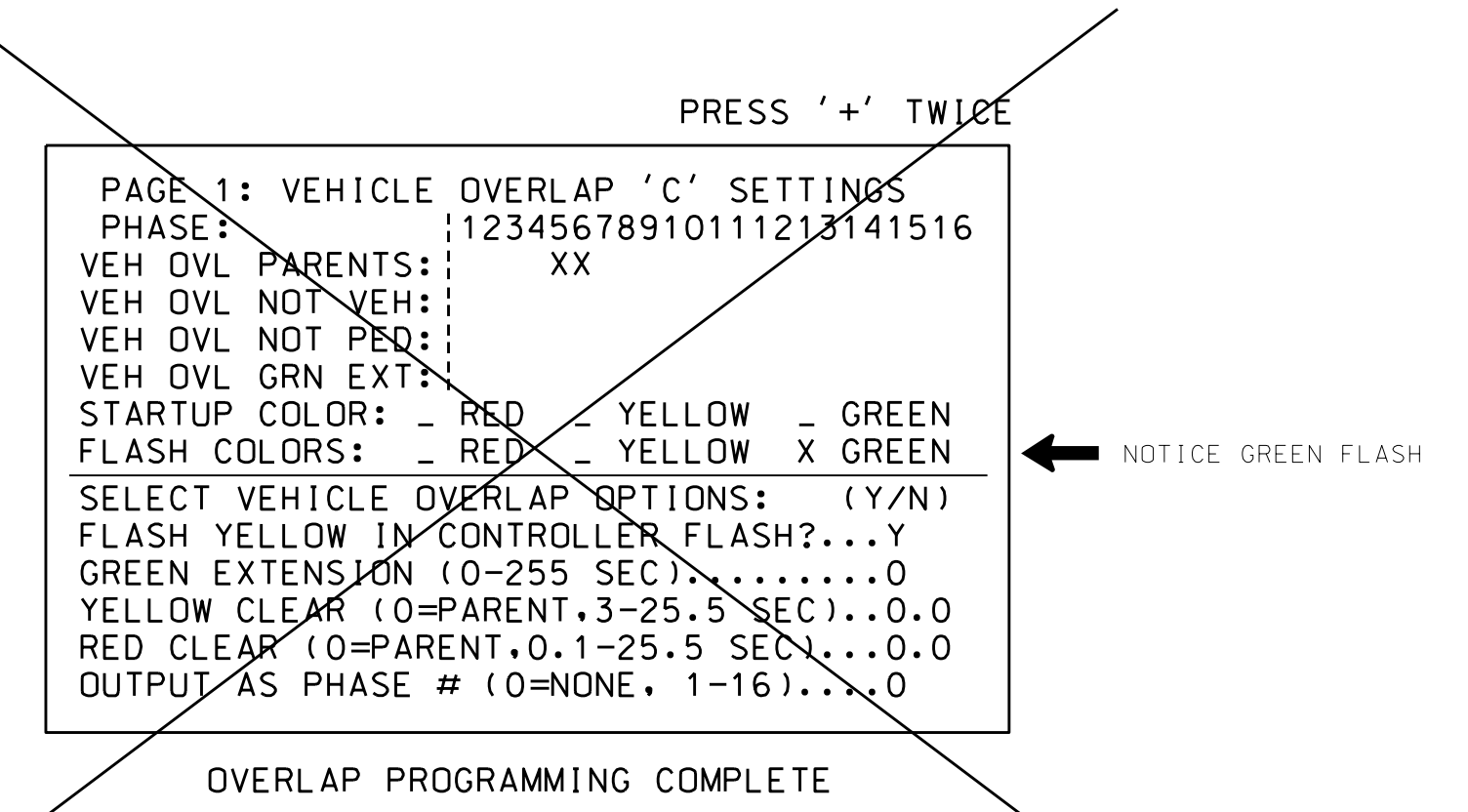


<b>OUTPUT REFERENCE SCHEDULE</b>	
OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 11-0341T4  
DESIGNED: September 2014  
SEALED: 10/14/2014  
REVISED: N/A

Electrical Detail - Temporary Design 4 - TCP Phase III (Step 5) - Sheet 2 of 2

	<b>US 221-NC 194</b> at <b>US 221 Business-NC 194/NC 163</b>		<b>SEAL</b> 
	Division 11 PLAN DATE: October 2014 PREPARED BY: S. Armstrong	Ashe County REVIEWED BY: JTR REVIEWED BY:	near West Jefferson DATE:
REVISIONS		INIT.	DATE
SIG. INVENTORY NO. 11-0341T4			

15-0075-2014\_15-11  
S:\TDS\15-0075\Sig\15-0075-2014\_15-11.dgn  
S:\TDS\15-0075\Sig\15-0075-2014\_15-11.dgn  
S:\TDS\15-0075\Sig\15-0075-2014\_15-11.dgn